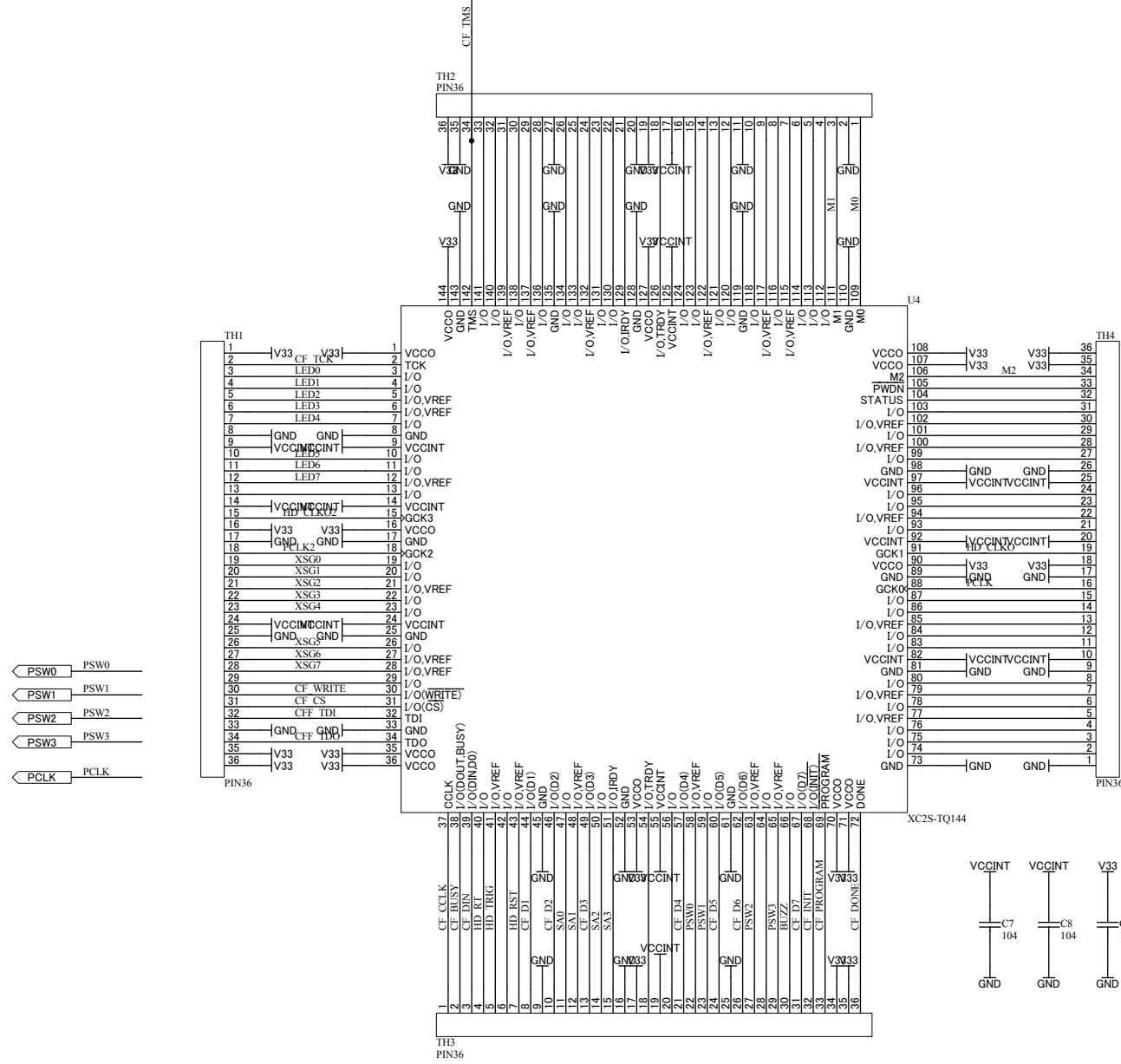


- a : SG0
- b : SG1
- c : SG2
- d : SG3
- e : SG4
- f : SG5
- g : SG6
- dp : SG7



Designed By FPGA-INFORMATION

		Spartan2 評価基板 (Rev2)		DOC. No.	XSP-005	C