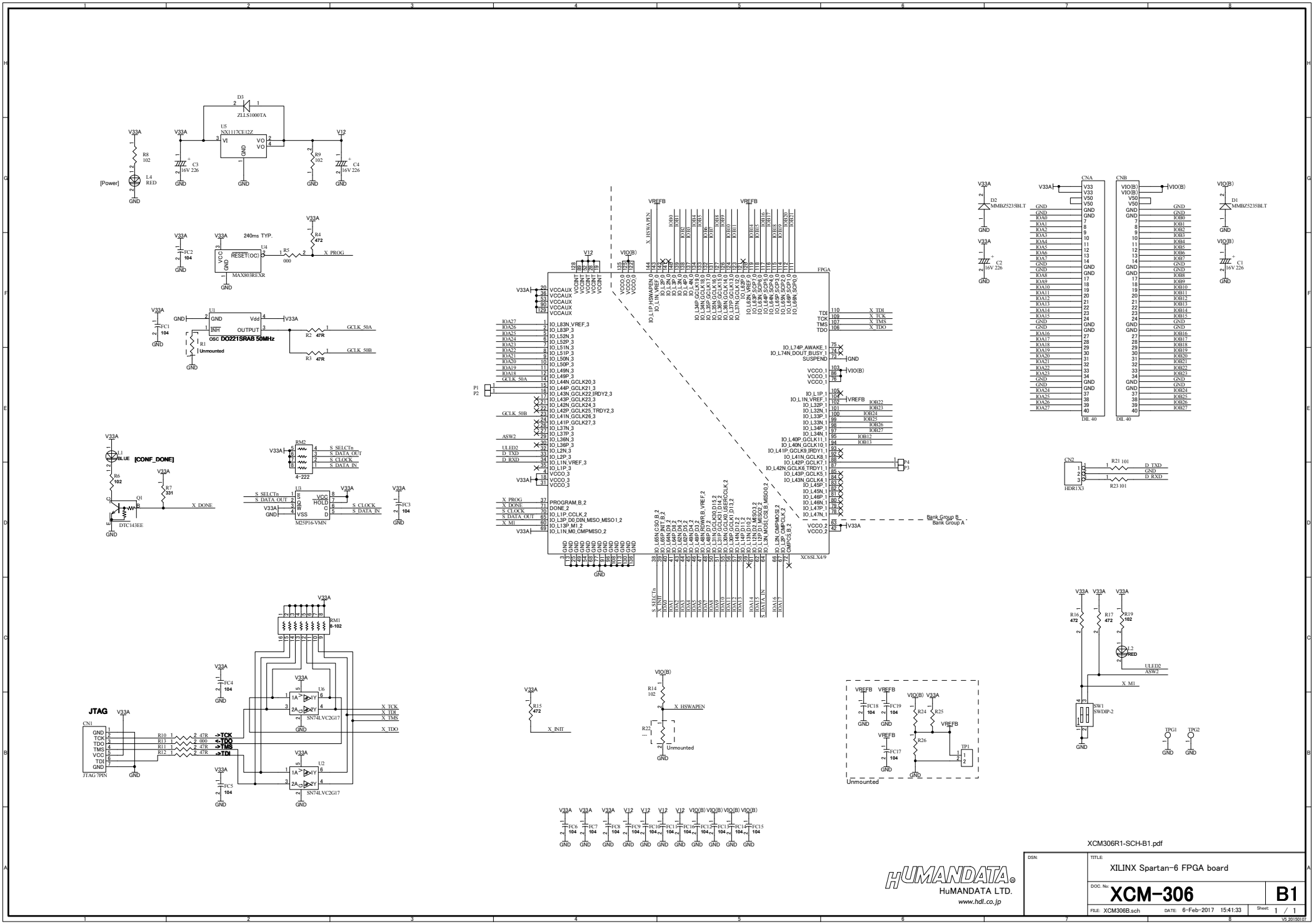
ZLLS1000TA
NX1117CE12Z
[Power]
240ms TYP.
RESET(OC)
MAX803REXR
X_PROG
OSC DO221SRAB 50MHz
GCLK_50A
GCLK_50B
[CONF_DONE]
X_DONE
M25P16-VMN
S_SELCTn
S_DATA_OUT
S_CLOCK
S_DATA_IN
JTAG
JTAG 7PIN
->TCK
->TDO
->TMS
->TDI
SN74LVC2G17
X_TCK
X_TDI
X_TMS
X_TDO
X_HSWAPEN
X_INIT
Unmounted
FPGA
XC6SLX4/9
Bank_Group B
Bank Group A
DIL 40
HDR1X3
D_TXD
D_RXD
ULED2
ASW2
X_M1
SWDIP-2
XCM306R1-SCH-B1.pdf
HuMANDATA LTD.
www.hdl.co.jp
XILINX Spartan-6 FPGA board
XCM-306
B1
XCM306B.sch
6-Feb-2017 15:41:33
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