

PCB Version : C



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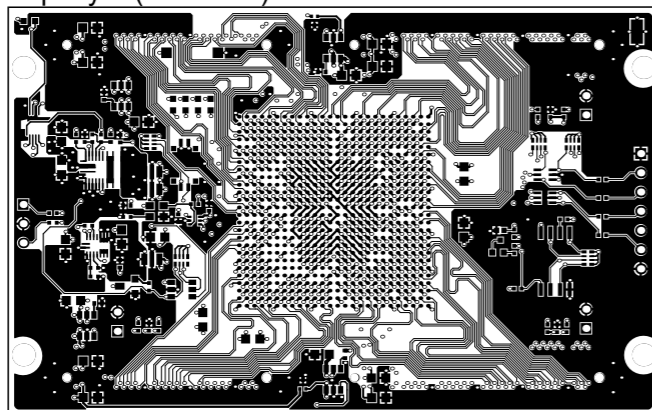
|     |                              |                               |
|-----|------------------------------|-------------------------------|
| DSN | TITLE:<br>XCM210 PCB Drawing |                               |
|     | DOC.No:<br>PCB-XCM210        | REV<br>B                      |
|     | FILE:                        | DATE: 2023/08/23 Sheet: 1 / 2 |

1 2 3 4 5 6 7 8

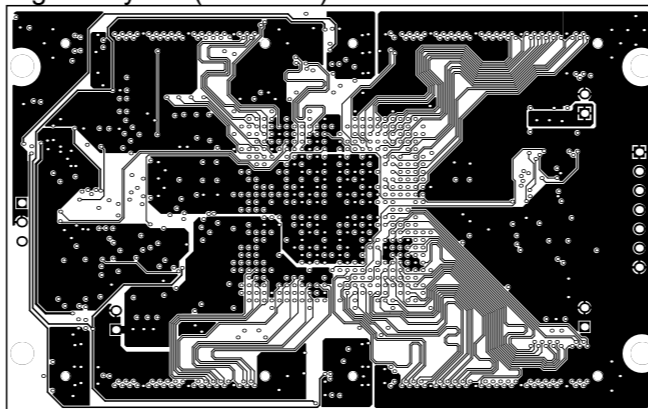
A

A

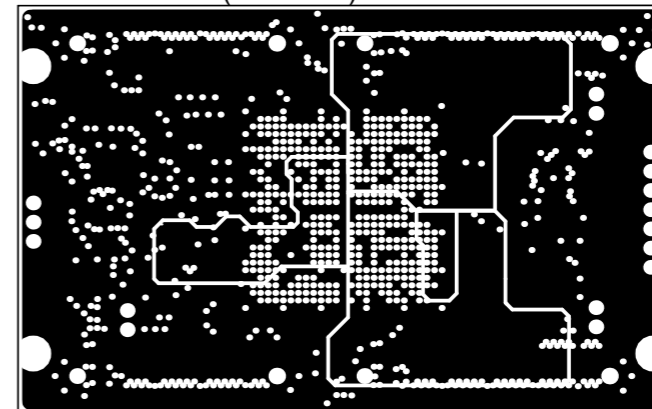
TopLayer (Scale 1:1)



Signal Layer 1 (Scale 1:1)



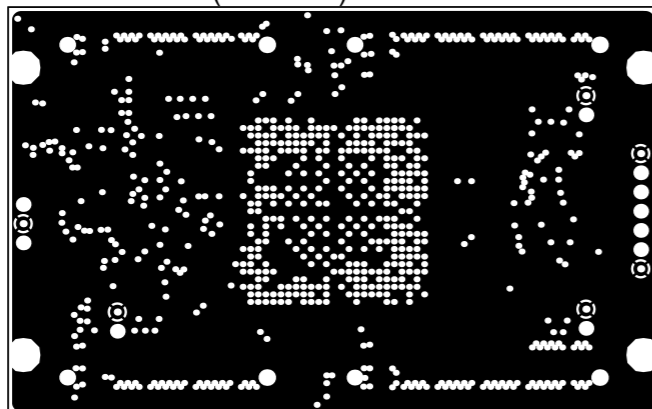
Internal Plane 2 (Scale 1:1)



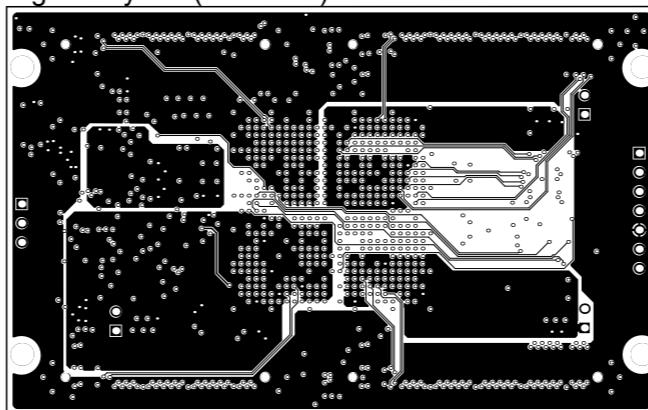
B

B

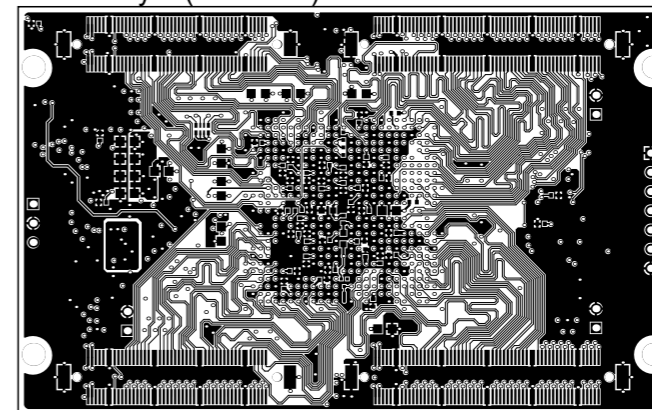
Internal Plane 1 (Scale 1:1)



Signal Layer 2 (Scale 1:1)



BottomLayer (Scale 1:1)



C

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D



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DSN

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FILE: DATE: 2023/08/23 Sheet: 2 / 2

1 2 3 4 5 6 7 8

V3\_20230404