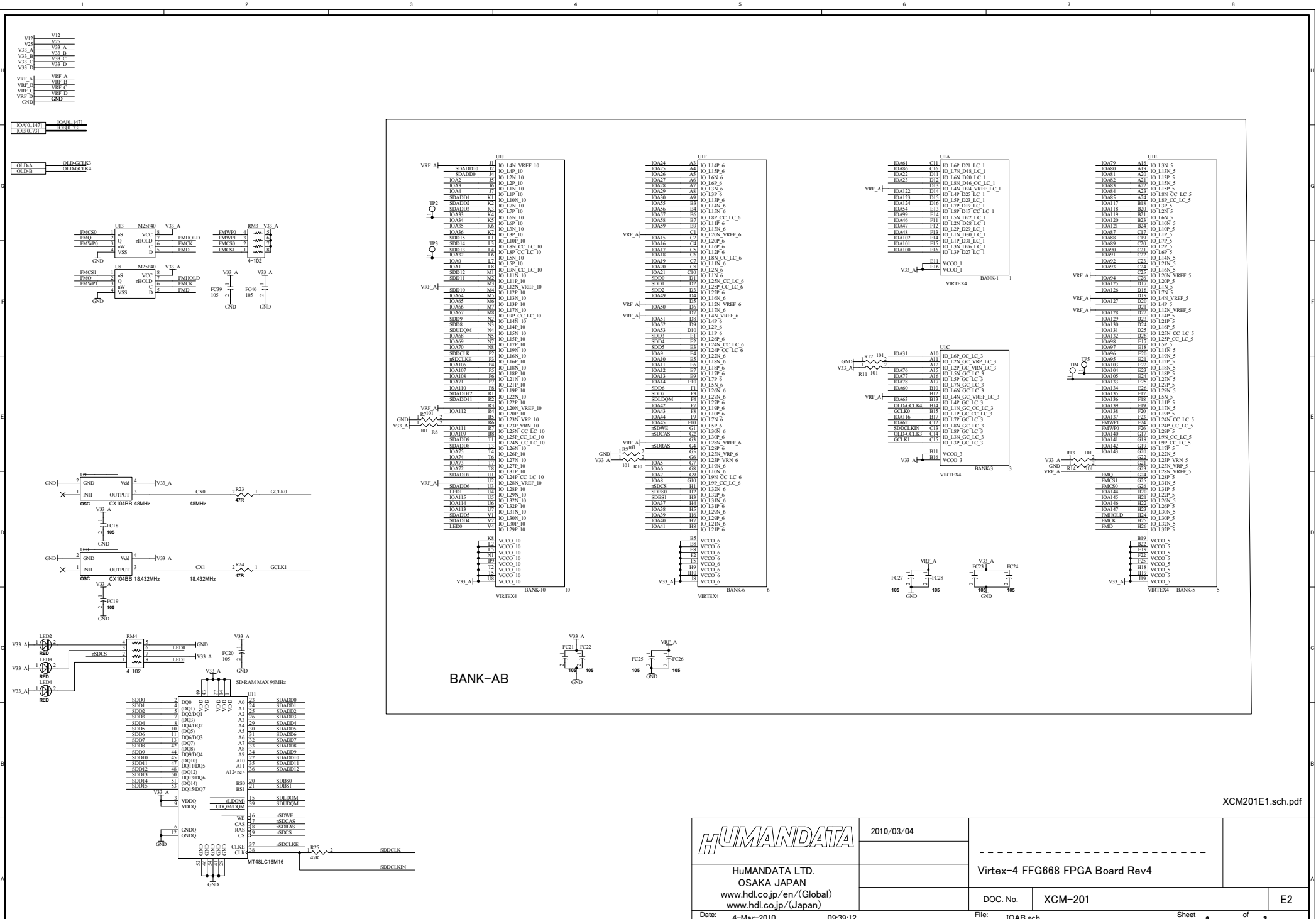


XCM201E1.sch.pdf

		2010/03/04	
		Virtex-4 FFG668 FPGA Board Rev4	
HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/ (Global) www.hdl.co.jp/ (Japan)		DOC. No. XCM-201	E2
Date: 4-Mar-2010		File: XCM201E.sch	Sheet 1 of 3

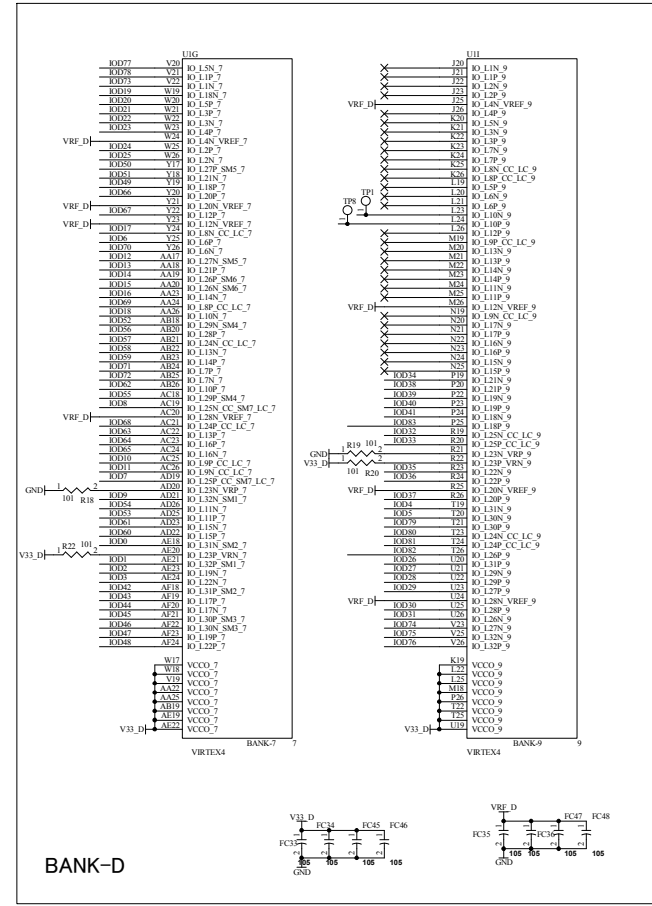
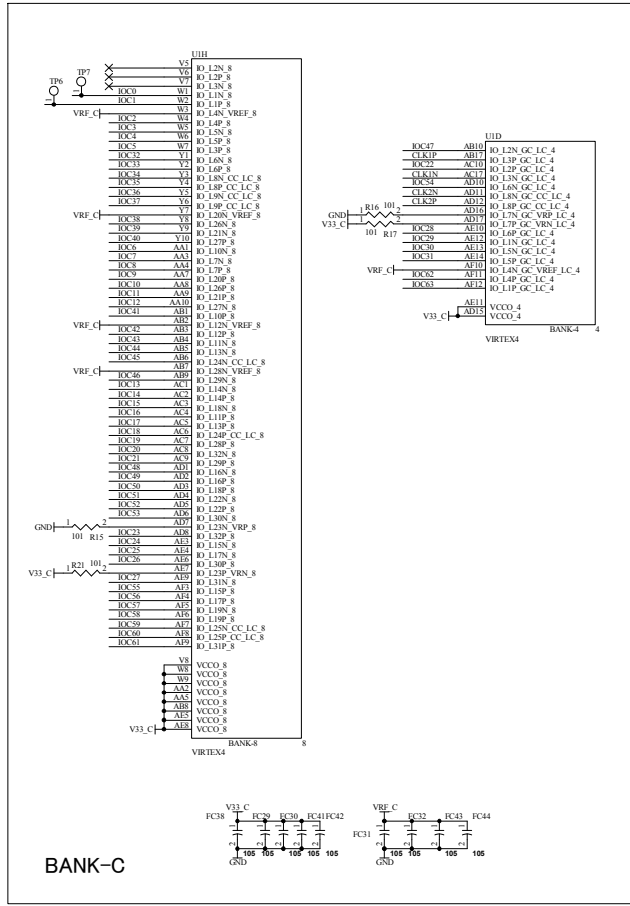


XCM201E1.sch.pdf

	2010/03/04		
	HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/ (Global) www.hdl.co.jp/ (Japan)		
Date: 4-Mar-2010 09:39:12		File: IOAB.sch	
HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/ (Global) www.hdl.co.jp/ (Japan)		Virtex-4 FFG668 FPGA Board Rev4	
DOC. No.	XCM-201	E2	
Date: 4-Mar-2010	09:39:12	File: IOAB.sch	Sheet 2 of 3

V12	V12
V21	V25
V31 A	V31 A
V31 B	V31 B
V31 C	V31 D
V31 D	V31 D
VRF_A	VRF_A
VRF_B	VRF_B
VRF_C	VRF_C
VRF_D	VRF_D
GND	GND

RC00_0A1	RC00_0A1
RC00_0B1	RC00_0B1
CLKSP	CLKSP
CLKIN	CLKIN
CLKTP	CLKTP
CLKIN	CLKIN



	2010/03/04	Virtex-4 FFG668 FPGA Board Rev4	DOC. No. XCM-201	E2
	HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/ (Global) www.hdl.co.jp/ (Japan)			
Date: 4-Mar-2010	09:39:13	File: IOCD.sch	Sheet 3	of 3