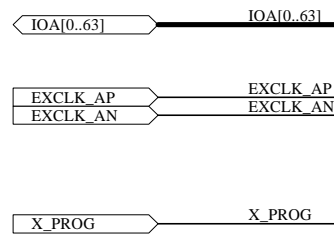


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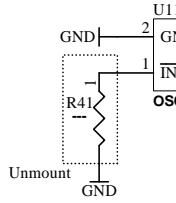
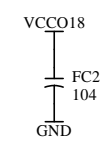
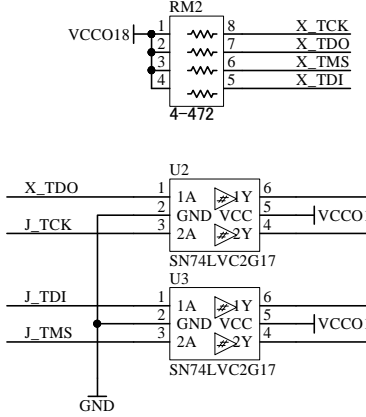
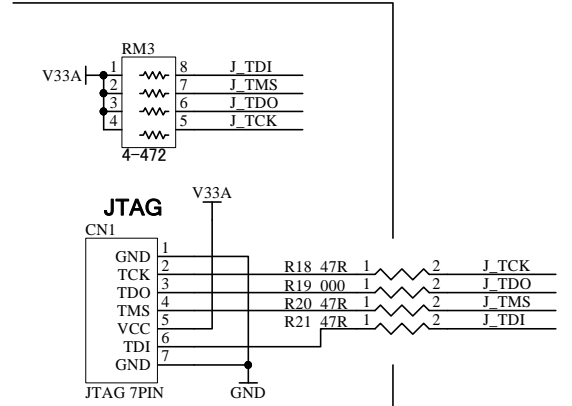
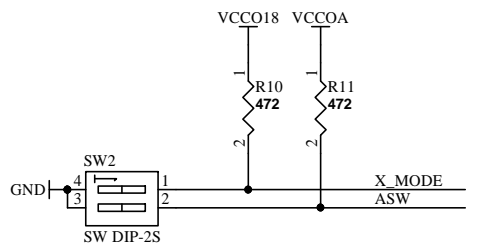
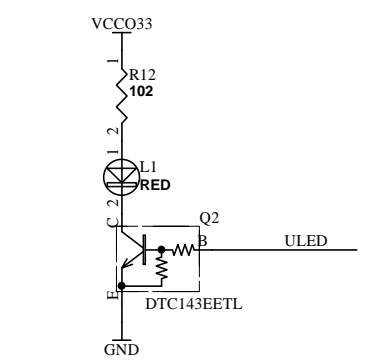
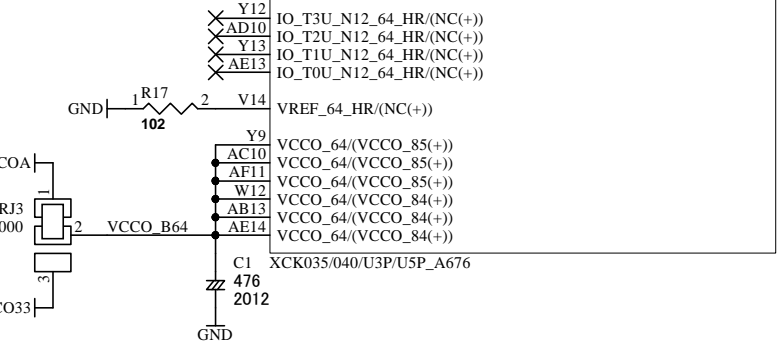


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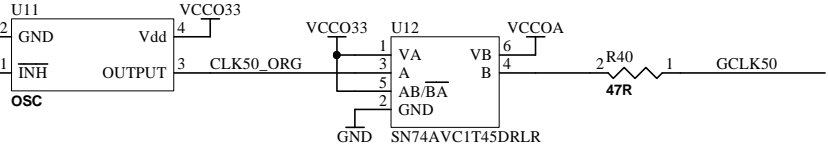
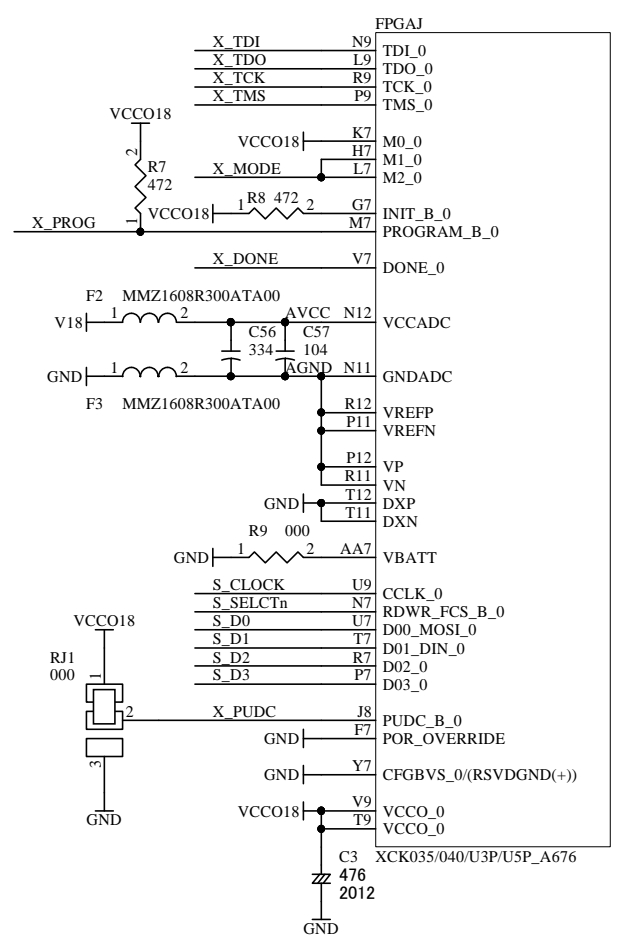
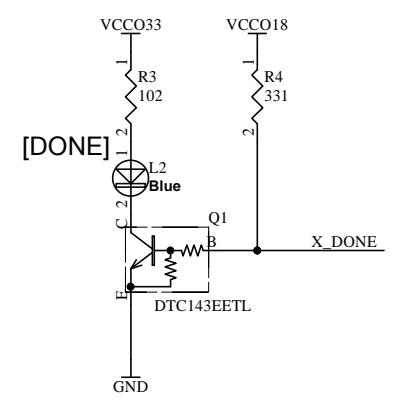
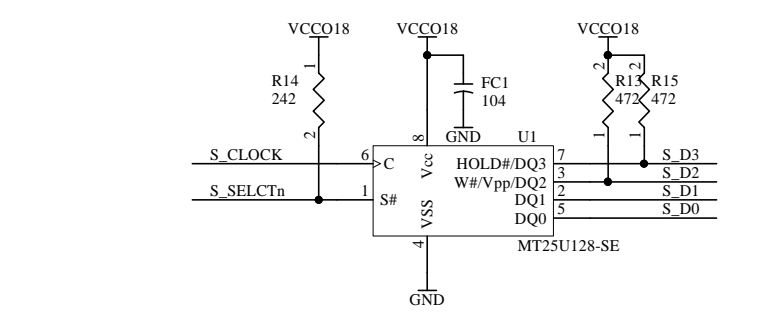
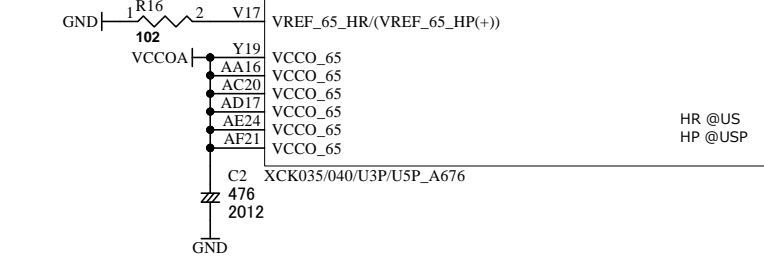
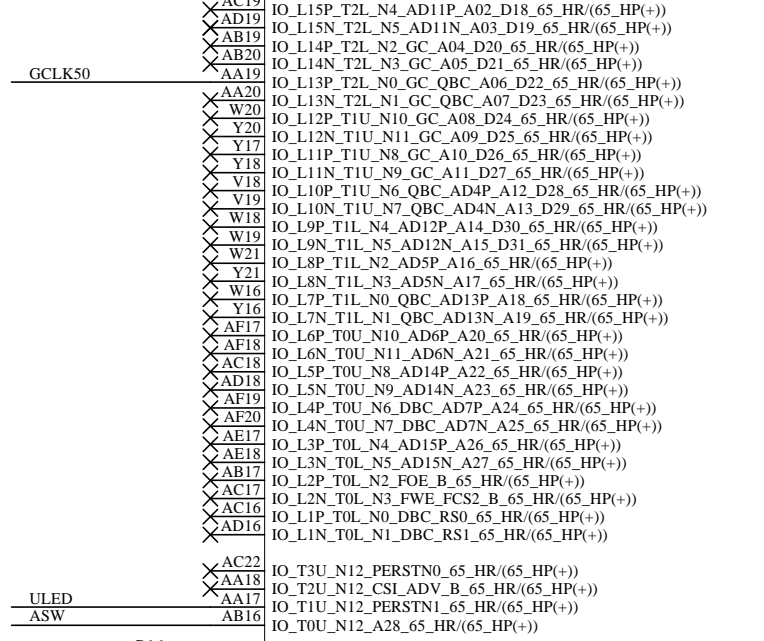


FPGAH	
IOA37	W11
IOA36	W10
IOA35	W9
IOA34	W8
IOA1	V12
IOA0	V11
IOA32	Y8
IOA33	AA8
IOA3	Y11
IOA2	Y10
IOA5	AA10
IOA4	AA9
IOA39	AB10
IOA38	AB9
IOA30	AC8
IOA31	AD8
IOA26	AE10
IOA27	AF10
IOA28	AC9
IOA29	AD9
IOA24	AD11
IOA25	AE11
EXCLK_AP	AC12
EXCLK_AN	AC11

IOA44	AB12
IOA45	AB11
IOA40	AA13
IOA41	AA12
IOA43	W14
IOA42	W13
IOA18	AA14
IOA19	AB14
IOA10	W15
IOA11	Y15
IOA12	AA15
IOA13	AB15
IOA21	AD14
IOA20	AD13
IOA6	AC14
IOA7	AC13
IOA23	AE12
IOA22	AF12
IOA8	AD15
IOA9	AE15
IOA16	AF14
IOA17	AF13
IOA14	AE16
IOA15	AF15



FPGA1	
IOA56	AD24
IOA57	AD25
IOA54	AD23
IOA55	AE23
IOA63	AD26
IOA62	AE26
IOA58	AF23
IOA59	AF24
IOA61	AE25
IOA60	AF25
IOA52	AE22
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IOA46	AD20
IOA47	AD21
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IOA49	AC21
IOA50	AE20
IOA51	AE21

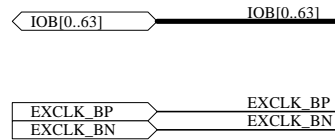


BANK Group A

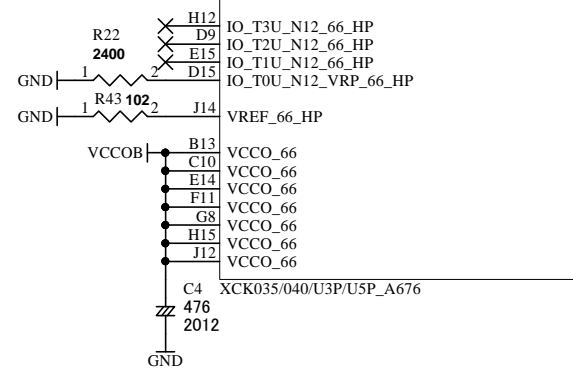
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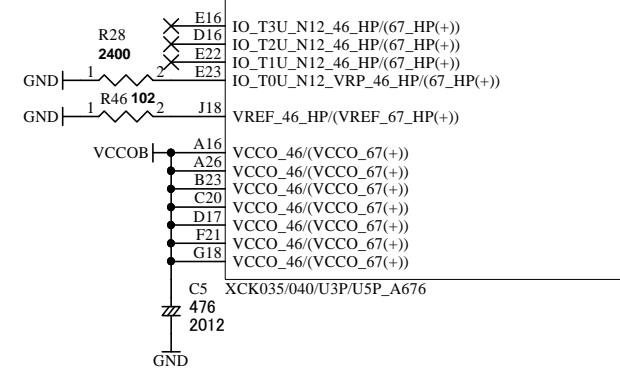
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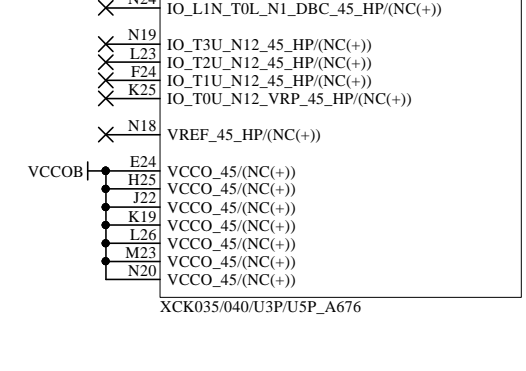
FPGA	
IOB54	G10
IOB55	G9
IOB26	J11
IOB27	H11
IOB52	G11
IOB53	F10
IOB24	J10
IOB25	J9
IOB63	F9
IOB62	F8
IOB11	H9
IOB10	H8
IOB45	B10
IOB44	A10
IOB59	E8
IOB58	D8
IOB47	C11
IOB46	B11
IOB56	C9
IOB57	C8
IOB61	E11
IOB60	D11
EXCLK_BP	E10
EXCLK_BN	D10
IOB16	E13
IOB17	E12
IOB22	G12
IOB23	F12
IOB19	F14
IOB18	F13
IOB31	J13
IOB30	H13
IOB15	G15
IOB14	F15
IOB29	H14
IOB28	G14
IOB48	A13
IOB49	A12
IOB12	C12
IOB13	B12
IOB2	B14
IOB3	A14
IOB42	D13
IOB43	C13
IOB0	B15
IOB1	A15
IOB51	D14
IOB50	C14



FPGA	
IOB6	H17
IOB7	H18
IOB9	E17
IOB8	E18
IOB21	H16
IOB20	G16
IOB39	F18
IOB38	F19
IOB41	G17
IOB40	F17
IOB35	G19
IOB34	F19
IOB37	F17
IOB36	G17
IOB5	D18
IOB4	C18
IOB33	D19
IOB32	C19
IOB33	D20
IOB32	C20
IOB33	D21
IOB32	C21
IOB33	D22
IOB32	C22
IOB33	D23
IOB32	C23
IOB33	D24
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IOB32	C96
IOB33	D97
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IOB33	D99
IOB32	C99
IOB33	D100
IOB32	C100



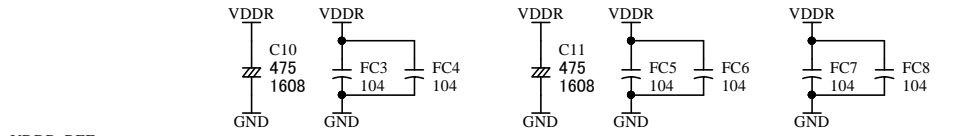
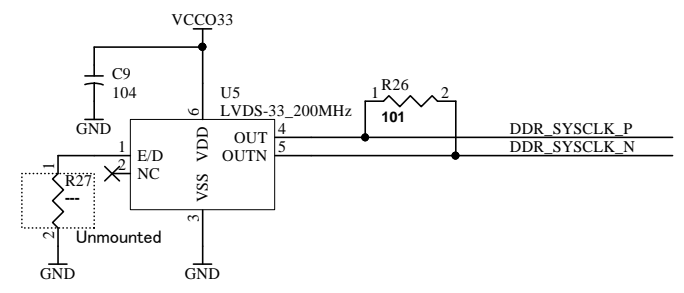
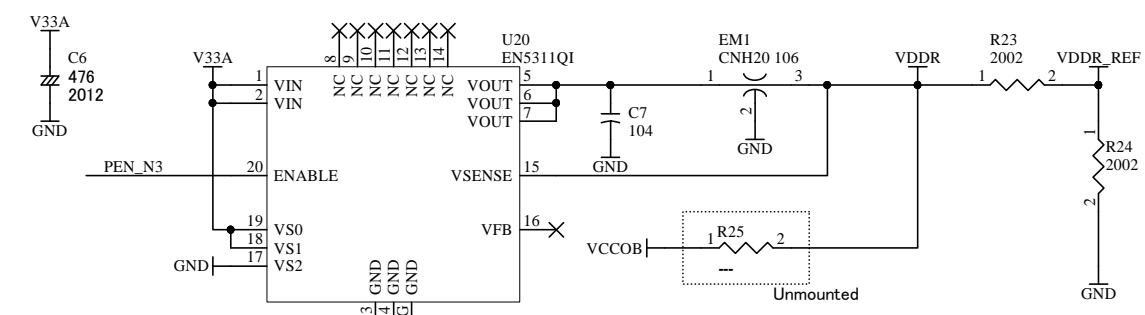
FPGA	
IOB21	N21
IOB20	M21
IOB39	J19
IOB38	J20
IOB41	M20
IOB40	L20
IOB35	M19
IOB34	L19
IOB37	K20
IOB36	K21
IOB5	L18
IOB4	K18
IOB33	J21
IOB32	H21
IOB33	G21
IOB32	G22
IOB33	L22
IOB32	K22
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IOB33	K23
IOB32	J23
IOB33	H22
IOB32	H23
IOB33	H24
IOB32	G24
IOB33	J24
IOB32	J25
IOB33	F22
IOB32	F23
IOB33	G25
IOB32	F25
IOB33	H26
IOB32	G26
IOB33	E25
IOB32	E26
IOB33	M25
IOB32	L25
IOB33	N22
IOB32	M22
IOB33	K26
IOB32	J26
IOB33	M24
IOB32	L24
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IOB32	M26
IOB33	N23
IOB32	N24
IOB33	N19
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IOB33	F24
IOB32	K25
IOB33	N18
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IOB33	H25
IOB32	J22
IOB33	K19
IOB32	L26
IOB33	M23
IOB32	N20



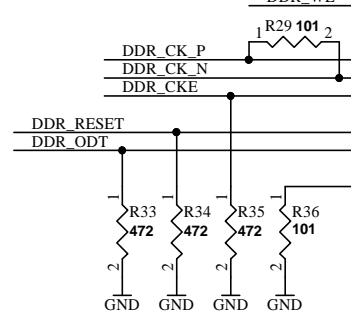
BANK Group B

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PEN_N3



DDR_A0	N3	A0
DDR_A1	P7	A1
DDR_A2	P3	A2
DDR_A3	N2	A3
DDR_A4	P8	A4
DDR_A5	P2	A5
DDR_A6	R8	A6
DDR_A7	A6	A7
DDR_A8	T8	A8
DDR_A9	R3	A9
DDR_A10	L7	A10/AP
DDR_A11	R7	A11
DDR_A12	N7	A12/BC#
DDR_A13	T3	A13
DDR_A14	T7	A14
DDR_BA0	M2	BA0
DDR_BA1	N8	BA1
DDR_BA2	M3	BA2
DDR_RAS	J3	RAS#
DDR_CAS	K3	CAS#
DDR_WE	L3	WE#
DDR_CK_P	J7	CK
DDR_CK_N	K7	CK#
DDR_CKE	K9	CKE
DDR_RESET	T2	RESET#
DDR_ODT	K1	ODT
CS#	L2	CS#



VDD	R9	VDD	R10	VDD	R11	VDD	R12	VDD	R13	VDD	R14	VDD	R15	VDD	R16	VDD	R17	VDD	R18	VDD	R19	VDD	R20	VDD	R21	VDD	R22	VDD	R23	VDD	R24	VDD	R25	VDD	R26	VDD	R27	VDD	R28	VDD	R29	VDD	R30	VDD	R31	VDD	R32	VDD	R33	VDD	R34	VDD	R35	VDD	R36	VDD	R37	VDD	R38	VDD	R39	VDD	R40	VDD	R41	VDD	R42	VDD	R43	VDD	R44	VDD	R45	VDD	R46	VDD	R47	VDD	R48	VDD	R49	VDD	R50	VDD	R51	VDD	R52	VDD	R53	VDD	R54	VDD	R55	VDD	R56	VDD	R57	VDD	R58	VDD	R59	VDD	R60	VDD	R61	VDD	R62	VDD	R63	VDD	R64	VDD	R65	VDD	R66	VDD	R67	VDD	R68	VDD	R69	VDD	R70	VDD	R71	VDD	R72	VDD	R73	VDD	R74	VDD	R75	VDD	R76	VDD	R77	VDD	R78	VDD	R79	VDD	R80	VDD	R81	VDD	R82	VDD	R83	VDD	R84	VDD	R85	VDD	R86	VDD	R87	VDD	R88	VDD	R89	VDD	R90	VDD	R91	VDD	R92	VDD	R93	VDD	R94	VDD	R95	VDD	R96	VDD	R97	VDD	R98	VDD	R99	VDD	R100	VDD	R101	VDD	R102	VDD	R103	VDD	R104	VDD	R105	VDD	R106	VDD	R107	VDD	R108	VDD	R109	VDD	R110	VDD	R111	VDD	R112	VDD	R113	VDD	R114	VDD	R115	VDD	R116	VDD	R117	VDD	R118	VDD	R119	VDD	R120	VDD	R121	VDD	R122	VDD	R123	VDD	R124	VDD	R125	VDD	R126	VDD	R127	VDD	R128	VDD	R129	VDD	R130	VDD	R131	VDD	R132	VDD	R133	VDD	R134	VDD	R135	VDD	R136	VDD	R137	VDD	R138	VDD	R139	VDD	R140	VDD	R141	VDD	R142	VDD	R143	VDD	R144	VDD	R145	VDD	R146	VDD	R147	VDD	R148	VDD	R149	VDD	R150	VDD	R151	VDD	R152	VDD	R153	VDD	R154	VDD	R155	VDD	R156	VDD	R157	VDD	R158	VDD	R159	VDD	R160	VDD	R161	VDD	R162	VDD	R163	VDD	R164	VDD	R165	VDD	R166	VDD	R167	VDD	R168	VDD	R169	VDD	R170	VDD	R171	VDD	R172	VDD	R173	VDD	R174	VDD	R175	VDD	R176	VDD	R177	VDD	R178	VDD	R179	VDD	R180	VDD	R181	VDD	R182	VDD	R183	VDD	R184	VDD	R185	VDD	R186	VDD	R187	VDD	R188	VDD	R189	VDD	R190	VDD	R191	VDD	R192	VDD	R193	VDD	R194	VDD	R195	VDD	R196	VDD	R197	VDD	R198	VDD	R199	VDD	R200	VDD	R201	VDD	R202	VDD	R203	VDD	R204	VDD	R205	VDD	R206	VDD	R207	VDD	R208	VDD	R209	VDD	R210	VDD	R211	VDD	R212	VDD	R213	VDD	R214	VDD	R215	VDD	R216	VDD	R217	VDD	R218	VDD	R219	VDD	R220	VDD	R221	VDD	R222	VDD	R223	VDD	R224	VDD	R225	VDD	R226	VDD	R227	VDD	R228	VDD	R229	VDD	R230	VDD	R231	VDD	R232	VDD	R233	VDD	R234	VDD	R235	VDD	R236	VDD	R237	VDD	R238	VDD	R239	VDD	R240	VDD	R241	VDD	R242	VDD	R243	VDD	R244	VDD	R245	VDD	R246	VDD	R247	VDD	R248	VDD	R249	VDD	R250	VDD	R251	VDD	R252	VDD	R253	VDD	R254	VDD	R255	VDD	R256	VDD	R257	VDD	R258	VDD	R259	VDD	R260	VDD	R261	VDD	R262	VDD	R263	VDD	R264	VDD	R265	VDD	R266	VDD	R267	VDD	R268	VDD	R269	VDD	R270	VDD	R271	VDD	R272	VDD	R273	VDD	R274	VDD	R275	VDD	R276	VDD	R277	VDD	R278	VDD	R279	VDD	R280	VDD	R281	VDD	R282	VDD	R283	VDD	R284	VDD	R285	VDD	R286	VDD	R287	VDD	R288	VDD	R289	VDD	R290	VDD	R291	VDD	R292	VDD	R293	VDD	R294	VDD	R295	VDD	R296	VDD	R297	VDD	R298	VDD	R299	VDD	R300	VDD	R301	VDD	R302	VDD	R303	VDD	R304	VDD	R305	VDD	R306	VDD	R307	VDD	R308	VDD	R309	VDD	R310	VDD	R311	VDD	R312	VDD	R313	VDD	R314	VDD	R315	VDD	R316	VDD	R317	VDD	R318	VDD	R319	VDD	R320	VDD	R321	VDD	R322	VDD	R323	VDD	R324	VDD	R325	VDD	R326	VDD	R327	VDD	R328	VDD	R329	VDD	R330	VDD	R331	VDD	R332	VDD	R333	VDD	R334	VDD	R335	VDD	R336	VDD	R337	VDD	R338	VDD	R339	VDD	R340	VDD	R341	VDD	R342	VDD	R343	VDD	R344	VDD	R345	VDD	R346	VDD	R347	VDD	R348	VDD	R349	VDD	R350	VDD	R351	VDD	R352	VDD	R353	VDD	R354	VDD	R355	VDD	R356	VDD	R357	VDD	R358	VDD	R359	VDD	R360	VDD	R361	VDD	R362	VDD	R363	VDD	R364	VDD	R365	VDD	R366	VDD	R367	VDD	R368	VDD	R369	VDD	R370	VDD	R371	VDD	R372	VDD	R373	VDD	R374	VDD	R375	VDD	R376	VDD	R377	VDD	R378	VDD	R379	VDD	R380	VDD	R381	VDD	R382	VDD	R383	VDD	R384	VDD	R385	VDD	R386	VDD	R387	VDD	R388	VDD	R389	VDD	R390	VDD	R391	VDD	R392	VDD	R393	VDD	R394	VDD	R395	VDD	R396	VDD	R397	VDD	R398	VDD	R399	VDD	R400	VDD	R401	VDD	R402	VDD	R403	VDD	R404	VDD	R405	VDD	R406	VDD	R407	VDD	R408	VDD	R409	VDD	R410	VDD	R411	VDD	R412	VDD	R413	VDD	R414	VDD	R415	VDD	R416	VDD	R417	VDD	R418	VDD	R419	VDD	R420	VDD	R421	VDD	R422	VDD	R423	VDD	R424	VDD	R425	VDD	R426	VDD	R427	VDD	R428	VDD	R429	VDD	R430	VDD	R431	VDD	R432	VDD	R433	VDD	R434	VDD	R435	VDD	R436	VDD	R437	VDD	R438	VDD	R439	VDD	R440	VDD	R441	VDD	R442	VDD	R443	VDD	R444	VDD	R445	VDD	R446	VDD	R447	VDD	R448	VDD	R449	VDD	R450	VDD	R451	VDD	R452	VDD	R453	VDD	R454	VDD	R455	VDD	R456	VDD	R457	VDD	R458	VDD	R459	VDD	R460	VDD	R461	VDD	R462	VDD	R463	VDD	R464	VDD	R465	VDD	R466	VDD	R467	VDD	R468	VDD	R469	VDD	R470	VDD	R471	VDD	R472	VDD	R473	VDD	R474	VDD	R475	VDD	R476	VDD	R477	VDD	R478	VDD	R479	VDD	R480	VDD	R481	VDD	R482	VDD	R483	VDD	R484	VDD	R485	VDD	R486	VDD	R487	VDD	R488	VDD	R489	VDD	R490	VDD	R491	VDD	R492	VDD	R493	VDD	R494	VDD	R495	VDD	R496	VDD	R497	VDD	R498	VDD	R499	VDD	R500	VDD	R501	VDD	R502	VDD	R503	VDD	R504	VDD	R505	VDD	R506	VDD	R507	VDD	R508	VDD	R509	VDD	R510	VDD	R511	VDD	R512	VDD	R513	VDD	R514	VDD	R515	VDD	R516	VDD	R517	VDD	R518	VDD	R519	VDD	R520	VDD	R521	VDD	R522	VDD	R523	VDD	R524	VDD	R525	VDD	R526	VDD	R527	VDD	R528	VDD	R529	VDD	R530	VDD	R531	VDD	R532	VDD	R533	VDD	R534	VDD	R535	VDD	R536	VDD	R537	VDD	R538	VDD	R539	VDD	R540	VDD	R541	VDD	R542	VDD	R543	VDD	R544	VDD	R545	VDD	R546	VDD	R547	VDD	R548	VDD	R549	VDD	R550	VDD	R551	VDD	R552	VDD	R553	VDD	R554	VDD	R555	VDD	R556	VDD	R557	VDD	R558	VDD	R559	VDD	R560	VDD	R561	VDD	R562	VDD	R563	VDD	R564	VDD	R565	VDD	R566	VDD	R567	VDD	R568	VDD	R569	VDD	R570	VDD	R571	VDD	R572	VDD	R573	VDD	R574	VDD	R575	VDD	R576	VDD	R577	VDD	R578	VDD	R579	VDD	R580	VDD	R581	VDD	R582	VDD	R583	VDD	R584	VDD	R585	VDD	R586	VDD	R587	VDD	R588	VDD	R589	VDD	R590	VDD	R591	VDD	R592	VDD	R593	VDD	R594	VDD	R595	VDD	R596	VDD	R597	VDD	R598	VDD	R599	VDD	R600	VDD	R601	VDD	R602	VDD	R603	VDD	R604	VDD	R605	VDD	R606	VDD	R607	VDD	R608	VDD	R609	VDD	R610	VDD	R611	VDD	R612	VDD	R613	VDD	R614	VDD	R615	VDD	R616	VDD	R617	VDD	R618	VDD	R619	VDD	R620	VDD	R621	VDD	R622	VDD	R623	VDD	R624	VDD	R625	VDD	R626	VDD	R627	VDD	R628	VDD	R629	VDD	R630	VDD	R631	VDD	R632	VDD	R633	VDD	R634	VDD	R635	VDD	R636	VDD	R637	VDD	R638	VDD	R639	VDD	R640	VDD	R641	VDD	R642	VDD	R643	VDD	R644	VDD	R645	VDD	R646	VDD	R647	VDD	R648	VDD	R649	VDD	R650	VDD	R651	VDD	R652	VDD	R653	VDD	R654	VDD	R655	VDD	R656	VDD	R657	VDD	R658	VDD	R659	VDD	R660	VDD	R661	VDD	R662	VDD	R663	VDD	R664	VDD	R665	VDD	R666	VDD	R667	VDD	R668	VDD	R669	VDD	R670	VDD	R671	VDD	R672	VDD	R673	VDD	R674	VDD	R675	VDD	R676	VDD	R677	VDD	R678	VDD	R679	VDD	R680	VDD	R681	VDD	R682	VDD	R683	VDD	R684	VDD	R685	VDD	R686	VDD	R687	VDD	R688	VDD	R689	VDD	R690	VDD	R691	VDD	R692	VDD	R693	VDD	R694	VDD	R695	VDD	R696	VDD	R697	VDD	R698	VDD	R699	VDD	R700	VDD	R701	VDD	R702	VDD	R703	VDD	R704	VDD	R705	VDD	R706	VDD	R707	VDD	R708	VDD	R709	VDD	R710	VDD	R711	VDD	R712	VDD	R713	VDD	R714	VDD	R715	VDD	R716	VDD	R717	VDD	R718	VDD	R719	VDD	R720	VDD	R721	VDD	R722	VDD	R723	VDD	R724	VDD	R725	VDD	R726	VDD	R727	VDD	R728	VDD	R729	VDD	R730	VDD	R731	VDD	R732	VDD	R733	VDD	R734	VDD	R735	VDD	R736	VDD	R737	VDD	R738	VDD	R739	VDD	R740	VDD	R741	VDD	R742	VDD	R743	VDD	R744	VDD	R745	VDD	R746	VDD	R747	VDD	R748	VDD	R749	VDD	R750	VDD	R751	VDD	R752	VDD	R753	VDD	R754	VDD	R755	VDD	R756	VDD	R757	VDD	R758	VDD	R759	VDD	R760	VDD	R761	VDD	R762	VDD	R763	VDD	R764	VDD	R765	VDD	R766	VDD	R767	VDD	R768	VDD	R769	VDD	R770	VDD	R771	VDD	R772	VDD	R773	VDD	R774	VDD	R775	VDD	R776	VDD	R777	VDD	R778	VDD	R779	VDD	R780	VDD	R781	VDD	R782	VDD	R783	VDD	R784	VDD	R785	VDD	R786	VDD	R787	VDD	R788	VDD	R789	VDD	R790	VDD	R791	VDD	R792	VDD	R793	VDD	R794	VDD	R795	VDD	R796	VDD	R797	VDD	R798	VDD	R799	VDD	R800	VDD	R801	VDD
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MGT_TXP0	MGT_TXP0
MGT_TXN0	MGT_TXN0
MGT_RXP0	MGT_RXP0
MGT_RXN0	MGT_RXN0
MGT_TXP1	MGT_TXP1
MGT_TXN1	MGT_TXN1
MGT_RXP1	MGT_RXP1
MGT_RXN1	MGT_RXN1
PEN_N1	PEN_N1
PEN_N2	PEN_N2
PEN_N3	PEN_N3

FPGA	
H6	MGTREFCLK0P_227
H5	MGTREFCLK0N_227
F6	MGTREFCLK1P_227
F5	MGTREFCLK1N_227
A8	MGHTXP3_227/(MGTYTXP3_227(+))
A7	MGHTXN3_227/(MGTYTXN3_227(+))
A4	MGTHRXP3_227/(MGTYRXP3_227(+))
A3	MGTHRXN3_227/(MGTYRXN3_227(+))
B6	MGHTXP2_227/(MGTYTXP2_227(+))
B5	MGHTXN2_227/(MGTYTXN2_227(+))
B2	MGTHRXP2_227/(MGTYRXP2_227(+))
B1	MGTHRXN2_227/(MGTYRXN2_227(+))
D6	MGHTXP1_227/(MGTYTXP1_227(+))
D5	MGHTXN1_227/(MGTYTXN1_227(+))
C4	MGTHRXP1_227/(MGTYRXP1_227(+))
C3	MGTHRXN1_227/(MGTYRXN1_227(+))
E4	MGHTXP0_227/(MGTYTXP0_227(+))
E3	MGHTXN0_227/(MGTYTXN0_227(+))
D2	MGTHRXP0_227/(MGTYRXP0_227(+))
D1	MGTHRXN0_227/(MGTYRXN0_227(+))

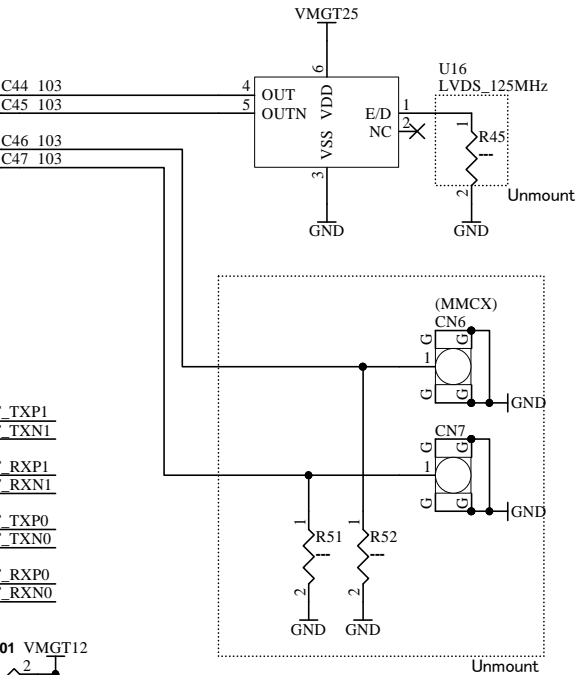
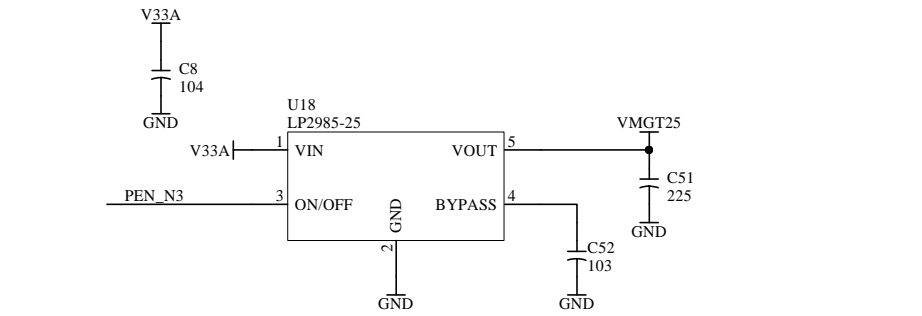
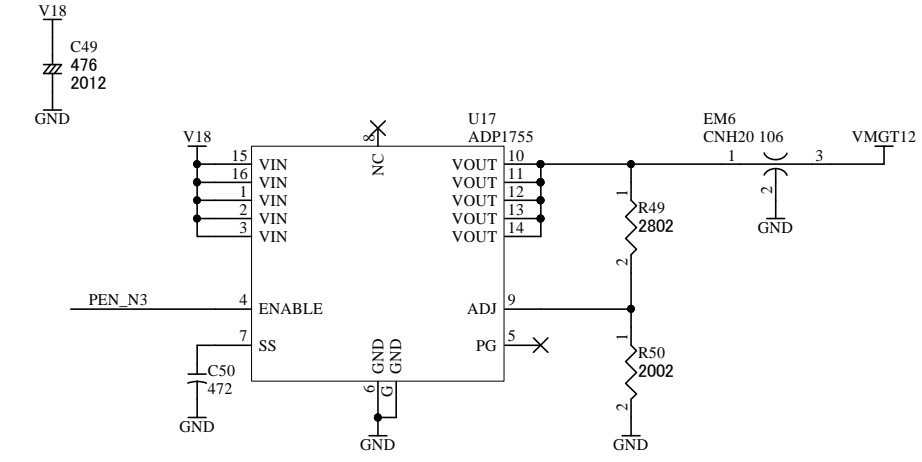
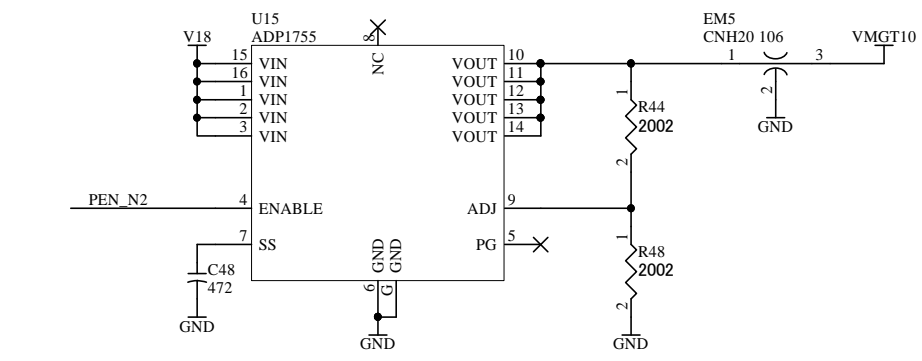
M6	MGTREFCLK0P_226
M5	MGTREFCLK0N_226
K6	MGTREFCLK1P_226
K5	MGTREFCLK1N_226
G4	MGHTXP3_226/(MGTYTXP3_226(+))
G3	MGHTXN3_226/(MGTYTXN3_226(+))
F2	MGTHRXP3_226/(MGTYRXP3_226(+))
F1	MGTHRXN3_226/(MGTYRXN3_226(+))
J4	MGHTXP2_226/(MGTYTXP2_226(+))
J3	MGHTXN2_226/(MGTYTXN2_226(+))
H2	MGTHRXP2_226/(MGTYRXP2_226(+))
H1	MGTHRXN2_226/(MGTYRXN2_226(+))
L4	MGHTXP1_226/(MGTYTXP1_226(+))
L3	MGHTXN1_226/(MGTYTXN1_226(+))
K2	MGTHRXP1_226/(MGTYRXP1_226(+))
K1	MGTHRXN1_226/(MGTYRXN1_226(+))
N4	MGHTXP0_226/(MGTYTXP0_226(+))
N3	MGHTXN0_226/(MGTYTXN0_226(+))
M2	MGTHRXP0_226/(MGTYRXP0_226(+))
M1	MGTHRXN0_226/(MGTYRXN0_226(+))

T6	MGTREFCLK0P_225
T5	MGTREFCLK0N_225
P6	MGTREFCLK1P_225
P5	MGTREFCLK1N_225
R4	MGHTXP3_225/(MGTYTXP3_225(+))
R3	MGHTXN3_225/(MGTYTXN3_225(+))
P2	MGTHRXP3_225/(MGTYRXP3_225(+))
P1	MGTHRXN3_225/(MGTYRXN3_225(+))
U4	MGHTXP2_225/(MGTYTXP2_225(+))
U3	MGHTXN2_225/(MGTYTXN2_225(+))
T2	MGTHRXP2_225/(MGTYRXP2_225(+))
T1	MGTHRXN2_225/(MGTYRXN2_225(+))
W4	MGHTXP1_225/(MGTYTXP1_225(+))
W3	MGHTXN1_225/(MGTYTXN1_225(+))
V2	MGTHRXP1_225/(MGTYRXP1_225(+))
V1	MGTHRXN1_225/(MGTYRXN1_225(+))
AA4	MGHTXP0_225/(MGTYTXP0_225(+))
AA3	MGHTXN0_225/(MGTYTXN0_225(+))
Y2	MGTHRXP0_225/(MGTYRXP0_225(+))
Y1	MGTHRXN0_225/(MGTYRXN0_225(+))

Y6	MGTREFCLK0P_224
Y5	MGTREFCLK0N_224
V6	MGTREFCLK1P_224
V5	MGTREFCLK1N_224
AB6	MGHTXP3_224/(MGTYTXP3_224(+))
AB5	MGHTXN3_224/(MGTYTXN3_224(+))
AB2	MGTHRXP3_224/(MGTYRXP3_224(+))
AB1	MGTHRXN3_224/(MGTYRXN3_224(+))
AC4	MGHTXP2_224/(MGTYTXP2_224(+))
AC3	MGHTXN2_224/(MGTYTXN2_224(+))
AD2	MGTHRXP2_224/(MGTYRXP2_224(+))
AD1	MGTHRXN2_224/(MGTYRXN2_224(+))
AD6	MGT_TXP1
AD5	MGT_TXN1
AE4	MGT_RXP1
AE3	MGT_RXN1
AF6	MGT_TXP0
AF5	MGT_TXN0
AF2	MGT_RXP0
AF1	MGT_RXN0

AF7	MGTREF_R
AF8	MGTAVTTRCAL_R
AA6	MGTAVCC/(MGTAVCC_R(+))
AC6	MGTAVCC/(MGTAVCC_R(+))
AE6	MGTAVCC/(MGTAVCC_R(+))
C6	MGTAVCC/(MGTAVCC_R(+))
E6	MGTAVCC/(MGTAVCC_R(+))
G6	MGTAVCC/(MGTAVCC_R(+))
L6	MGTAVCC/(MGTAVCC_R(+))
N6	MGTAVCC/(MGTAVCC_R(+))
R6	MGTAVCC/(MGTAVCC_R(+))
U6	MGTAVCC/(MGTAVCC_R(+))
AC2	MGTAVTT/(MGTAVTT_R(+))
AD3	MGTAVTT/(MGTAVTT_R(+))
C2	MGTAVTT/(MGTAVTT_R(+))
D3	MGTAVTT/(MGTAVTT_R(+))
G2	MGTAVTT/(MGTAVTT_R(+))
H3	MGTAVTT/(MGTAVTT_R(+))
L2	MGTAVTT/(MGTAVTT_R(+))
M3	MGTAVTT/(MGTAVTT_R(+))
R2	MGTAVTT/(MGTAVTT_R(+))
T3	MGTAVTT/(MGTAVTT_R(+))
W2	MGTAVTT/(MGTAVTT_R(+))
Y3	MGTAVTT/(MGTAVTT_R(+))

J6	MGTVCCAUX/(MGTVCCAUX_R(+))
W6	MGTVCCAUX/(MGTVCCAUX_R(+))



XCK035/040/U3P/U5P_A676



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