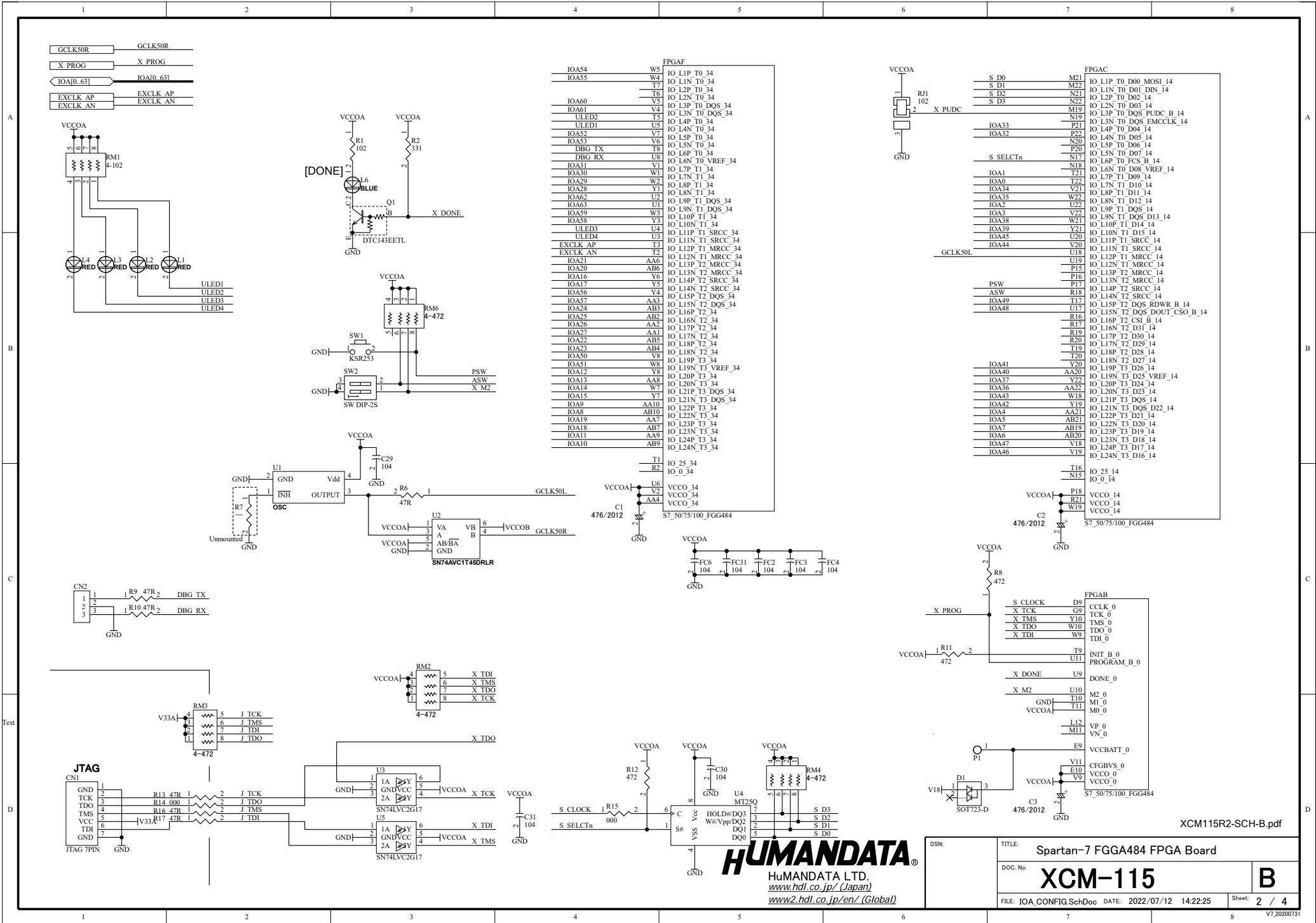


CNCB		GND		GND		GND	
EXCLK BP	EXCLK BN	EXCLK AP	EXCLK AN	EXCLK BP	EXCLK BN	EXCLK AP	EXCLK AN
1	2	3	4	5	6	7	8
9	10	11	12	13	14	15	16
17	18	19	20	21	22	23	24
25	26	27	28	29	30	31	32
33	34	35	36	37	38	39	40
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57	58	59	60	61	62	63	64
65	66	67	68	69	70	71	72
73	74	75	76	77	78	79	80

FX10-80PHG-CNB		CNA		GND		GND	
EXCLK AP	EXCLK AN	EXCLK BP	EXCLK BN	EXCLK AP	EXCLK AN	EXCLK BP	EXCLK BN
1	2	3	4	5	6	7	8
9	10	11	12	13	14	15	16
17	18	19	20	21	22	23	24
25	26	27	28	29	30	31	32
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73	74	75	76	77	78	79	80

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DSN: TITLE: Spartan-7 FGA484 FPGA Board  
 DOC. No: XCM-115  
 FILE: XCM115B.SchDoc DATE: 2022/07/12 14:22:25 Sheet: 1 / 4  
 V7.20220731



IOA	IO	FPGA
IOA54	W5	IO L1P T0_34
IOA55	W4	IO L1N T0_34
	T7	IO L2P T0_34
	T6	IO L2N T0_34
IOA60	V5	IO L3P T0_DQS_34
IOA61	V4	IO L3N T0_DQS_34
ULED2	T5	IO L4P T0_34
ULED1	U5	IO L4N T0_34
IOA52	V7	IO L5P T0_34
IOA53	V6	IO L5N T0_34
DBG TX	T8	IO L6P T0_34
DBG RX	U8	IO L6N T0_VREF_34
IOA31	V1	IO L7P T1_34
IOA30	W1	IO L7N T1_34
IOA29	W2	IO L8P T1_34
IOA28	Y1	IO L8N T1_34
IOA62	U2	IO L9P T1_DQS_34
IOA63	U1	IO L9N T1_DQS_34
IOA59	W3	IO L10P T1_34
IOA58	Y3	IO L10N T1_34
ULED3	U4	IO L11P T1_SRCC_34
ULED4	U3	IO L11N T1_SRCC_34
EXCLK AP	T3	IO L12P T1_MRCC_34
EXCLK AN	T2	IO L12N T1_MRCC_34
IOA21	AA6	IO L13P T2_MRCC_34
IOA20	AB6	IO L13N T2_MRCC_34
IOA16	Y6	IO L14P T2_SRCC_34
IOA17	Y5	IO L14N T2_SRCC_34
IOA56	Y4	IO L15P T2_DQS_34
IOA57	AA3	IO L15N T2_DQS_34
IOA24	AB3	IO L16P T2_34
IOA25	AB2	IO L16N T2_34
IOA26	AA2	IO L17P T2_34
IOA27	AA1	IO L17N T2_34
IOA22	AB5	IO L18P T2_34
IOA23	AB4	IO L18N T2_34
IOA50	V8	IO L19P T3_34
IOA51	W8	IO L19N T3_VREF_34
IOA12	Y8	IO L20P T3_34
IOA13	AA8	IO L20N T3_34
IOA14	W7	IO L21P T3_DQS_34
IOA15	Y7	IO L21N T3_DQS_34
IOA9	AB10	IO L22P T3_34
IOA8	AB10	IO L22N T3_34
IOA19	AA7	IO L23P T3_34
IOA18	AB7	IO L23N T3_34
IOA11	AA9	IO L24P T3_34
IOA10	AB9	IO L24N T3_34

IOA	IO	FPGA
	S D0	M21
	S D1	M22
	S D2	N21
	S D3	N22
		M19
		N19
IOA33	P21	IO L3P T0_DQS_PUDC_B_14
IOA32	P22	IO L3N T0_DQS_EMCCLK_14
	N20	IO L4P T0_D04_14
	P20	IO L4N T0_D05_14
S SELCTn	N17	IO L5P T0_D06_14
	N17	IO L5N T0_D07_14
	N18	IO L6P T0_FCS_B_14
	N18	IO L6N T0_D08_VREF_14
IOA1	T21	IO L7P T1_D09_14
IOA0	T22	IO L7N T1_D10_14
IOA34	V21	IO L8P T1_D11_14
IOA35	W22	IO L8N T1_D12_14
IOA2	U22	IO L9P T1_DQS_14
IOA3	V22	IO L9N T1_DQS_D13_14
IOA38	W21	IO L10P T1_D15_14
IOA39	Y21	IO L10N T1_D14_14
IOA45	U20	IO L11P T1_SRCC_14
IOA44	V20	IO L11N T1_SRCC_14
	U19	IO L12P T1_MRCC_14
	U18	IO L12N T1_MRCC_14
	P15	IO L13P T2_MRCC_14
	P16	IO L13N T2_MRCC_14
	P17	IO L14P T2_SRCC_14
	R18	IO L14N T2_SRCC_14
IOA49	T17	IO L15P T2_DQS_14
IOA48	U17	IO L15N T2_DQS_RDWR_B_14
	R16	IO L16P T2_CSI_B_14
	R17	IO L16N T2_D31_14
	R19	IO L17P T2_D30_14
	R20	IO L17N T2_D31_14
	T19	IO L18P T2_D28_14
	T20	IO L18N T2_D27_14
	V20	IO L19P T3_D26_14
IOA40	AA20	IO L19N T3_D25_VREF_14
IOA37	Y22	IO L20P T3_D24_14
IOA36	AA22	IO L20N T3_D23_14
IOA43	W18	IO L21P T3_DQS_14
IOA42	Y19	IO L21N T3_DQS_D22_14
IOA4	AA21	IO L22P T3_D21_14
IOA5	AB21	IO L22N T3_D20_14
IOA7	AB19	IO L23P T3_D19_14
IOA6	AB20	IO L23N T3_D18_14
IOA47	V18	IO L24P T3_D17_14
IOA46	V19	IO L24N T3_D16_14



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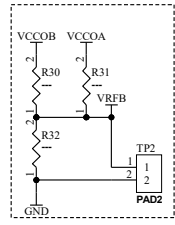
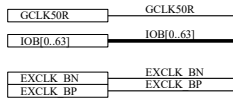
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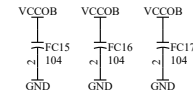
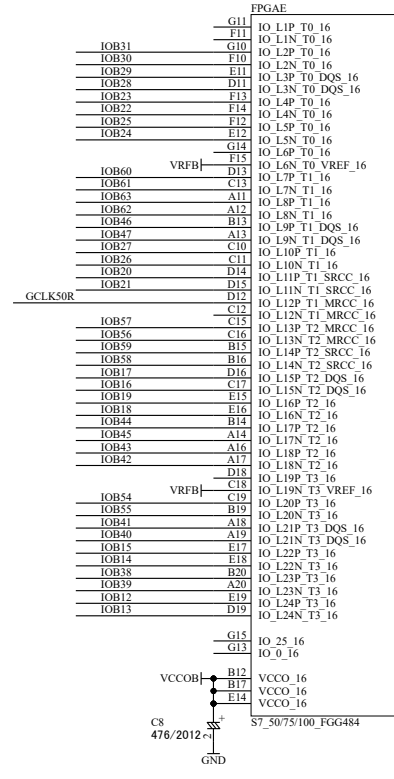
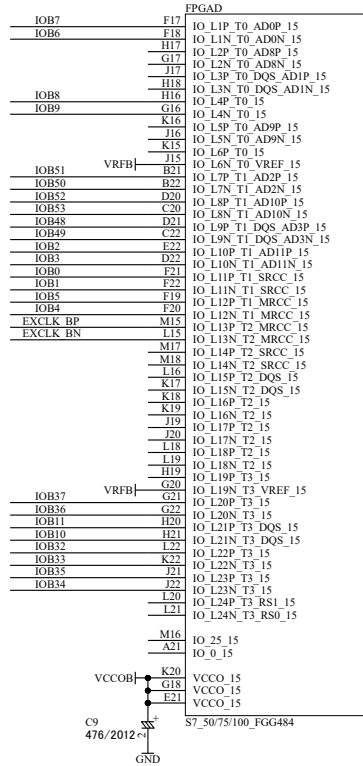
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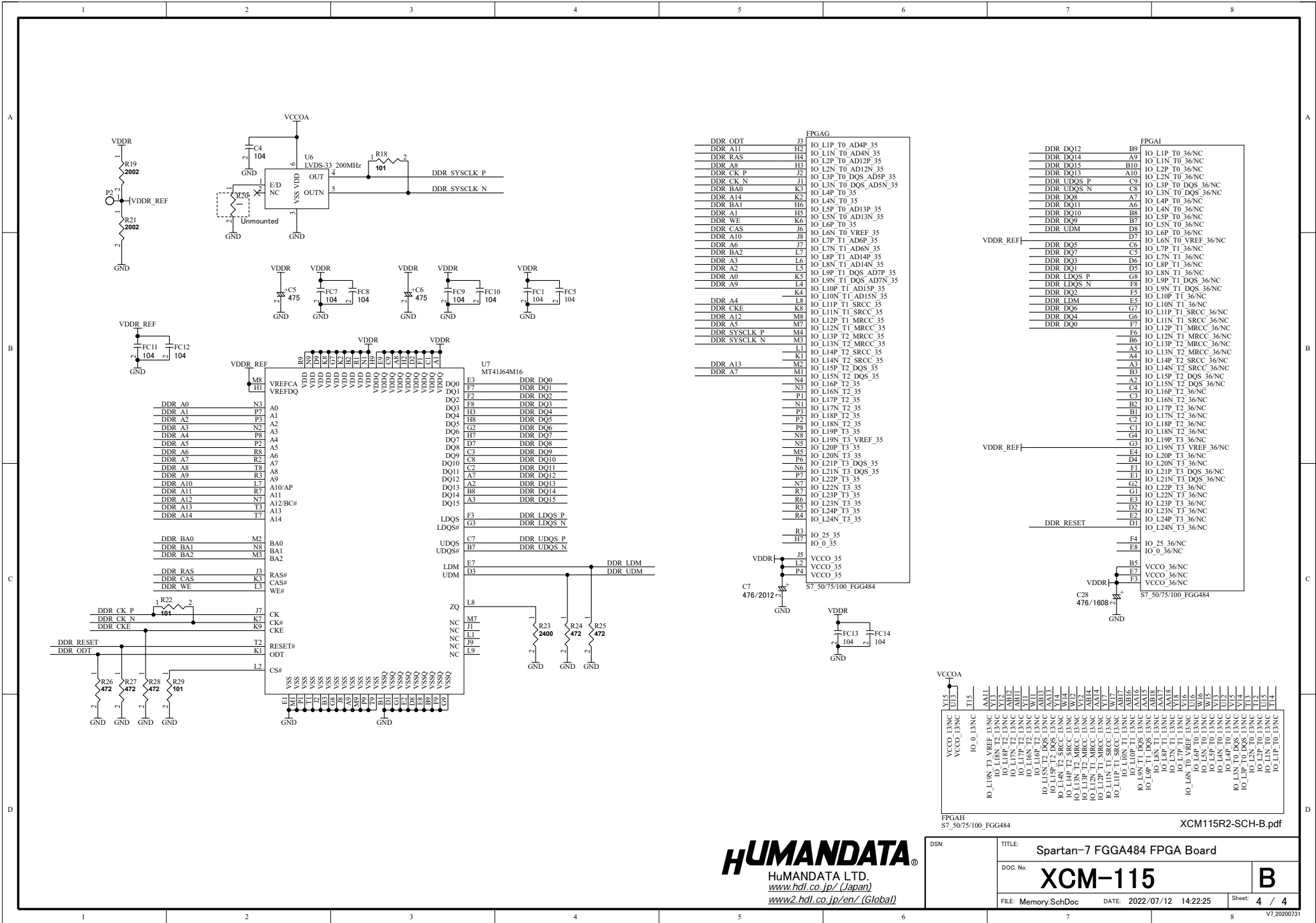
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DOC. No:	<b>XCM-115</b>
FILE: IOB.SchDoc	DATE: 2022/07/12 14:22:25
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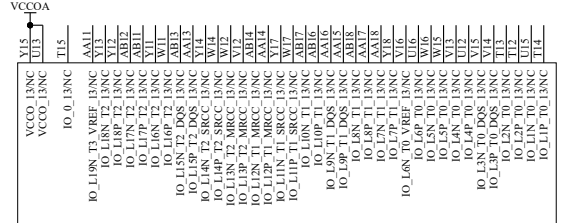
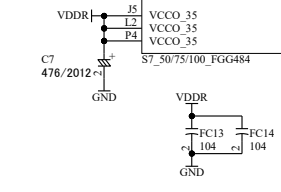


FPGA

DDR ODT	J3	IO L1P T0_AD4P_35
DDR A11	H2	IO L1N T0_AD4N_35
DDR RAS	H4	IO L2P T0_AD12P_35
DDR A8	H3	IO L2N T0_AD12N_35
DDR CK_P	J2	IO L3P T0_DQS_AD5P_35
DDR CK_N	J1	IO L3N T0_DQS_AD5N_35
DDR BA0	K3	IO L4P T0_35
DDR A14	K2	IO L4N T0_35
DDR BA1	H6	IO L5P T0_AD13P_35
DDR A1	H5	IO L5N T0_AD13N_35
DDR WE	K6	IO L6P T0_35
DDR CAS	J6	IO L6N T0_VREF_35
DDR A10	J8	IO L7P T1_AD6P_35
DDR A6	J7	IO L7N T1_AD6N_35
DDR BA2	L7	IO L8P T1_AD14P_35
DDR A3	L6	IO L8N T1_AD14N_35
DDR A2	L5	IO L9P T1_DQS_AD7P_35
DDR A0	K5	IO L9N T1_DQS_AD7N_35
DDR A9	L4	IO L10P T1_AD15P_35
DDR A4	L8	IO L10N T1_AD15N_35
DDR CKE	K8	IO L11P T1_SRCC_35
DDR A12	M8	IO L11N T1_SRCC_35
DDR A5	M7	IO L12P T1_MRCC_35
DDR SYSCLK_P	M4	IO L12N T1_MRCC_35
DDR SYSCLK_N	M3	IO L13P T2_MRCC_35
	M2	IO L13N T2_MRCC_35
	K1	IO L14P T2_SRCC_35
	M1	IO L14N T2_SRCC_35
	L1	IO L15P T2_DQS_35
	M2	IO L15N T2_DQS_35
	N3	IO L16P T2_35
	N1	IO L16N T2_35
	P1	IO L17P T2_35
	N1	IO L17N T2_35
	P2	IO L18P T2_35
	C2	IO L18N T2_35
	P8	IO L19P T3_35
	N8	IO L19N T3_VREF_35
	N5	IO L20P T3_35
	P6	IO L20N T3_35
	N4	IO L21P T3_DQS_35
	N6	IO L21N T3_DQS_35
	P7	IO L22P T3_35
	N7	IO L22N T3_35
	R7	IO L23P T3_35
	R6	IO L23N T3_35
	R5	IO L24P T3_35
	R4	IO L24N T3_35
	R3	IO_25_35
	H7	IO_0_35
	J5	VCCO_35
	L2	VCCO_35
	P4	VCCO_35

FPGA1

DDR DQ12	B9	IO L1P T0_36NC
DDR DQ14	A9	IO L1N T0_36NC
DDR DQ15	B10	IO L2P T0_36NC
DDR DQ13	A10	IO L2N T0_36NC
DDR UDOS_P	C9	IO L3P T0_DQS_36NC
DDR UDOS_N	C8	IO L3N T0_DQS_36NC
DDR DQ8	A7	IO L4P T0_36NC
DDR DQ11	A6	IO L4N T0_36NC
DDR DQ10	B8	IO L5P T0_36NC
DDR DQ9	B7	IO L5N T0_36NC
DDR UDM	D8	IO L6P T0_VREF_36NC
	D7	IO L6N T0_VREF_36NC
	C6	IO L7P T1_36NC
	C5	IO L7N T1_36NC
	D6	IO L8P T1_36NC
	D5	IO L8N T1_36NC
	G8	IO L9P T1_DQS_36NC
	F8	IO L9N T1_DQS_36NC
	E5	IO L10P T1_36NC
	E4	IO L10N T1_36NC
	G6	IO L11P T1_SRCC_36NC
	G5	IO L11N T1_SRCC_36NC
	F7	IO L12P T1_MRCC_36NC
	F6	IO L12N T1_MRCC_36NC
	B6	IO L13P T2_MRCC_36NC
	A5	IO L13N T2_MRCC_36NC
	A4	IO L14P T2_SRCC_36NC
	A3	IO L14N T2_SRCC_36NC
	A2	IO L15P T2_DQS_36NC
	A1	IO L15N T2_DQS_36NC
	C4	IO L16P T2_36NC
	C3	IO L16N T2_36NC
	B2	IO L17P T2_36NC
	B1	IO L17N T2_36NC
	C2	IO L18P T2_36NC
	C1	IO L18N T2_36NC
	G4	IO L19P T3_36NC
	G3	IO L19N T3_VREF_36NC
	E4	IO L20P T3_36NC
	D4	IO L20N T3_36NC
	F1	IO L21P T3_DQS_36NC
	E1	IO L21N T3_DQS_36NC
	G2	IO L22P T3_36NC
	G1	IO L22N T3_36NC
	E3	IO L23P T3_36NC
	D2	IO L23N T3_36NC
	F2	IO L24P T3_36NC
	D1	IO L24N T3_36NC
	F4	IO_25_36NC
	E8	IO_0_36NC
	B5	VCCO_36NC
	E7	VCCO_36NC
	F3	VCCO_36NC



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DOC. No:	<b>XCM-115</b>		<b>B</b>
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