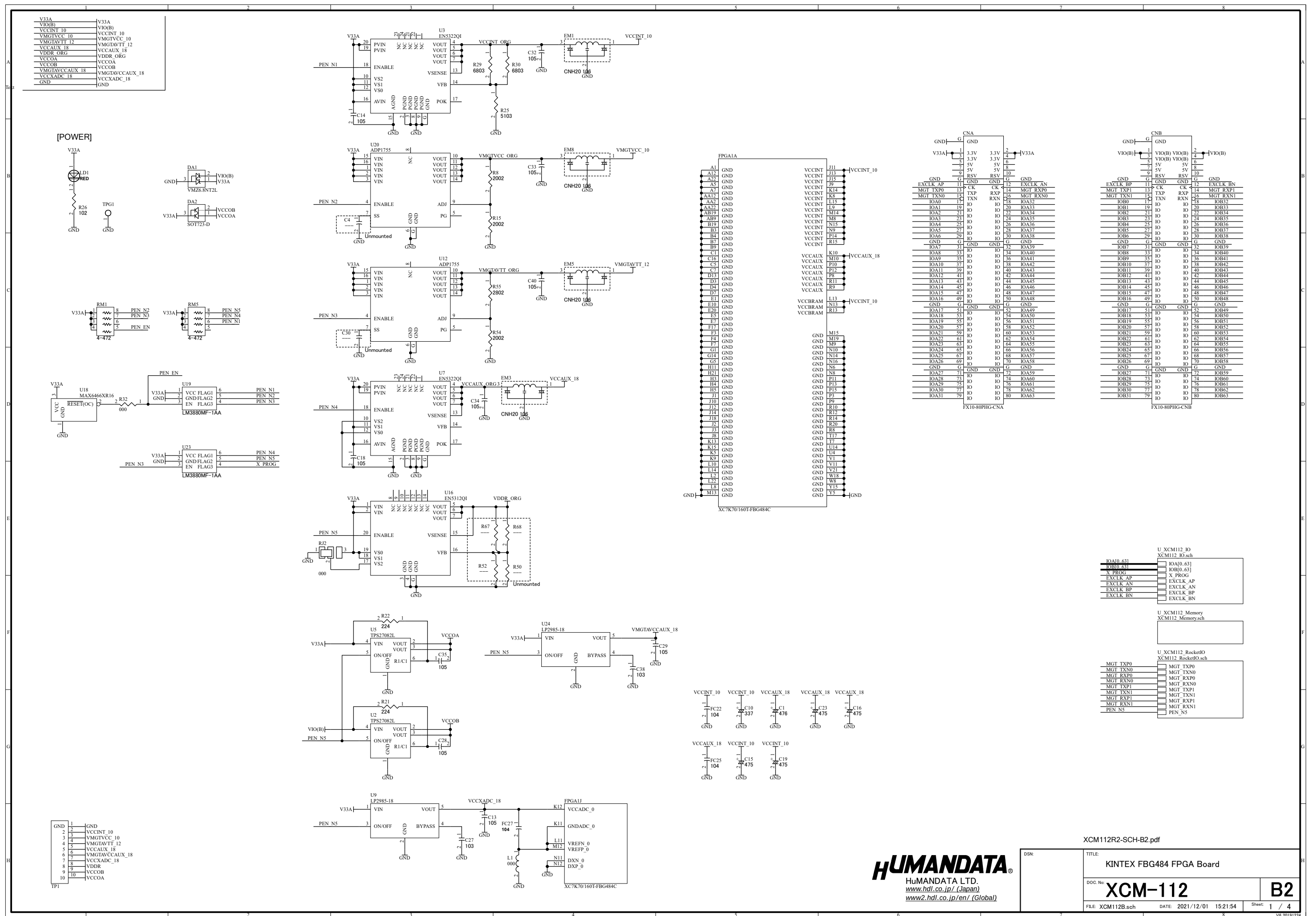
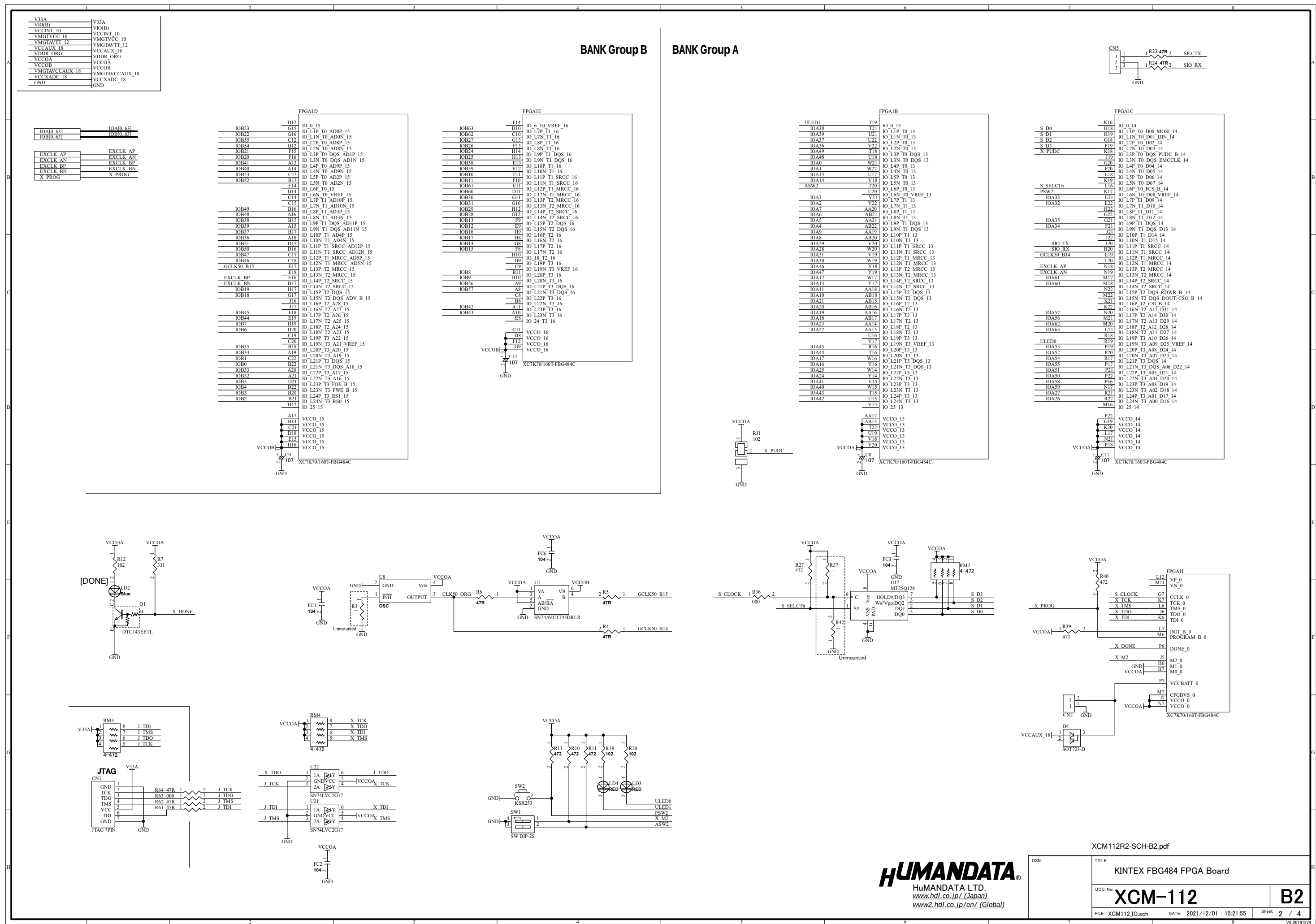
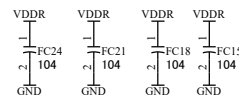
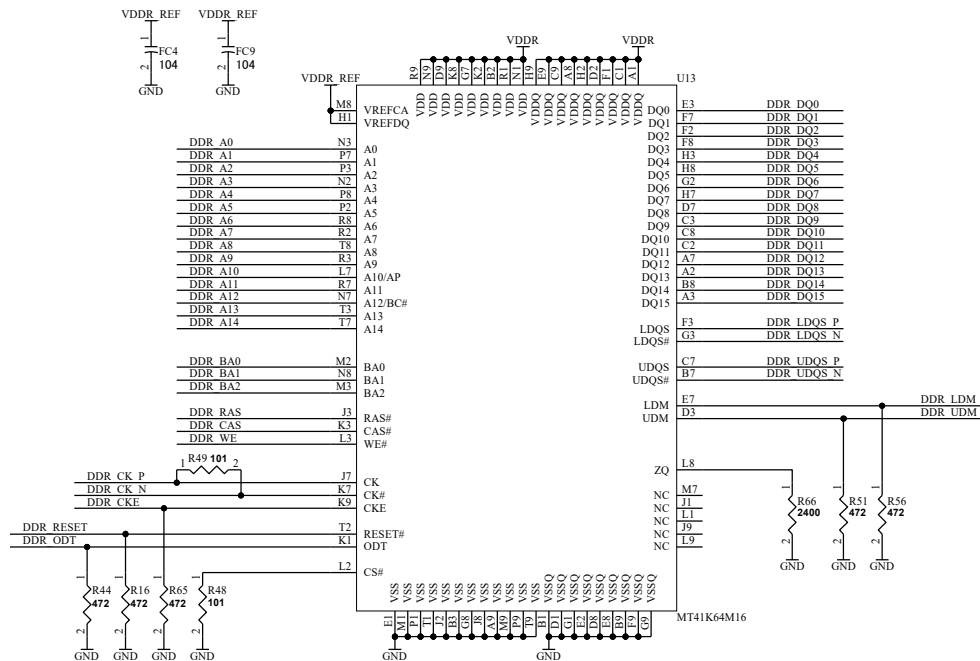
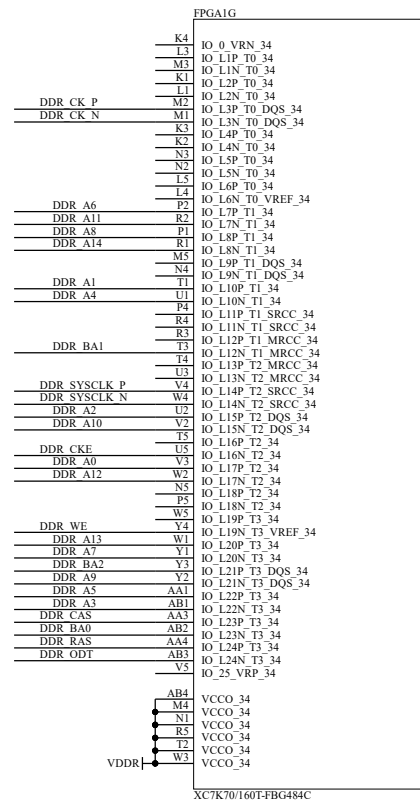
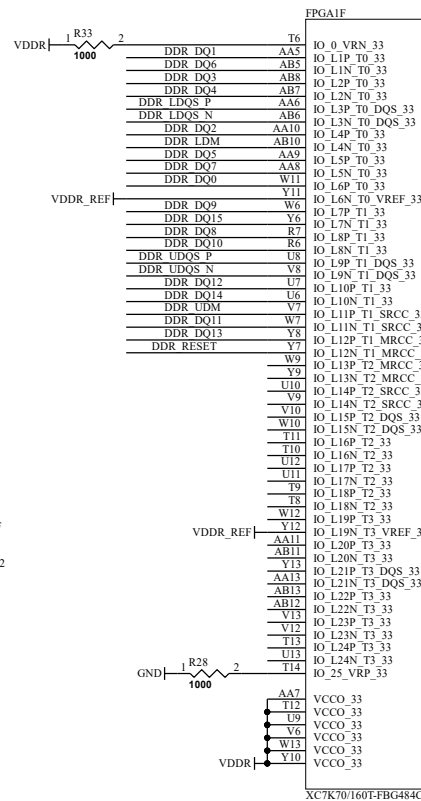
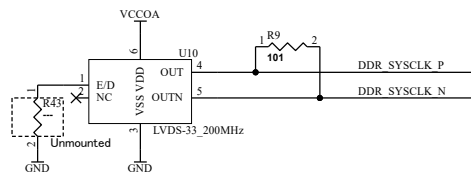
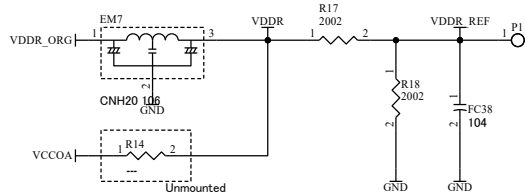






V33A
VIO(B)
VCCINT_10
VMGTAVCC_10
VMGTAVTT_12
VCCAUX_18
VDDR_ORG
VCCOA
VCCOB
VMGTAVCCAUX_18
VCCXADC_18
GND

V33A
VIO(B)
VCCINT_10
VMGTAVCC_10
VMGTAVTT_12
VCCAUX_18
VDDR_ORG
VCCOA
VCCOB
VMGTAVCCAUX_18
VCCXADC_18
GND



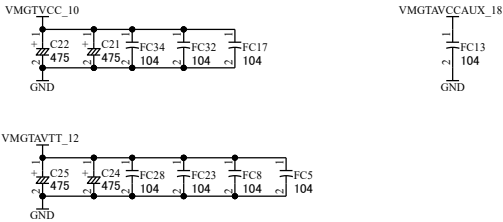
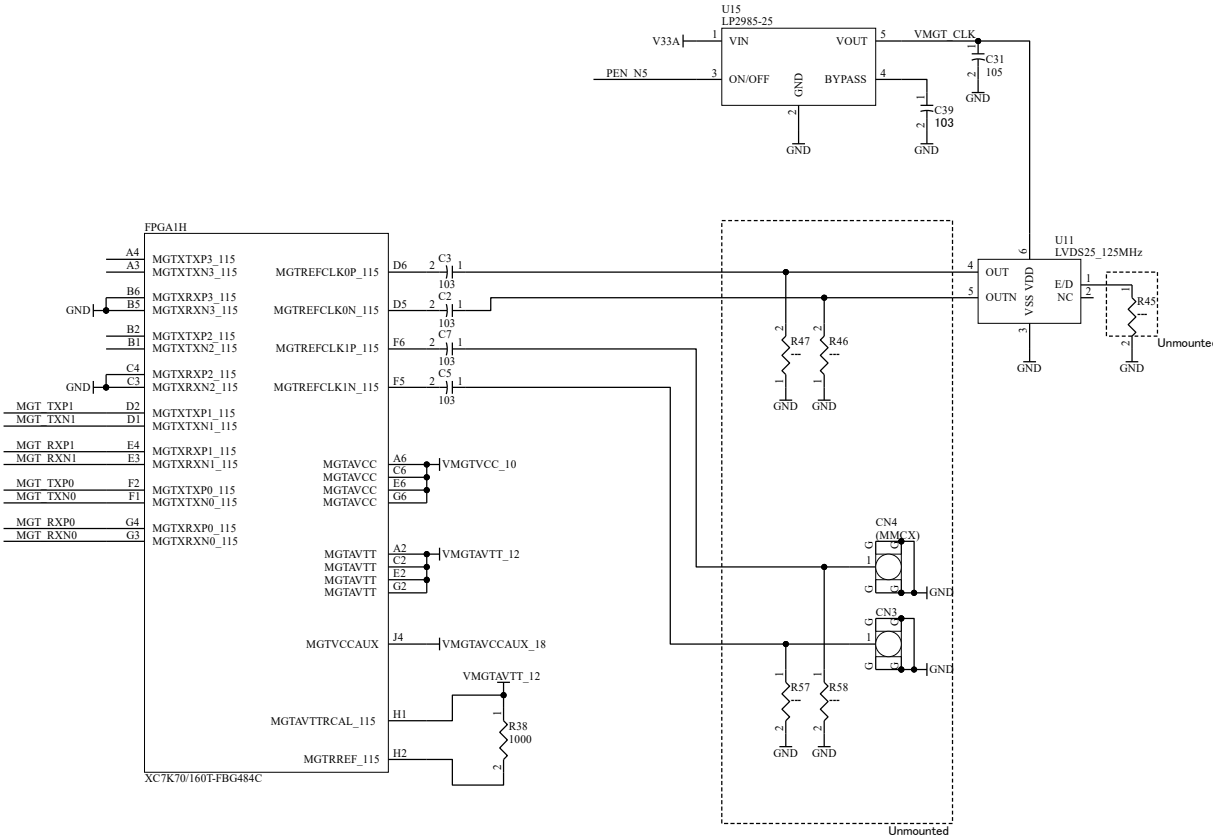
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FILE: XCM112_Memory.sch DATE: 2021/12/01 15:21:55 Sheet: 3 / 4	

V33A	V33A
VIO(B)	VIO(B)
VCCINT_10	VCCINT_10
VMGTAVCC_10	VMGTAVCC_10
VMGTAVTT_12	VMGTAVTT_12
VCCAUX_18	VCCAUX_18
VDDR_ORG	VDDR_ORG
VCCOA	VCCOA
VCCOB	VCCOB
VMGTAVCCAUX_18	VMGTAVCCAUX_18
VCCXADC_18	VCCXADC_18
GND	GND

MGT_TXP0	MGT_TXP0
MGT_TXN0	MGT_TXN0
MGT_RXP0	MGT_RXP0
MGT_RXN0	MGT_RXN0
MGT_TXP1	MGT_TXP1
MGT_TXN1	MGT_TXN1
MGT_RXP1	MGT_RXP1
MGT_RXN1	MGT_RXN1

PEN_N5	PEN_N5
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