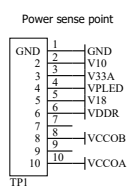


FPGA		
A1	GND	VCCINT
A8	GND	VCCINT
A15	GND	VCCINT
A22	GND	VCCINT
B4	GND	VCCINT
B11	GND	VCCINT
B18	GND	VCCINT
C7	GND	VCCINT
C14	GND	VCCINT
C21	GND	VCCINT
D3	GND	VCCINT
D10	GND	VCCINT
D17	GND	VCCINT
E6	GND	VCCINT
E13	GND	VCCINT
E20	GND	VCCINT
F2	GND	VCCINT
F16	GND	VCCINT
G5	GND	VCCINT
G12	GND	VCCINT
G19	GND	VCCINT
H1	GND	VCCINT
H8	GND	VCCINT
H10	GND	VCCINT
H12	GND	VCCINT
H14	GND	VCCINT
H15	GND	VCCINT
H22	GND	VCCINT
J4	GND	VCCINT
J9	GND	VCCINT
J11	GND	VCCINT
J13	GND	VCCINT
J18	GND	VCCINT
K7	GND	VCCINT
K10	GND	VCCINT
K14	GND	VCCINT
L3	GND	VCCINT
L9	GND	VCCINT
L13	GND	VCCINT
L17	GND	VCCINT
M6	GND	VCCINT
M10	GND	VCCINT
M14	GND	VCCINT
N2	GND	VCCINT
N9	GND	VCCINT
N13	GND	VCCINT
N16	GND	VCCINT
P5	GND	VCCINT
P10	GND	VCCINT
P12	GND	VCCINT
H9	VCCBRAM	VCCBRAM
K9	VCCBRAM	VCCBRAM
M9	VCCBRAM	VCCBRAM
P9	VCCBRAM	VCCBRAM
J14	VCCCAUX	VCCCAUX
L14	VCCCAUX	VCCCAUX
N14	VCCCAUX	VCCCAUX
R14	VCCCAUX	VCCCAUX
AB22	GND	VCCINT
AB15	GND	VCCINT
AB8	GND	VCCINT
AB1	GND	VCCINT
AA19	GND	VCCINT
AA12	GND	VCCINT
AA5	GND	VCCINT
Y16	GND	VCCINT
Y9	GND	VCCINT
Y2	GND	VCCINT
W20	GND	VCCINT
W13	GND	VCCINT
W6	GND	VCCINT
Y17	GND	VCCINT
V3	GND	VCCINT
U21	GND	VCCINT
U14	GND	VCCINT
U7	GND	VCCINT
T18	GND	VCCINT
T4	GND	VCCINT
R22	GND	VCCINT
R15	GND	VCCINT
R13	GND	VCCINT
R11	GND	VCCINT
R9	GND	VCCINT
R8	GND	VCCINT
R1	GND	VCCINT
P19	GND	VCCINT
P14	GND	VCCINT

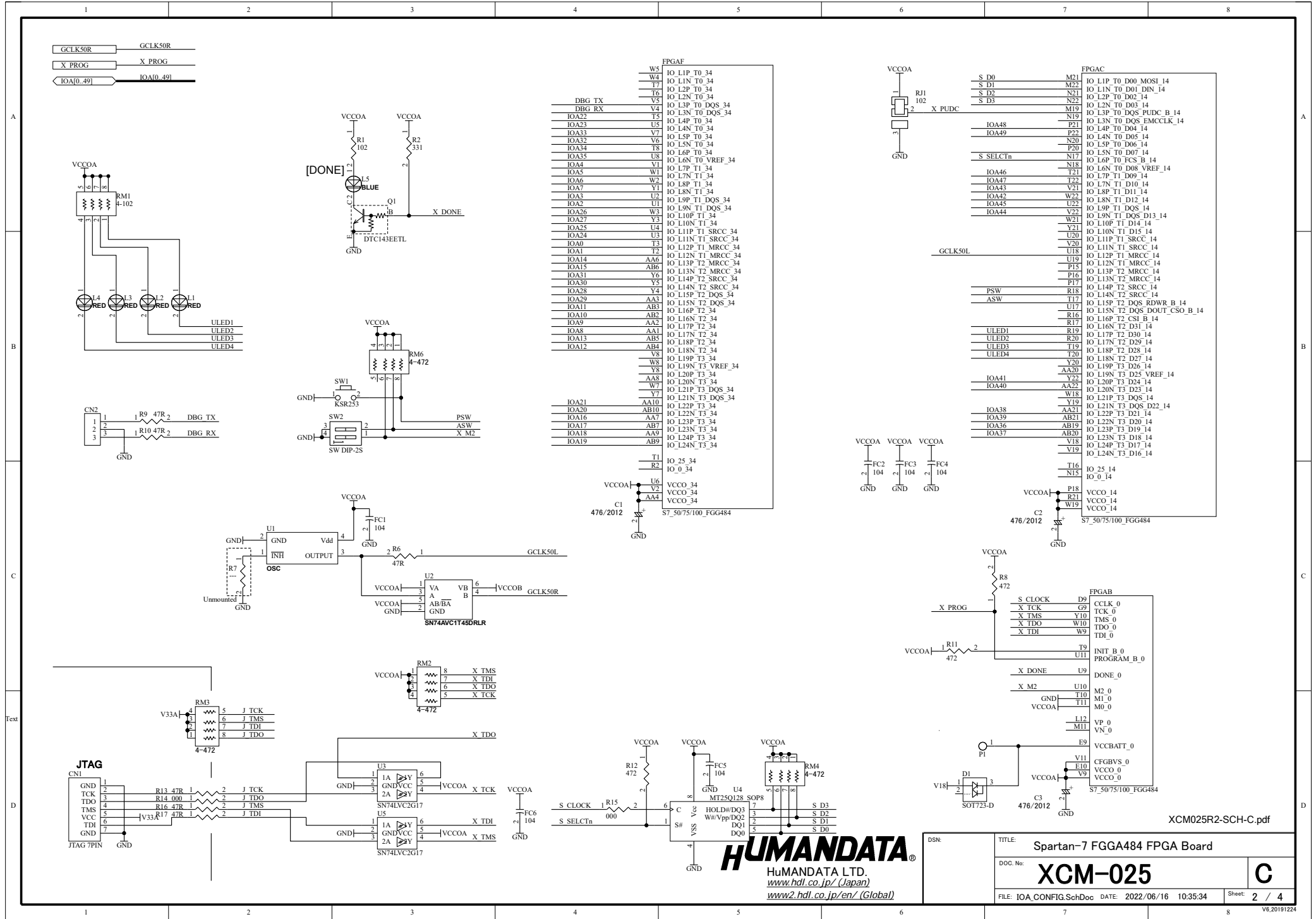
CNA		
V33A1	VIO(A)	VIO(A)
GND	5	GND
GND	6	GND
GND	7	GND
GND	8	GND
GND	9	GND
GND	10	GND
GND	11	GND
GND	12	GND
GND	13	GND
GND	14	GND
GND	15	GND
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GND	62	GND
GND	63	GND
GND	64	GND
GND	65	GND
GND	66	GND



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DSN:	TITLE: Spartan-7 FGA484 FPGA Board
DOC. No:	<b>XCM-025</b>
FILE: XCM025C.SchDoc	DATE: 2022/06/16 10:35:34
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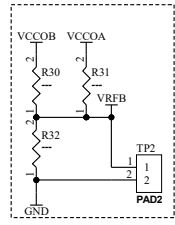
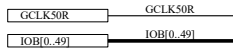
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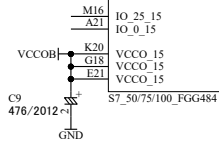
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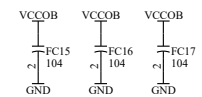
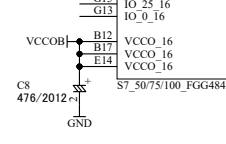


Unmounted

IOB	Signal	IOB	Signal
IOB31	F17	IOB34	J19
IOB30	F18	IOB35	J20
	H17		L18
	G17		L19
	H17		H19
	H18		G20
	H16		G22
	G16		H20
	H16		H21
	K16		L22
	H16		K22
	K15		J21
	J15		J22
IOB12	VRFB  B21	IOB36	L20
IOB13	B22	IOB37	L21
IOB24	D20		M16
IOB25	C20		A21
IOB38	D21		K20
IOB39	C22		G18
IOB40	F22		E21
IOB41	D22		G18
IOB43	F21		E21
IOB42	F22		E21
	F19		E21
	F20		E21
	M15		E21
	L15		E21
	M17		E21
	M18		E21
	L16		E21
	K17		E21
	K18		E21
	K19		E21
	J19		E21
	J20		E21
	L18		E21
	L19		E21
	H19		E21
	G20		E21
	G22		E21
	H20		E21
	H21		E21
	L22		E21
	K22		E21
	J21		E21
	J22		E21
	L20		E21
	L21		E21



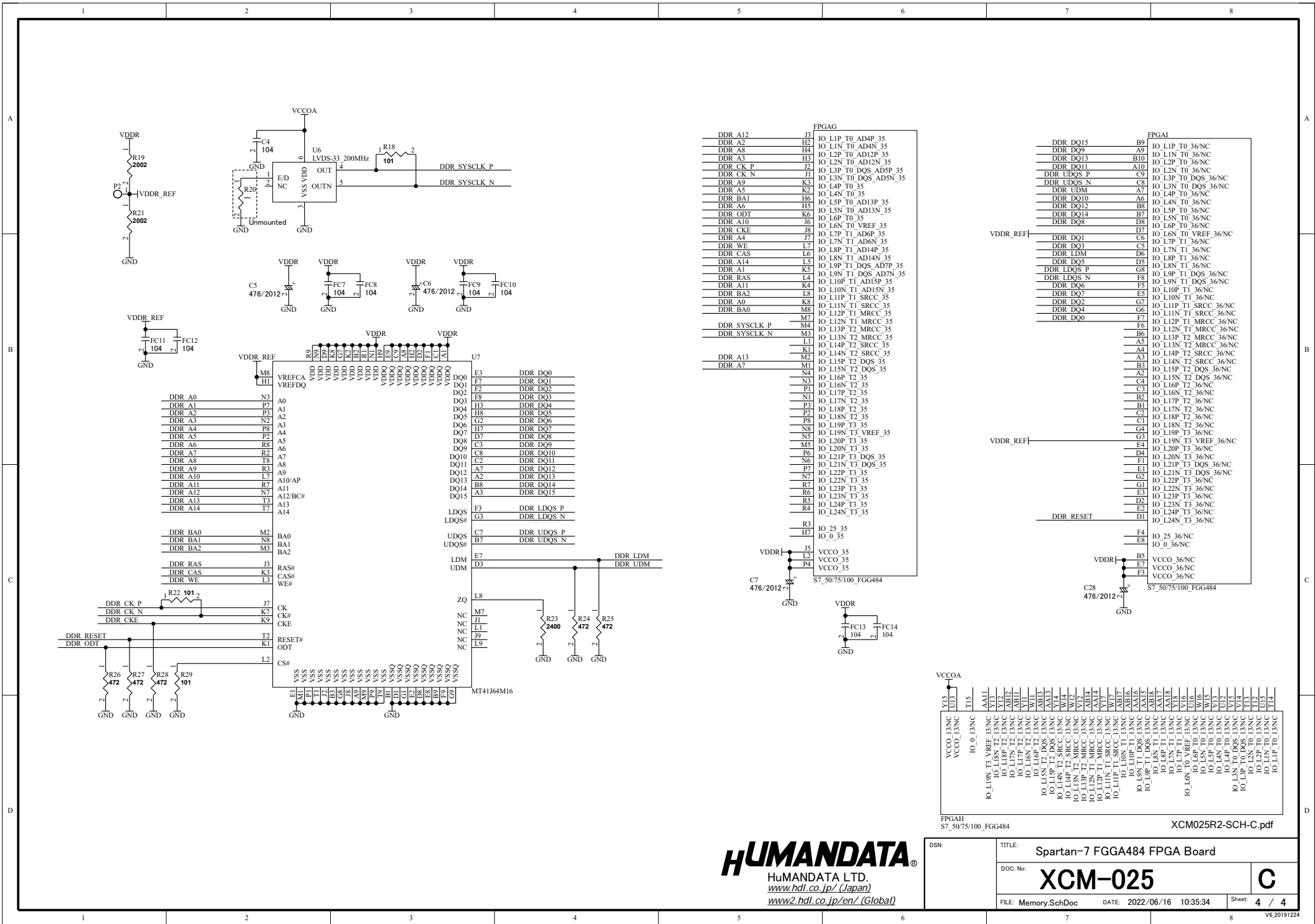
IOB	Signal	IOB	Signal
IOB14	VRFB  D13	IOB22	VRFB  C19
IOB15	C13	IOB23	B19
IOB0	A11	IOB8	A18
IOB1	A12	IOB9	A19
IOB2	B13	IOB29	E17
IOB3	A13	IOB38	F18
	C10	IOB11	B20
	C11	IOB10	A20
	D14	IOB27	E19
	D15	IOB26	D19
	D12		G15
	C12		G13
	C15		B12
	C16		B17
	B15		E14
	B16		E14
	D16		E14
	C17		E14
	E15		E14
	E16		E14
	B14		E14
	IOB5		E14
	IOB6		E14
	IOB7		E14
	D18		E14
	C18		E14
	C19		E14
	B19		E14
	A18		E14
	A19		E14
	E17		E14
	F18		E14
	B20		E14
	A20		E14
	E19		E14
	D19		E14



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FPGA

DDR_A12	J3
DDR_A2	H2
DDR_A8	H4
DDR_A3	H3
DDR_CK_P	J2
DDR_CK_N	J1
DDR_A9	K3
DDR_A5	K2
DDR_BA1	H6
DDR_A6	H5
DDR_ODT	K6
DDR_A10	J6
DDR_CKE	J8
DDR_A4	J7
DDR_WE	L7
DDR_CAS	L6
DDR_A14	L5
DDR_A1	K5
DDR_RAS	L4
DDR_A11	K4
DDR_BA2	L8
DDR_A0	K8
DDR_BA0	M8
DDR_SYSCLK_P	M7
DDR_SYSCLK_N	M4
DDR_A13	K1
DDR_A7	M1
DDR_DQ0	E3
DDR_DQ1	F7
DDR_DQ2	F2
DDR_DQ3	F8
DDR_DQ4	H3
DDR_DQ5	H8
DDR_DQ6	G2
DDR_DQ7	H7
DDR_DQ8	D7
DDR_DQ9	R8
DDR_DQ10	D9
DDR_DQ11	C2
DDR_DQ12	A7
DDR_DQ13	A2
DDR_DQ14	B8
DDR_DQ15	A3
DDR_LDQS_P	F3
DDR_LDQS_N	G3
DDR_UDQS_P	C7
DDR_UDQS_N	B7
DDR_LDM	E7
DDR_UDM	D3
DDR_CK_P	J7
DDR_CK_N	K7
DDR_CKE	K9
DDR_RESET	T2
DDR_ODT	K1
VDD	J5
VDDREF	L2
VDDCOA	P4

FPGA

DDR_DQ15	B9
DDR_DQ9	A9
DDR_DQ13	B10
DDR_DQ11	A10
DDR_LDQS_P	C9
DDR_LDQS_N	C8
DDR_UDM	A7
DDR_DQ10	A6
DDR_DQ12	B8
DDR_DQ14	B7
DDR_DQ8	D8
DDR_DQ1	C6
DDR_DQ3	C5
DDR_LDM	D6
DDR_DQ5	D5
DDR_LDQS_P	G8
DDR_LDQS_N	F8
DDR_DQ6	F5
DDR_DQ7	E5
DDR_DQ2	G7
DDR_DQ4	G6
DDR_DQ0	F7
IO_L1P_T0_AD4P_35	F6
IO_L1N_T0_AD4N_35	B6
IO_L2P_T0_AD12P_35	A5
IO_L2N_T0_AD12N_35	C3
IO_L3P_T0_DQ5_36NC	B2
IO_L3N_T0_DQ5_36NC	B1
IO_L4P_T0_35	C2
IO_L4N_T0_35	G1
IO_L5P_T0_35	E3
IO_L5N_T0_35	D2
IO_L6P_T0_35	E1
IO_L6N_T0_35	G2
IO_L7P_T0_AD13P_35	G3
IO_L7N_T0_AD13N_35	F4
IO_L8P_T0_AD14P_35	D1
IO_L8N_T0_AD14N_35	F1
IO_L9P_T1_DQ5_36NC	D1
IO_L9N_T1_DQ5_36NC	D1
IO_L10P_T1_36NC	F4
IO_L10N_T1_36NC	E8
IO_L11P_T1_SRCC_36NC	F4
IO_L11N_T1_SRCC_36NC	E8
IO_L12P_T1_MRCC_36NC	F4
IO_L12N_T1_MRCC_36NC	E8
IO_L13P_T2_MRCC_36NC	F4
IO_L13N_T2_MRCC_36NC	E8
IO_L14P_T2_SRCC_35	F4
IO_L14N_T2_SRCC_35	E8
IO_L15P_T2_DQ5_36NC	F4
IO_L15N_T2_DQ5_36NC	E8
IO_L16P_T2_35	F4
IO_L16N_T2_35	E8
IO_L17P_T2_35	F4
IO_L17N_T2_35	E8
IO_L18P_T2_35	F4
IO_L18N_T2_35	E8
IO_L19P_T3_VREF_35	F4
IO_L19N_T3_VREF_35	E8
IO_L20P_T3_35	F4
IO_L20N_T3_35	E8
IO_L21P_T3_DQ5_35	F4
IO_L21N_T3_DQ5_35	E8
IO_L22P_T3_35	F4
IO_L22N_T3_35	E8
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IO_L23N_T3_35	E8
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IO_L24N_T3_35	E8
IO_25_35	F4
IO_0_35	E8
VDDR	B5
VDDREF	E7
VDDCOA	F3

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