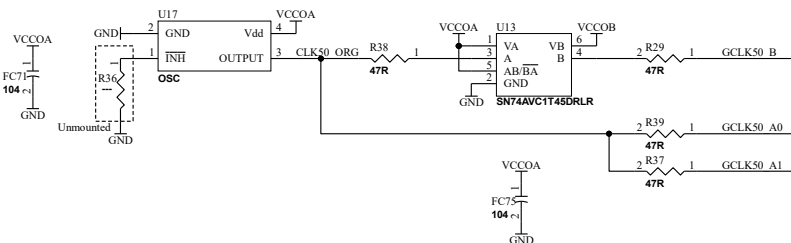
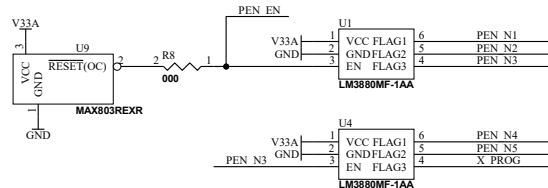
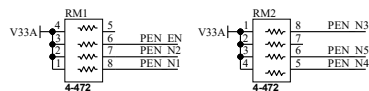
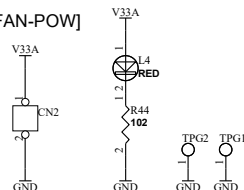


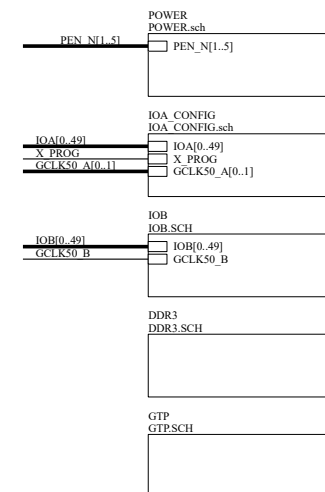
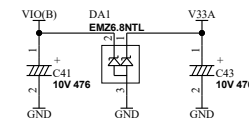
V33A
VIO(B)
VCCINT 10
VMGT VCC 10
VMGTAVTT 12
VCCAUX 18
VMGTCLK
VCCOA
VCCOB
VDDR
GND

[POWER]

[FAN-POW]



V33A	1	VIO(A)	1	VIO(B)	1
GND	5	VIO(A)	5	VIO(B)	5
GND	6	GND	6	GND	6
IOA0	7	GND	7	IOB0	7
IOA1	8	GND	8	IOB1	8
IOA2	9	GND	9	IOB2	9
IOA3	10	GND	10	IOB3	10
IOA4	11	GND	11	IOB4	11
IOA5	12	GND	12	IOB5	12
IOA6	13	GND	13	IOB6	13
IOA7	14	GND	14	IOB7	14
GND	15	GND	15	GND	15
GND	16	GND	16	GND	16
IOA8	17	GND	17	IOB8	17
IOA9	18	GND	18	IOB9	18
IOA10	19	GND	19	IOB10	19
IOA11	20	GND	20	IOB11	20
IOA12	21	GND	21	IOB12	21
IOA13	22	GND	22	IOB13	22
IOA14	23	GND	23	IOB14	23
IOA15	24	GND	24	IOB15	24
GND	25	GND	25	GND	25
GND	26	GND	26	GND	26
IOA16	27	GND	27	IOB16	27
IOA17	28	GND	28	IOB17	28
IOA18	29	GND	29	IOB18	29
IOA19	30	GND	30	IOB19	30
IOA20	31	GND	31	IOB20	31
IOA21	32	GND	32	IOB21	32
IOA22	33	GND	33	IOB22	33
IOA23	34	GND	34	IOB23	34
GND	35	GND	35	GND	35
GND	36	GND	36	GND	36
IOA24	37	GND	37	IOB24	37
IOA25	38	GND	38	IOB25	38
IOA26	39	GND	39	IOB26	39
IOA27	40	GND	40	IOB27	40
IOA28	41	GND	41	IOB28	41
IOA29	42	GND	42	IOB29	42
IOA30	43	GND	43	IOB30	43
IOA31	44	GND	44	IOB31	44
GND	45	GND	45	GND	45
GND	46	GND	46	GND	46
IOA32	47	GND	47	IOB32	47
IOA33	48	GND	48	IOB33	48
IOA34	49	GND	49	IOB34	49
IOA35	50	GND	50	IOB35	50
IOA36	51	GND	51	IOB36	51
IOA37	52	GND	52	IOB37	52
IOA38	53	GND	53	IOB38	53
IOA39	54	GND	54	IOB39	54
GND	55	GND	55	GND	55
GND	56	GND	56	GND	56
IOA40	57	GND	57	IOB40	57
IOA41	58	GND	58	IOB41	58
IOA42	59	GND	59	IOB42	59
IOA43	60	GND	60	IOB43	60
IOA44	61	GND	61	IOB44	61
IOA45	62	GND	62	IOB45	62
IOA46	63	GND	63	IOB46	63
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IOA48	65	GND	65	IOB48	65
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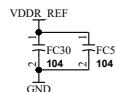
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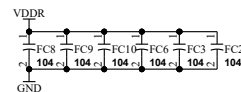
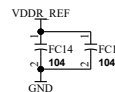
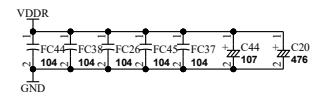
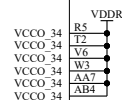
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V33A V33A
VIO(B) VIO(B)
VCCINT_10 VCCINT_10
VMGTVCC_10 VMGTVCC_10
VMGTAVTT_12 VMGTAVTT_12
VCCAUX_18 VCCAUX_18
VMGTCLK VMGTCLK
VCCOA VCCOA
VCCOB VCCOB
VDDR VDDR
GND GND



DDR DQ6	T1	FPGA
DDR DQ4	U1	IO L1P T0_34
DDR DQ0	U2	IO L1N T0_34
DDR DQ7	V2	IO L2P T0_34
DDR LDQS P	R3	IO L2N T0_34
DDR LDQS N	R2	IO L3P T0 DQS_34
DDR DQ1	W2	IO L3N T0 DQS_34
DDR LDM	Y2	IO L4P T0_34
DDR DQ5	W1	IO L4N T0_34
DDR DQ3	Y1	IO L5P T0_34
DDR DQ2	U3	IO L5N T0_34
	V3	IO L6P T0_34
DDR DQ11	AA1	IO L6N T0 VREF_34
DDR DQ9	AB1	IO L7P T1_34
DDR DQ8	AB3	IO L7N T1_34
DDR UDM	AB2	IO L8P T1_34
DDR UDQS P	Y3	IO L8N T1_34
DDR UDQS N	AA3	IO L9P T1 DQS_34
DDR DQ14	AA5	IO L9N T1 DQS_34
DDR DQ10	AB5	IO L10P T1_34
DDR DQ13	Y4	IO L10N T1_34
DDR DQ12	AA4	IO L11P T1 SRCC_34
DDR DQ15	V4	IO L11N T1 SRCC_34
DDR RESET	W4	IO L12P T1 MRCC_34
DDR CKE	R4	IO L12N T1 MRCC_34
DDR CAS	T4	IO L13P T2 MRCC_34
DDR A3	T5	IO L13N T2 MRCC_34
DDR BA2	U5	IO L14P T2 SRCC_34
DDR ODT	W6	IO L14N T2 SRCC_34
DDR WE	W5	IO L15P T2 DQS_34
DDR A5	U6	IO L15N T2 DQS_34
DDR BA0	V5	IO L16P T2_34
DDR A7	R6	IO L16N T2_34
DDR A2	T6	IO L17P T2_34
DDR A10	Y6	IO L17N T2_34
DDR A6	AA6	IO L18P T2_34
DDR A12	W7	IO L18N T2_34
	W7	IO L19P T3_34
DDR A11	AB7	IO L19N T3 VREF_34
DDR A8	AB6	IO L20P T3_34
DDR CK P	V9	IO L20N T3_34
DDR CK N	V8	IO L21P T3 DQS_34
DDR A1	AA8	IO L21N T3 DQS_34
DDR BA1	AB8	IO L22P T3_34
DDR RAS	Y8	IO L22N T3_34
DDR A4	Y7	IO L23P T3_34
DDR A0	W9	IO L23N T3_34
DDR A9	Y9	IO L24P T3_34
	Y9	IO L24N T3_34

T3	IO 0_34
U7	IO 25_34

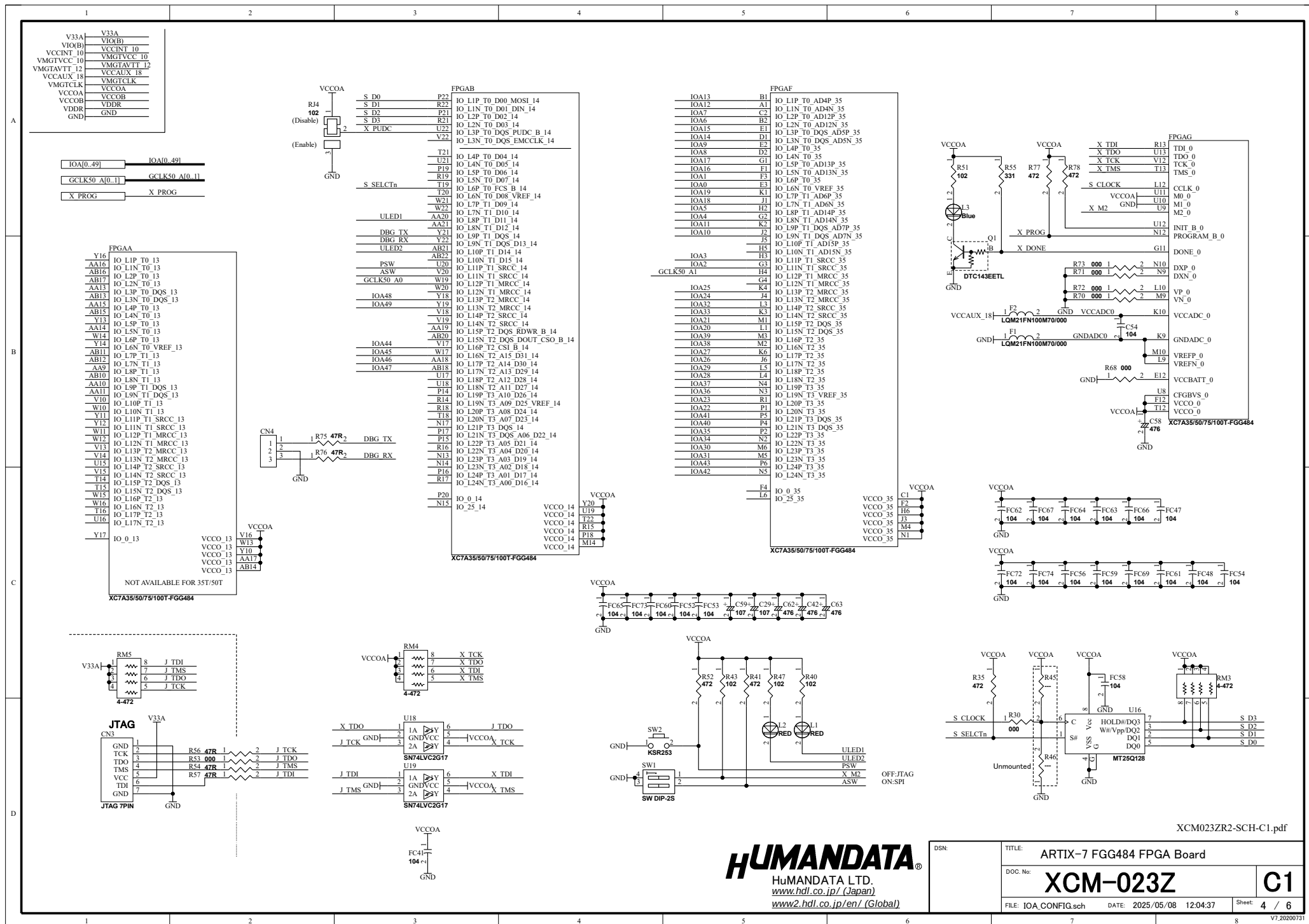


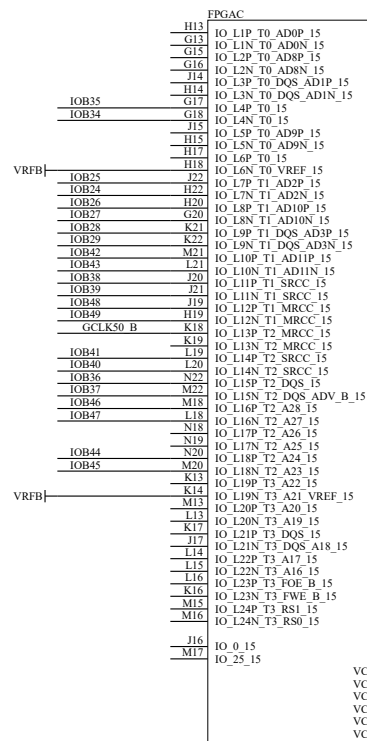
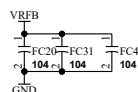
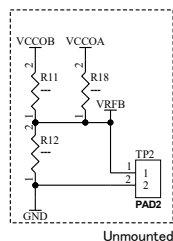
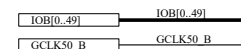
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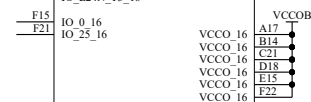
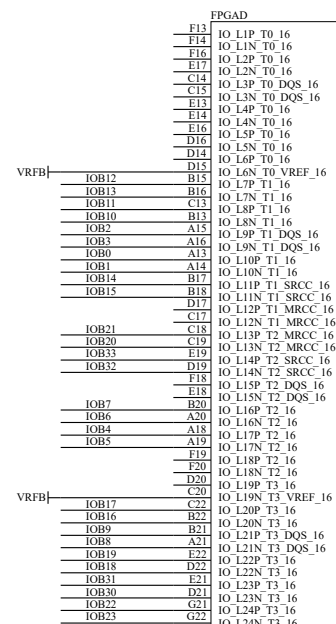
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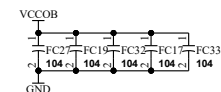
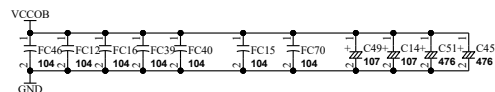




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