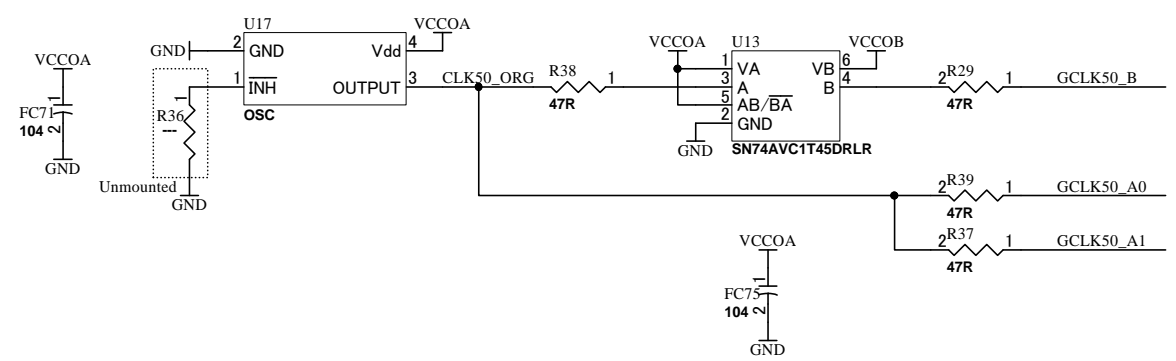
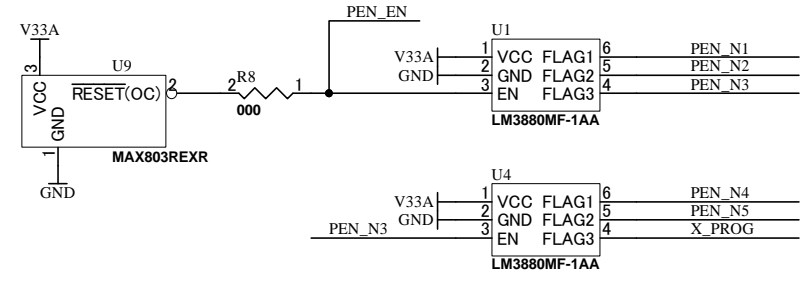
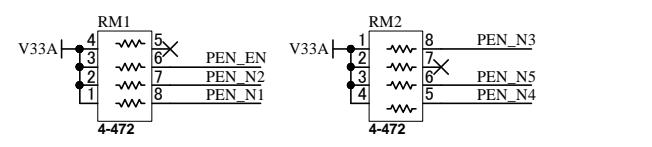
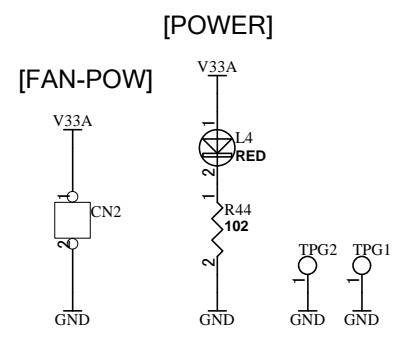
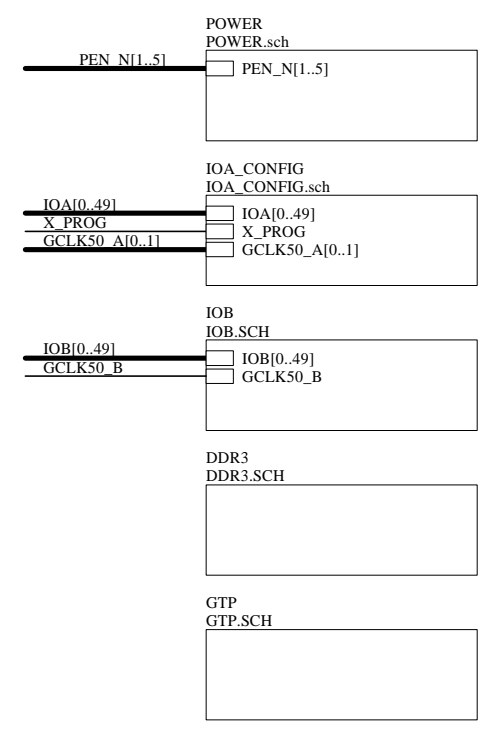
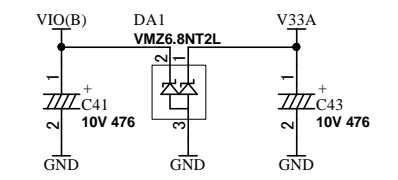


V33A	V33A
VIO(B)	VIO(B)
VCCINT_10	VCCINT_10
VMGTVCC_10	VMGTVCC_10
VMGTAVTT_12	VMGTAVTT_12
VCCAUX_18	VCCAUX_18
VMGTCLK	VMGTCLK
VCCOA	VCCOA
VCCOB	VCCOB
VDDR	VDDR
GND	GND



V33A	CNA	VIO(A)
GND	1	VIO(A)
GND	2	5V
IOA0	3	5V
IOA1	4	GND
IOA2	5	GND
IOA3	6	GND
IOA4	7	GND
IOA5	8	GND
IOA6	9	GND
IOA7	10	GND
IOA8	11	GND
IOA9	12	GND
IOA10	13	GND
IOA11	14	GND
IOA12	15	GND
IOA13	16	GND
IOA14	17	GND
IOA15	18	GND
GND	19	GND
GND	20	GND
IOA16	21	GND
IOA17	22	GND
IOA18	23	GND
IOA19	24	GND
IOA20	25	GND
IOA21	26	GND
IOA22	27	GND
IOA23	28	GND
GND	29	GND
GND	30	GND
IOA24	31	GND
IOA25	32	GND
IOA26	33	GND
IOA27	34	GND
IOA28	35	GND
IOA29	36	GND
IOA30	37	GND
IOA31	38	GND
GND	39	GND
GND	40	GND
IOA32	41	GND
IOA33	42	GND
IOA34	43	GND
IOA35	44	GND
IOA36	45	GND
IOA37	46	GND
IOA38	47	GND
IOA39	48	GND
GND	49	GND
GND	50	GND
IOA40	51	GND
IOA41	52	GND
IOA42	53	GND
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IOA45	56	GND
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IOA48	59	GND
IOA49	60	GND
GND	61	GND
GND	62	GND
GND	63	GND
GND	64	GND
GND	65	GND
GND	66	GND

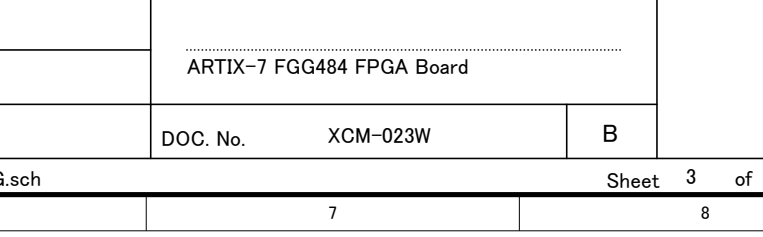
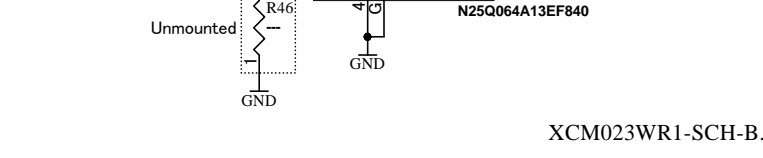
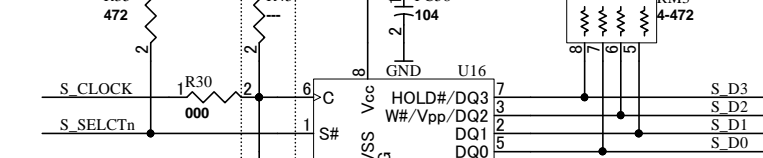
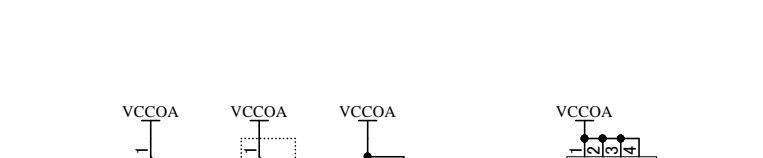
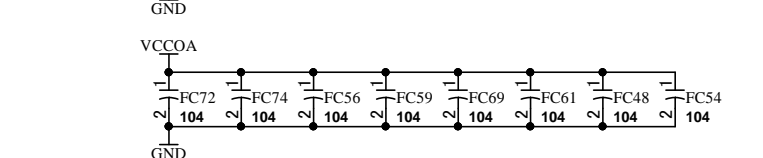
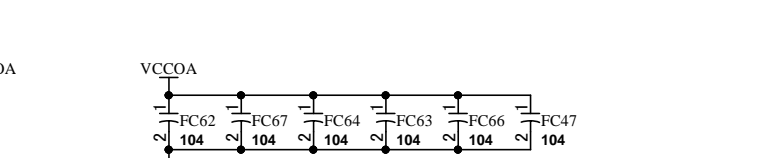
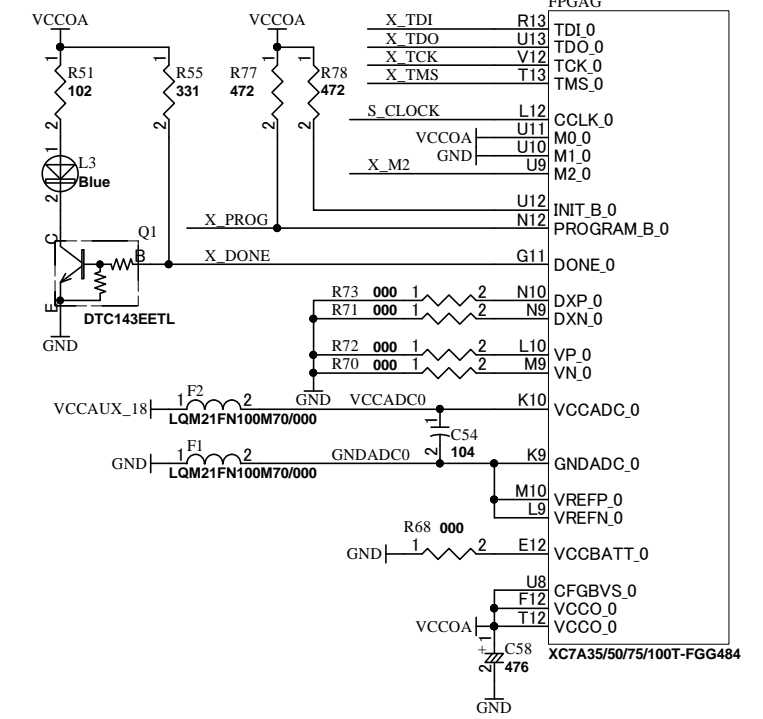
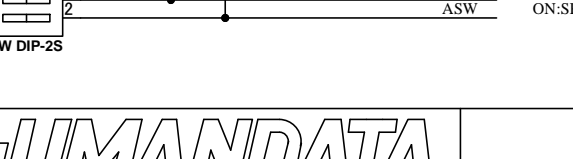
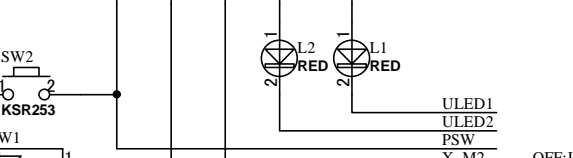
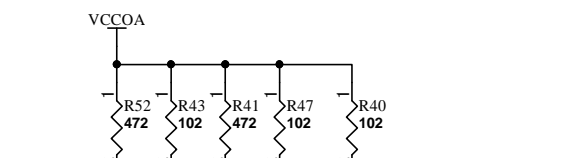
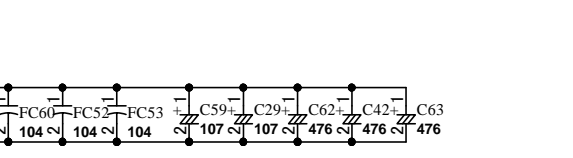
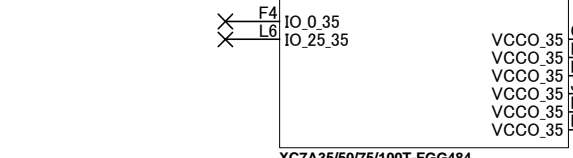
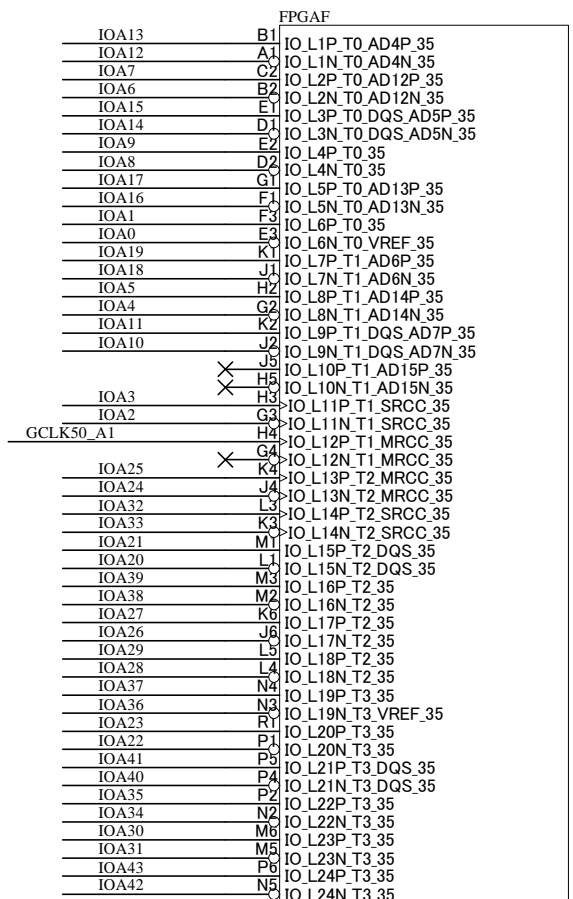
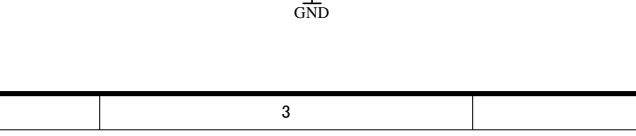
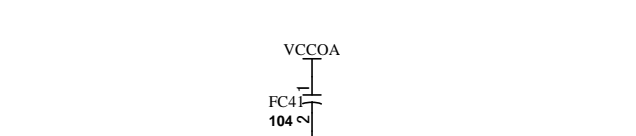
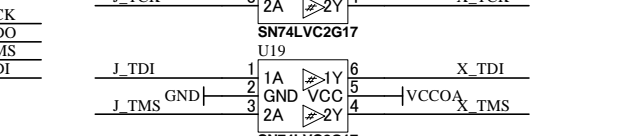
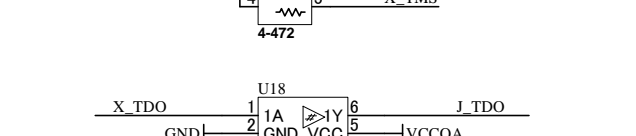
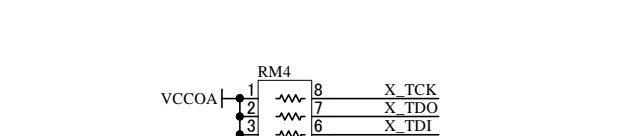
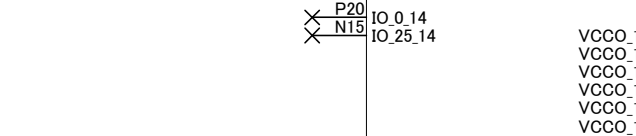
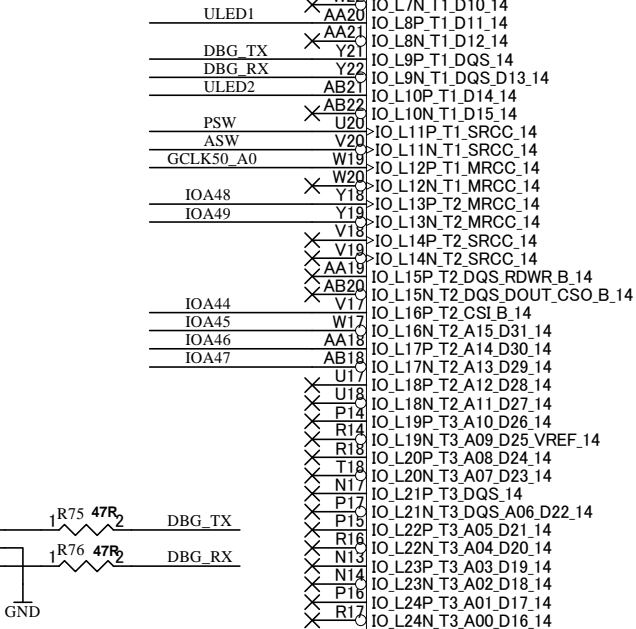
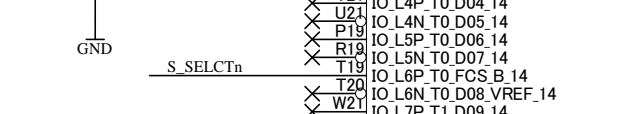
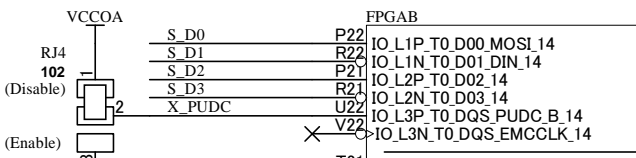
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VIO(B)	1	VIO(B)
5V	2	GND
5V	3	GND
GND	4	GND
GND	5	GND
IOB0	6	GND
IOB1	7	GND
IOB2	8	GND
IOB3	9	GND
IOB4	10	GND
IOB5	11	GND
IOB6	12	GND
IOB7	13	GND
IOB8	14	GND
IOB9	15	GND
IOB10	16	GND
IOB11	17	GND
IOB12	18	GND
IOB13	19	GND
IOB14	20	GND
IOB15	21	GND
GND	22	GND
GND	23	GND
IOB16	24	GND
IOB17	25	GND
IOB18	26	GND
IOB19	27	GND
IOB20	28	GND
IOB21	29	GND
IOB22	30	GND
IOB23	31	GND
GND	32	GND
GND	33	GND
IOB24	34	GND
IOB25	35	GND
IOB26	36	GND
IOB27	37	GND
IOB28	38	GND
IOB29	39	GND
IOB30	40	GND
IOB31	41	GND
GND	42	GND
GND	43	GND
GND	44	GND
GND	45	GND
GND	46	GND
IOB32	47	GND
IOB33	48	GND
IOB34	49	GND
IOB35	50	GND
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IOB42	59	GND
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IOB44	61	GND
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IOB47	64	GND
IOB48	65	GND
IOB49	66	GND



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	DOC. No. XCM-023W	B
Date: 2014/10/27	10:10:59	File: XCM023B.sch
Sheet 1 of 6		

V33A	V33A
VIO(B)	VIO(B)
VCCINT_10	VCCINT_10
VMGTVCC_10	VMGTVCC_10
VMGTAVTT_12	VMGTAVTT_12
VCCAUX_18	VCCAUX_18
VMGTCLK	VMGTCLK
VCCOA	VCCOA
VCCOB	VCCOB
VDDR	VDDR
GND	GND



HUMAN DATA

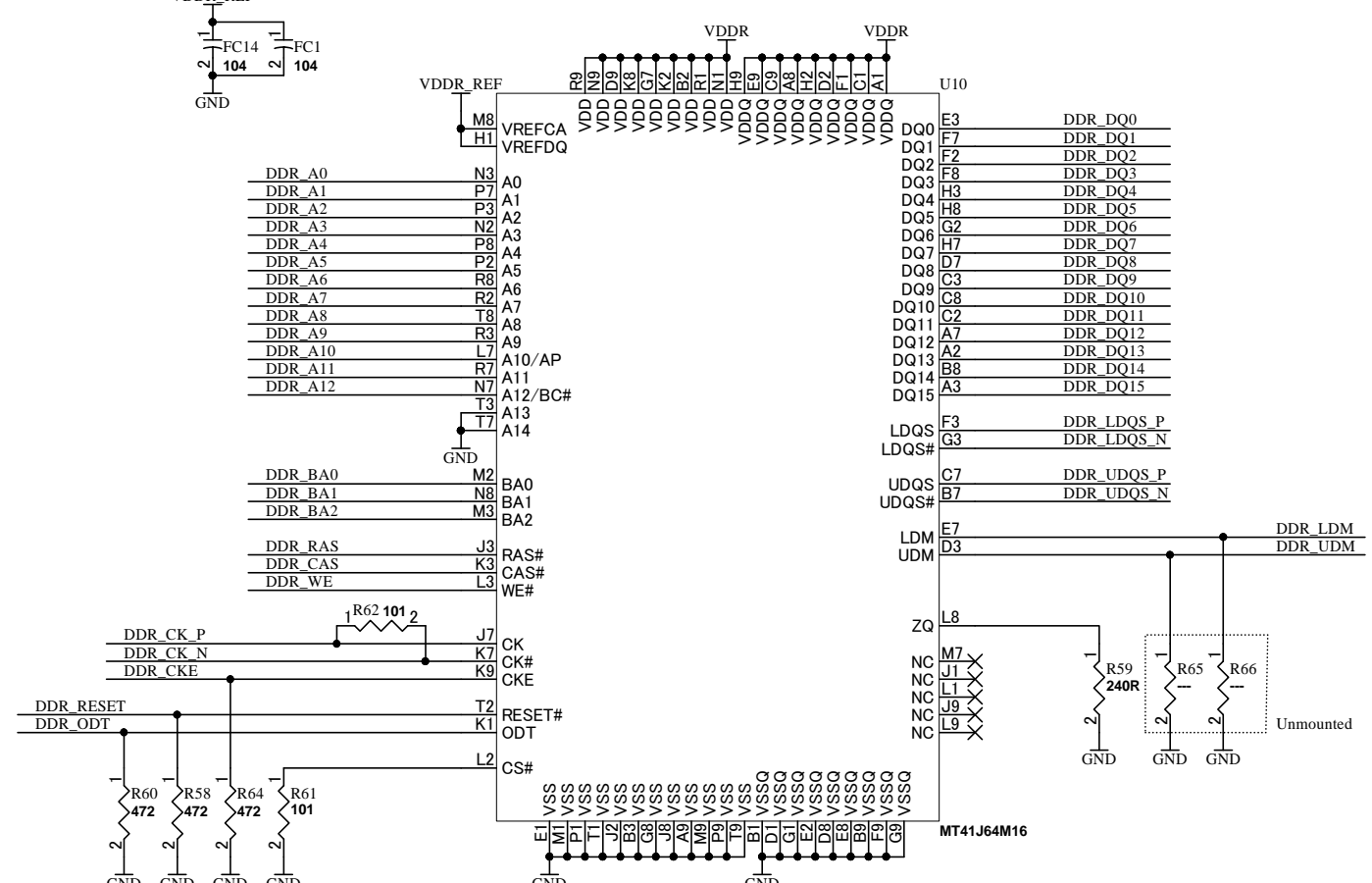
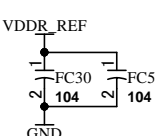
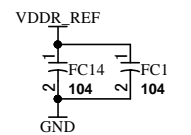
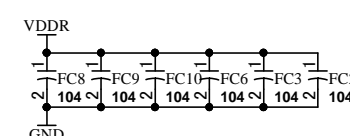
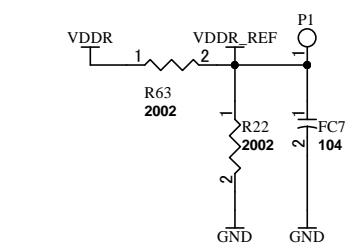
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ARTIX-7 FGG484 FPGA Board

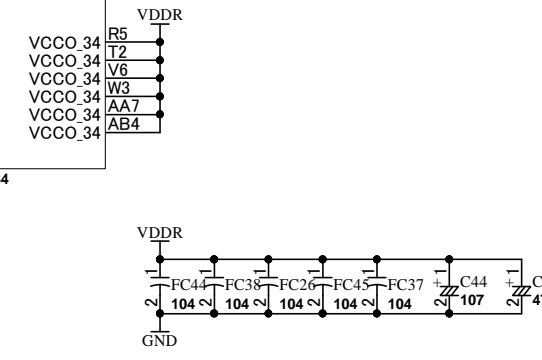
DOC. No. XCM-023W B

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V33A	V33A
VIO(B)	VIO(B)
VCCINT_10	VCCINT_10
VMGTVCC_10	VMGTVCC_10
VMGTAVTT_12	VMGTAVTT_12
VCCAUX_18	VCCAUX_18
VMGTCLK	VMGTCLK
VCCOA	VCCOA
VCCOB	VCCOB
VDDR	VDDR
GND	GND



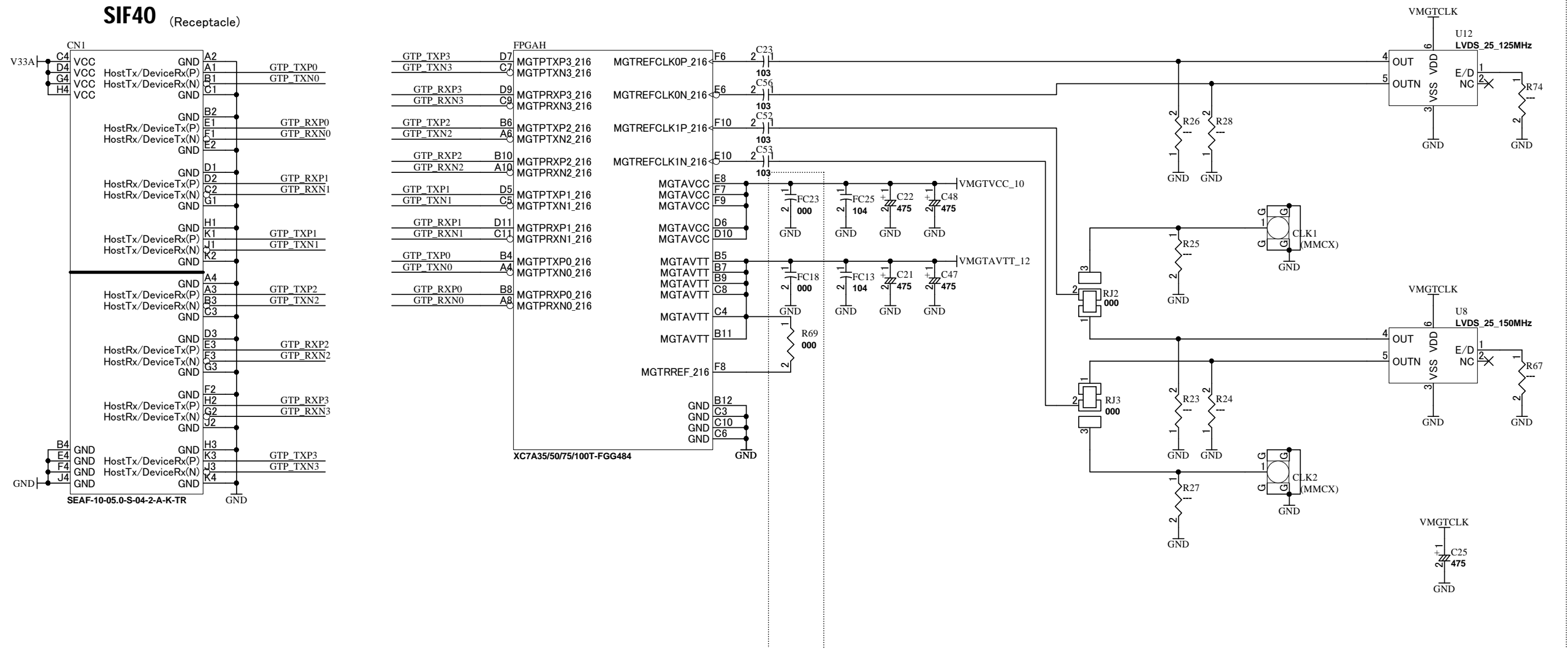
FPGA	
DDR_DQ6	T1
DDR_DQ4	U1
DDR_DQ0	U2
DDR_DQ7	V2
DDR_LDQS_P	R3
DDR_LDQS_N	R2
DDR_DQ1	W2
DDR_LDM	Y2
DDR_DQ5	W1
DDR_DQ3	Y1
DDR_DQ2	U3
VDDR_REF	V3
DDR_DQ11	AA1
DDR_DQ9	AB1
DDR_DQ8	AB3
DDR_UDM	AB2
DDR_UDQS_P	Y3
DDR_UDQS_N	AA3
DDR_DQ14	AA5
DDR_DQ10	AB5
DDR_DQ13	Y4
DDR_DQ12	AA4
DDR_DQ15	V4
DDR_RESET	W4
DDR_CKE	R4
DDR_CAS	T4
DDR_A3	T5
DDR_BA2	U5
DDR_ODT	W6
DDR_WE	W5
DDR_A5	U6
DDR_BA0	V5
DDR_A7	R6
DDR_A2	T6
DDR_A10	Y6
DDR_A6	AA6
DDR_A12	V7
VDDR_REF	W7
DDR_A11	AB7
DDR_A8	AB6
DDR_CK_P	V9
DDR_CK_N	V8
DDR_A1	AA8
DDR_BA1	AB8
DDR_RAS	Y8
DDR_A4	Y7
DDR_A0	W9
DDR_A9	V9
IO_0_34	T3
IO_25_34	U7



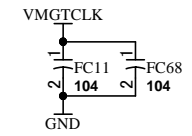
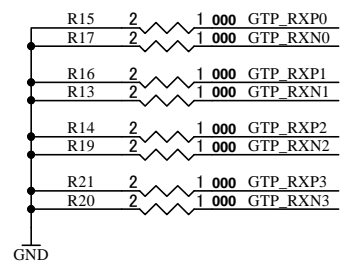
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VDDR	R5
VCCO_34	T2
VCCO_34	V6
VCCO_34	W3
VCCO_34	AA7
VCCO_34	AB4

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V33A	V33A
VIO(B)	VIO(B)
VCCINT_10	VCCINT_10
VMGTAVCC_10	VMGTAVCC_10
VCCINT_12	VCCINT_12
VCCINT_18	VCCINT_18
VCCINT_18	VCCINT_18
VMGTCLK	VMGTCLK
VCCOA	VCCOA
VCCOB	VCCOB
VDDR	VDDR
GND	GND



Unmounted



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	ARTIX-7 FGG484 FPGA Board	
	DOC. No. XCM-023W	B