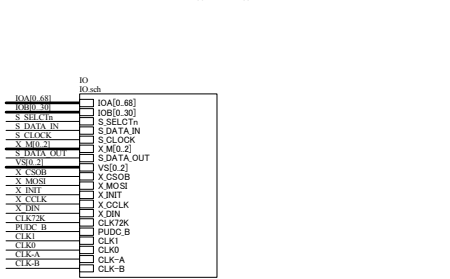
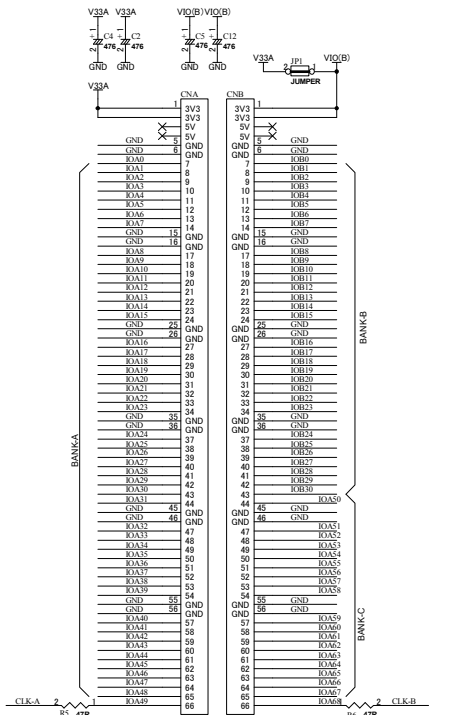
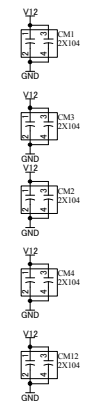
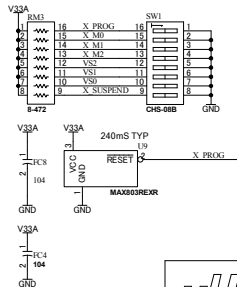
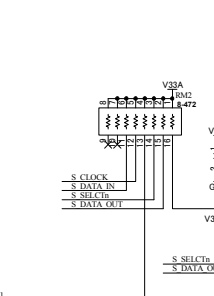


[xxx] is FPGA PIN NAME of 700/1400

FPGA	VCC(AUX/VCC(AUX))	E11	V(AUX)
A1	GND	F5	
A16	GND	F6	
A31	GND	F7	
A47	GND	F8	
A62	GND	F9	
A77	GND	F10	
A92	GND	F11	
A107	GND	F12	
A122	GND	F13	
A137	GND	F14	
A152	GND	F15	
A167	GND	F16	
A182	GND	F17	
A197	GND	F18	
A212	GND	F19	
A227	GND	F20	
A242	GND	F21	
A257	GND	F22	
A272	GND	F23	
A287	GND	F24	
A302	GND	F25	
A317	GND	F26	
A332	GND	F27	
A347	GND	F28	
A362	GND	F29	
A377	GND	F30	
A392	GND	F31	
A407	GND	F32	
A422	GND	F33	
A437	GND	F34	
A452	GND	F35	
A467	GND	F36	
A482	GND	F37	
A497	GND	F38	
A512	GND	F39	
A527	GND	F40	
A542	GND	F41	
A557	GND	F42	
A572	GND	F43	
A587	GND	F44	
A602	GND	F45	
A617	GND	F46	
A632	GND	F47	
A647	GND	F48	
A662	GND	F49	
A677	GND	F50	
A692	GND	F51	
A707	GND	F52	
A722	GND	F53	
A737	GND	F54	
A752	GND	F55	
A767	GND	F56	
A782	GND	F57	
A797	GND	F58	
A812	GND	F59	
A827	GND	F60	
A842	GND	F61	
A857	GND	F62	
A872	GND	F63	
A887	GND	F64	
A902	GND	F65	
A917	GND	F66	
A932	GND	F67	
A947	GND	F68	
A962	GND	F69	
A977	GND	F70	
A992	GND	F71	
A1007	GND	F72	
A1022	GND	F73	
A1037	GND	F74	
A1052	GND	F75	
A1067	GND	F76	
A1082	GND	F77	
A1097	GND	F78	
A1112	GND	F79	
A1127	GND	F80	
A1142	GND	F81	
A1157	GND	F82	
A1172	GND	F83	
A1187	GND	F84	
A1202	GND	F85	
A1217	GND	F86	
A1232	GND	F87	
A1247	GND	F88	
A1262	GND	F89	
A1277	GND	F90	
A1292	GND	F91	
A1307	GND	F92	
A1322	GND	F93	
A1337	GND	F94	
A1352	GND	F95	
A1367	GND	F96	
A1382	GND	F97	
A1397	GND	F98	
A1412	GND	F99	
A1427	GND	F100	

FPGA	SUSPEND[SUSPEND]	R16	
X SUSPEND	SUSPEND[SUSPEND]	R16	
X DONE	DONE[DONE]	T12	
X PROG	PROG_B[PROG_B]	A2	
X TDI	TDI[TDI]	B1	
X TDO	TDO[TDO]	B16	
X TCK	TCK[TCK]	A15	
X TMS	TMS[TMS]	B2	



IO	R3	IO
IOA0[0..68]		
IOB0[0..30]		
S_SELECTn		
S_DATA_IN		
S_CLOCK		
S_DATA_OUT		
VSS0[0..1]		
X_CS0B		
X_MENI		
X_CCLK		
X_TDI		
CLK2K		
RUDC_B		
CLK0		
CLKA		
CLK-B		

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