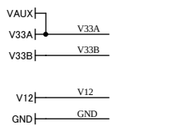

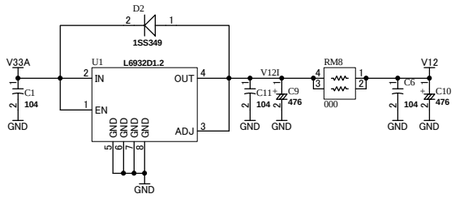

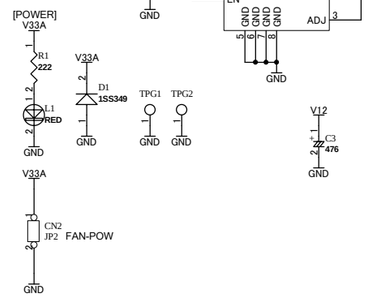

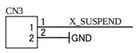

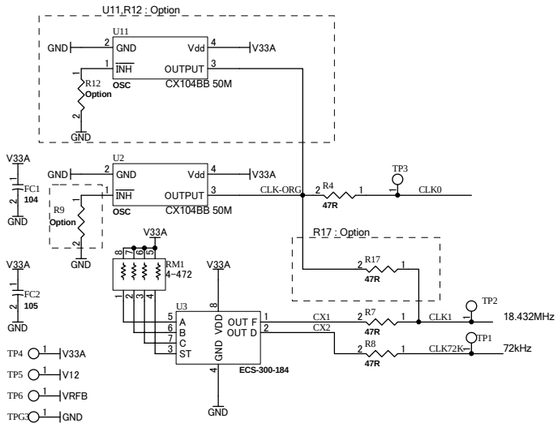

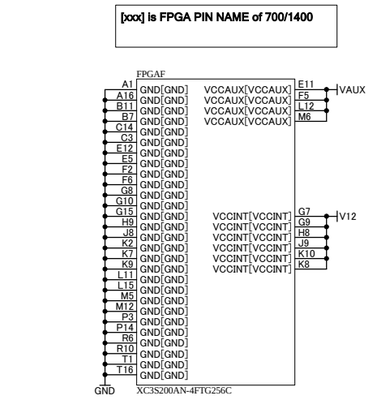

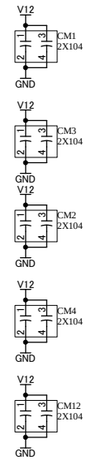

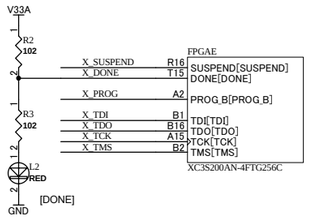

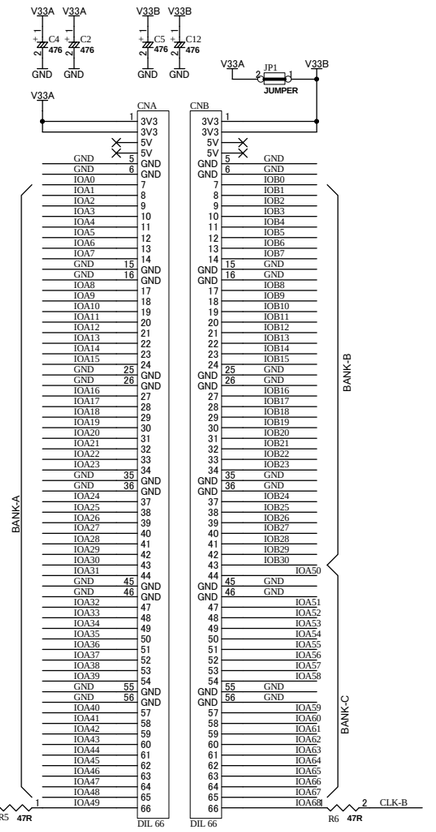

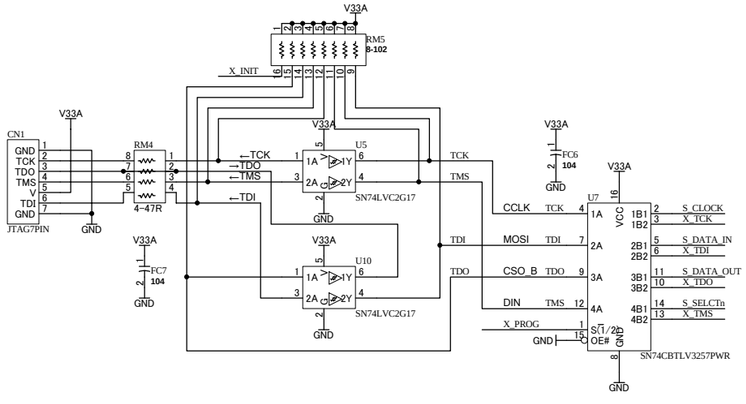

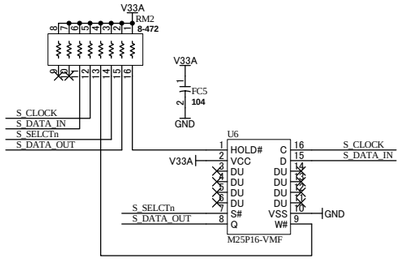

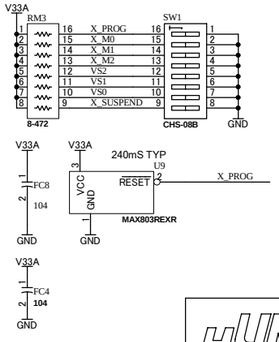

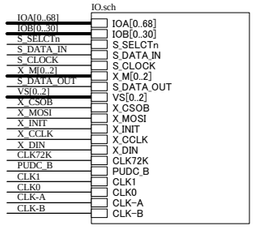

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[xxx] is FPGA PIN NAME of 700/1400

VREFB : for 700 or higher device.
GND : for 400 or lower device.

FRAM使用時の注意

	400使用時FPGAPIN
FRAM_A8	J4
FRAM_A11	G4

	700/1400使用時FPGAPIN
FRAM_A8	E4
FRAM_A11	F4

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