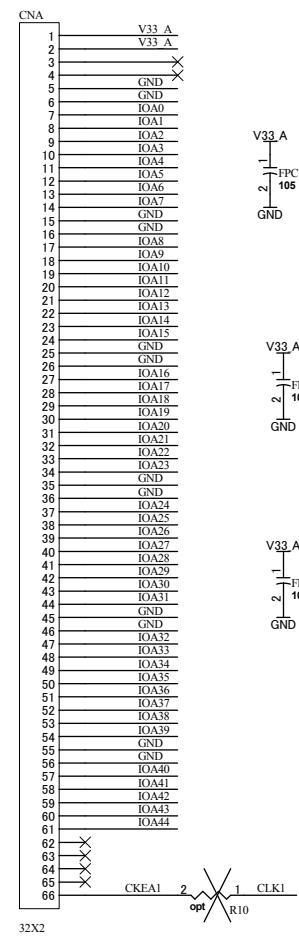
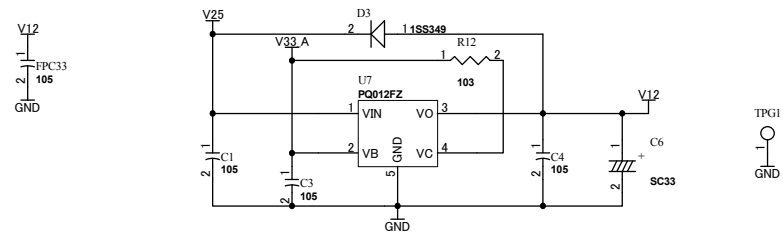
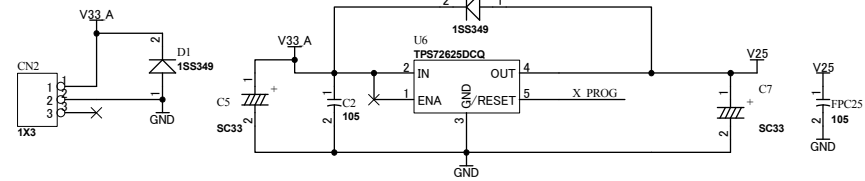
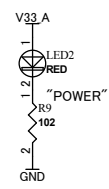
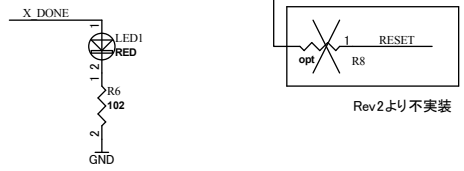
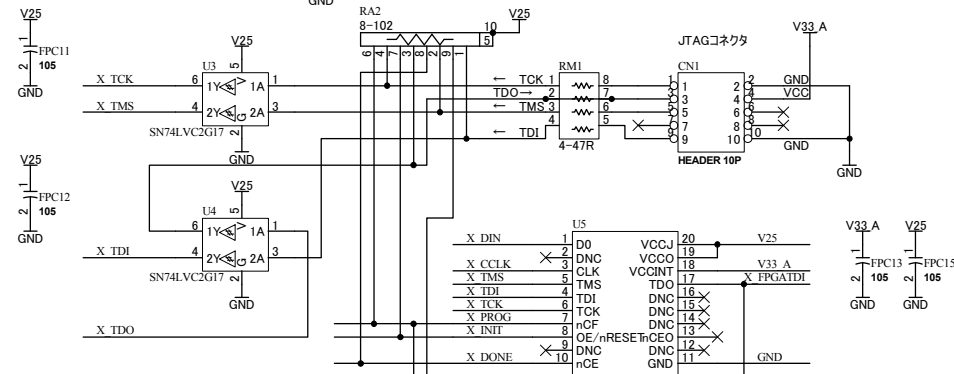
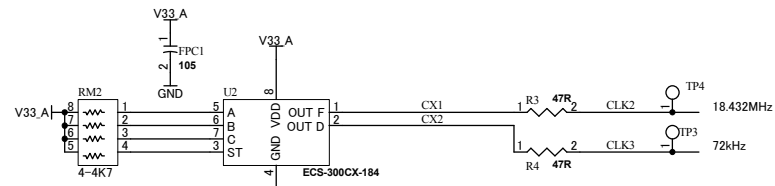
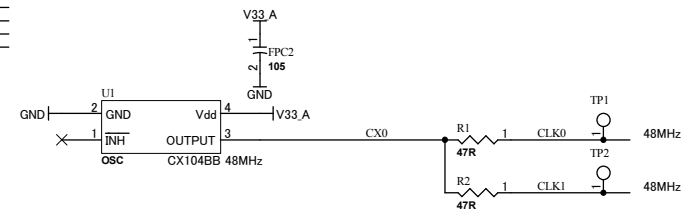
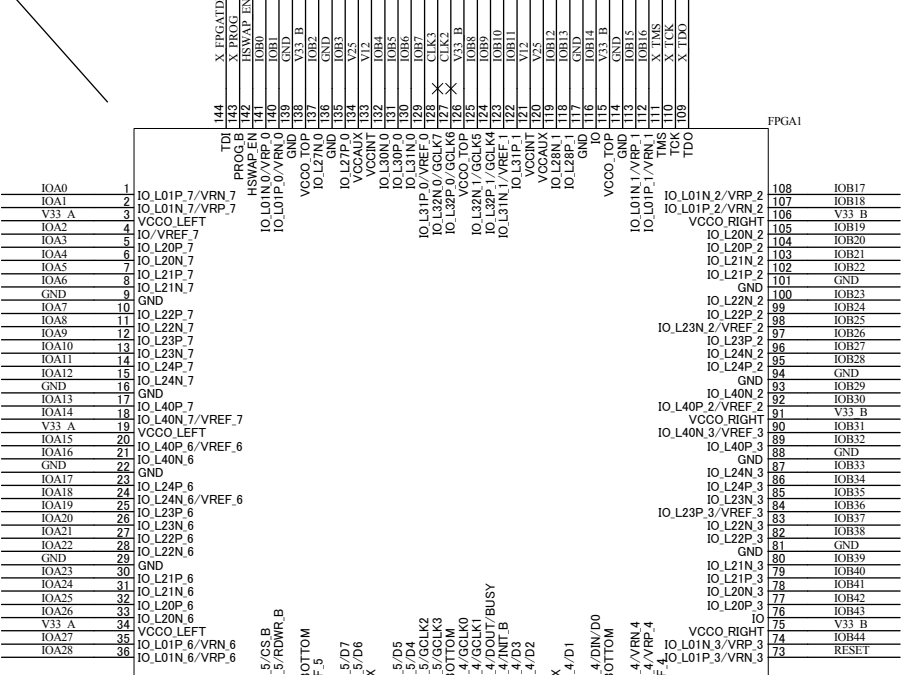
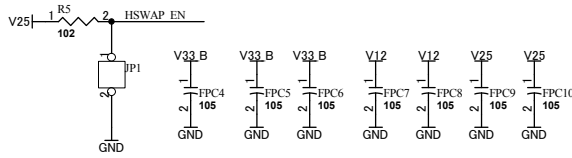


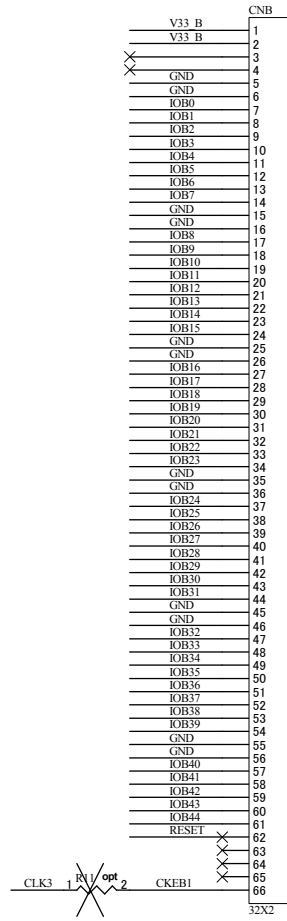
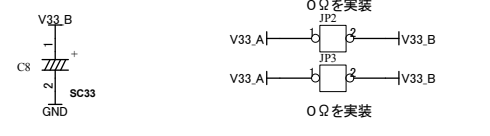
V12  
V25  
V33\_A  
V50  
GND



BANKA  
BANKB



BANKA  
BANKB



Rev2

		2007/12/14	
		SP3 TQ144 FPGA BOARD	
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