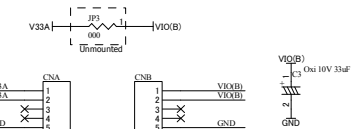


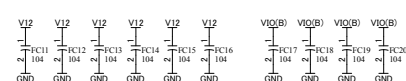
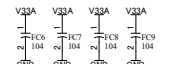
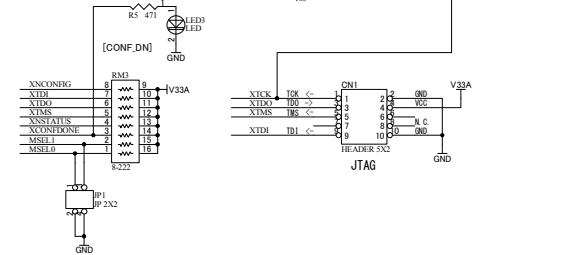
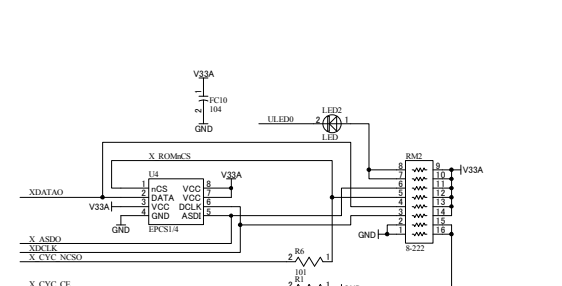
Pin	Signal	Notes
1	X_CYC_NCS0	
2	IO/ASD0	
3	IO/ASD0	
4	IO/LVDS9p/CRC_ERROR(LVDS15p/CRC_ERROR)	
5	IO/LVDS9n/CLKUSK_LVDS15n/CLKUSP	
6	VCCIO1	
7	GND	
8	IO/VREFB1N0	
9	IO/LVDS9p/IO/LVDS8p	
10	IO/LVDS9n/IO/LVDS8n	
11	XTD0	
12	XTMS	
13	XTCK	
14	XTDF	
15	XTD1	
16	DATA0	
17	DCLK	
18	X_CYC_CE	
19	CLK0/LVDSCLK0p/input(3)	
20	CLK1/LVDSCLK0n/input(3)	
21	XNCONF0	
22	XNCONF0	
23	CLK2/LVDSCLK1p/input(3)	
24	CLK3/LVDSCLK1n/input(3)	
25	VCCIO1	
26	IO/LVDS4p/IO/LVDS7p	
27	IO/LVDS4n/IO/LVDS7n	
28	IO/LVDS3p/VCCINT	
29	IO/LVDS3n(GND)	
30	IO/VREFB1N1	
31	VCCIO1	
32	IO	
33	IO/PLL1_OUTp	
34	IO/PLL1_OUTn	
35	GND	
36	GND/PLL1	
37	VCCA_PLL1	
38	GND/PLL1	
39	VCCA_PLL1	
40	GND/PLL1	
41	VCCA_PLL1	
42	IO/LVDS5p/DEV_OE(LVDS7p/DEV_OE)	
43	IO/LVDS5n(LVDS7n/C42)	
44	IO/LVDS7p(LVDS7p)	
45	IO/LVDS7n(LVDS7n)	
46	IO/LVDS5p(LVDS5p)	
47	IO/LVDS5n(LVDS5n)	
48	VCCIO4	
49	IO/LVDS55(LVDS74p)	
50	IO/LVDS55n(LVDS74n)	
51	VCCINT	
52	VCCIO3	
53	IO/VREFB4N1	
54	IO/LVDS46(LVDS70p)	
55	IO/LVDS33p(LVDS88p)	
56	VCCIO4	
57	IO/LVDS52p(LVDS97p)	
58	IO/LVDS52n(LVDS97n)	
59	IO/LVDS51p(LVDS96p)	
60	IO/LVDS51n(LVDS96n)	
61	GND	
62	VCCINT	
63	CONF_DONE	
64	IO/VREFB4N0	
65	IO/LVDS46(LVDS96p)	
66	IO/LVDS46n(LVDS96n)	
67	IO/LVDS45n(LVDS59n)	
68	GND	
69	IO/LVDS44(LVDS58p)	
70	IO/LVDS44n(LVDS58n)	
71	IO/LVDS43p(LVDS53p)	
72	IO/LVDS43n(LVDS53n)	

Pin	Signal	Notes
144	IO/LVDS10n(LVDS16n)	
145	IO/LVDS10p(LVDS16p)	
146	IO/LVDS17n/DEV_CLPn/DEV_CLRp	
147	IO/LVDS17p/DEV_CLPp/DEV_CLRp	
148	IO/LVDS17n(LVDS17n)	
149	IO/LVDS17p(LVDS17p)	
150	VCCIO2	
151	IO/LVDS12p(LVDS18p)	
152	IO/LVDS12n(LVDS18n)	
153	IO/LVDS13p(LVDS19p)	
154	IO/LVDS13n(LVDS19n)	
155	IO/LVDS17p(LVDS23p)	
156	IO/LVDS17n(LVDS23n)	
157	IO/VREFB2N1	
158	VCCINT	
159	GND	
160	IO/LVDS20p	
161	IO/LVDS20n	
162	VCCIO2	
163	IO/LVDS21p(LVDS29p)	
164	IO/LVDS21n(LVDS29n)	
165	VCCINT	
166	GND	
167	IO/LVDS24p(LVDS33p)	
168	IO/LVDS24n(LVDS33n)	
169	IO/VREFB2N0	
170	IO/LVDS25p(LVDS34p)	
171	IO/LVDS25n(LVDS34n)	
172	GND	
173	VCCIO2	
174	IO/LVDS27p(LVDS39p)	
175	IO/LVDS27n(LVDS39n)	
176	IO/LVDS28p(LVDS37p)	
177	IO/LVDS28n(LVDS37n)	
178	GND	
179	GND/PLL2	
180	VCCA_PLL2	
181	GND/PLL2	
182	VCCA_PLL2	
183	GND/PLL2	
184	VCCA_PLL2	
185	IO/PLL2_OUTp	
186	IO/PLL2_OUTn	
187	VCCIO3	
188	IO/LVDS30p(LVDS39p)	
189	IO/LVDS30n(LVDS39n)	
190	IO/VREFB3N0	
191	GND	
192	IO/LVDS33n(LVDS45n)	
193	IO/LVDS33p(LVDS45p)	
194	IO/LVDS34p(LVDS46p)	
195	IO/LVDS34n(LVDS46n)	
196	IO/LVDS34p(LVDS46p)	
197	IO/LVDS34n(LVDS46n)	
198	IO/LVDS35p(LVDS47p)	
199	IO/LVDS35n(LVDS47n)	
200	CLK4/LVDSCLK2p/input(3)	
201	CLK5/LVDSCLK2n/input(3)	
202	CLK6/LVDSCLK3p/input(3)	
203	CLK7/LVDSCLK3n/input(3)	
204	IO/LVDS38p(LVDS48p)	
205	IO/LVDS38n(LVDS48n)	
206	MSEL0	
207	MSEL1	
208	MSEL1	
209	CONF_DONE	
210	XNCONF0	
211	nSTATUS	
212	IO/LVDS37n/VCCINT	
213	IO/LVDS37n(GND)	
214	IO/VREFB1N1	
215	GND	
216	VCCIO3	
217	IO/LVDS41p/CEO(LVDS46n/CEO)	
218	IO/LVDS41n/INIT_DONE(LVDS47n/INIT_DONE)	
219	IO/LVDS42p(LVDS56p)	
220	IO/LVDS42n(LVDS56n)	

(NOTICE)
 (*) For EPFCS, these pins are connected to power.
 Please set them as input.



Pin	Signal	Notes
1	V33A	
2	V33A	
3	GND	
4	GND	
5	GND	
6	GND	
7	IOA1	
8	IOA2	
9	IOA3	
10	IOA4	
11	IOA5	
12	IOA6	
13	IOA7	
14	GND	
15	IOA8	
16	IOA9	
17	IOA10	
18	IOA11	
19	IOA12	
20	IOA13	
21	IOA14	
22	IOA15	
23	GND	
24	IOA16	
25	GND	
26	IOA17	
27	IOA18	
28	IOA19	
29	IOA20	
30	IOA21	
31	IOA22	
32	IOA23	
33	IOA24	
34	GND	
35	GND	
36	IOA25	
37	IOA26	
38	IOA27	
39	IOA28	
40	IOA29	



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DSM TITLE
 Cyclone II Bread Board (TQ144)

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