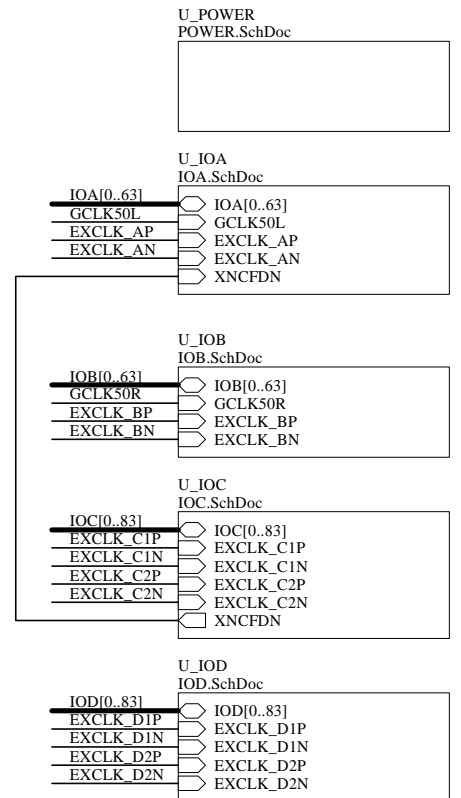
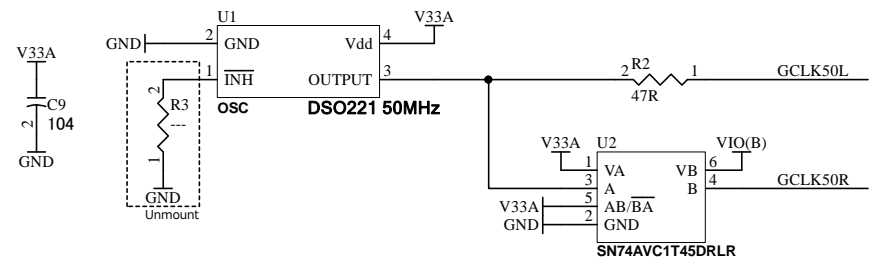


CNA		GND		V33A	
1	VIO	2	VIO	3	V33A
3	VIO	4	VIO	4	V33A
5	5V	6	5V	5	
7	5V	8	5V	7	
9	RSV	10	RSV	9	
11	CK	12	CK	11	
13	RSV	14	RSV	13	
15	RSV	16	RSV	15	
17	IOA0	18	IOA32	17	
19	IOA1	20	IOA33	19	
21	IOA2	22	IOA34	21	
23	IOA3	24	IOA35	23	
25	IOA4	26	IOA36	25	
27	IOA5	28	IOA37	27	
29	IOA6	30	IOA38	29	
31	IOA7	32	IOA39	31	
33	IOA8	34	IOA40	33	
35	IOA9	36	IOA41	35	
37	IOA10	38	IOA42	37	
39	IOA11	40	IOA43	39	
41	IOA12	42	IOA44	41	
43	IOA13	44	IOA45	43	
45	IOA14	46	IOA46	45	
47	IOA15	48	IOA47	47	
49	IOA16	50	IOA48	49	
51	IOA17	52	IOA49	51	
53	IOA18	54	IOA50	53	
55	IOA19	56	IOA51	55	
57	IOA20	58	IOA52	57	
59	IOA21	60	IOA53	59	
61	IOA22	62	IOA54	61	
63	IOA23	64	IOA55	63	
65	IOA24	66	IOA56	65	
67	IOA25	68	IOA57	67	
69	IOA26	70	IOA58	69	
71	IOA27	72	IOA59	71	
73	IOA28	74	IOA60	73	
75	IOA29	76	IOA61	75	
77	IOA30	78	IOA62	77	
79	IOA31	80	IOA63	79	

CNB		GND		VIO(B)	
1	VIO(B)	2	VIO(B)	3	VIO(B)
3	VIO(B)	4	VIO(B)	4	VIO(B)
5	5V	6	5V	5	
7	5V	8	5V	7	
9	RSV	10	RSV	9	
11	CK	12	CK	11	
13	RSV	14	RSV	13	
15	RSV	16	RSV	15	
17	IOB0	18	IOB32	17	
19	IOB1	20	IOB33	19	
21	IOB2	22	IOB34	21	
23	IOB3	24	IOB35	23	
25	IOB4	26	IOB36	25	
27	IOB5	28	IOB37	27	
29	IOB6	30	IOB38	29	
31	IOB7	32	IOB39	31	
33	IOB8	34	IOB40	33	
35	IOB9	36	IOB41	35	
37	IOB10	38	IOB42	37	
39	IOB11	40	IOB43	39	
41	IOB12	42	IOB44	41	
43	IOB13	44	IOB45	43	
45	IOB14	46	IOB46	45	
47	IOB15	48	IOB47	47	
49	IOB16	50	IOB48	49	
51	IOB17	52	IOB49	51	
53	IOB18	54	IOB50	53	
55	IOB19	56	IOB51	55	
57	IOB20	58	IOB52	57	
59	IOB21	60	IOB53	59	
61	IOB22	62	IOB54	61	
63	IOB23	64	IOB55	63	
65	IOB24	66	IOB56	65	
67	IOB25	68	IOB57	67	
69	IOB26	70	IOB58	69	
71	IOB27	72	IOB59	71	
73	IOB28	74	IOB60	73	
75	IOB29	76	IOB61	75	
77	IOB30	78	IOB62	77	
79	IOB31	80	IOB63	79	

CNC		GND		VIO(C)	
1	VIO(C)	2	VIO(C)	3	VIO(C)
3	VIO(C)	4	VIO(C)	4	VIO(C)
5	5V	6	5V	5	
7	5V	8	5V	7	
9	RSV	10	RSV	9	
11	CK	12	CK	11	
13	CK	14	CK	13	
15	RSV	16	RSV	15	
17	IOC0	18	IOC42	17	
19	IOC1	20	IOC43	19	
21	IOC2	22	IOC44	21	
23	IOC3	24	IOC45	23	
25	IOC4	26	IOC46	25	
27	IOC5	28	IOC47	27	
29	IOC6	30	IOC48	29	
31	IOC7	32	IOC49	31	
33	IOC8	34	IOC50	33	
35	IOC9	36	IOC51	35	
37	IOC10	38	IOC52	37	
39	IOC11	40	IOC53	39	
41	IOC12	42	IOC54	41	
43	IOC13	44	IOC55	43	
45	IOC14	46	IOC56	45	
47	IOC15	48	IOC57	47	
49	IOC16	50	IOC58	49	
51	IOC17	52	IOC59	51	
53	IOC18	54	IOC60	53	
55	IOC19	56	IOC61	55	
57	IOC20	58	IOC62	57	
59	IOC21	60	IOC63	59	
61	IOC22	62	IOC64	61	
63	IOC23	64	IOC65	63	
65	IOC24	66	IOC66	65	
67	IOC25	68	IOC67	67	
69	IOC26	70	IOC68	69	
71	IOC27	72	IOC69	71	
73	IOC28	74	IOC70	73	
75	IOC29	76	IOC71	75	
77	IOC30	78	IOC72	77	
79	IOC31	80	IOC73	79	
81	IOC32	82	IOC74	81	
83	IOC33	84	IOC75	83	
85	IOC34	86	IOC76	85	
87	IOC35	88	IOC77	87	
89	IOC36	90	IOC78	89	
91	IOC37	92	IOC79	91	
93	IOC38	94	IOC80	93	
95	IOC39	96	IOC81	95	
97	IOC40	98	IOC82	97	
99	IOC41	100	IOC83	99	

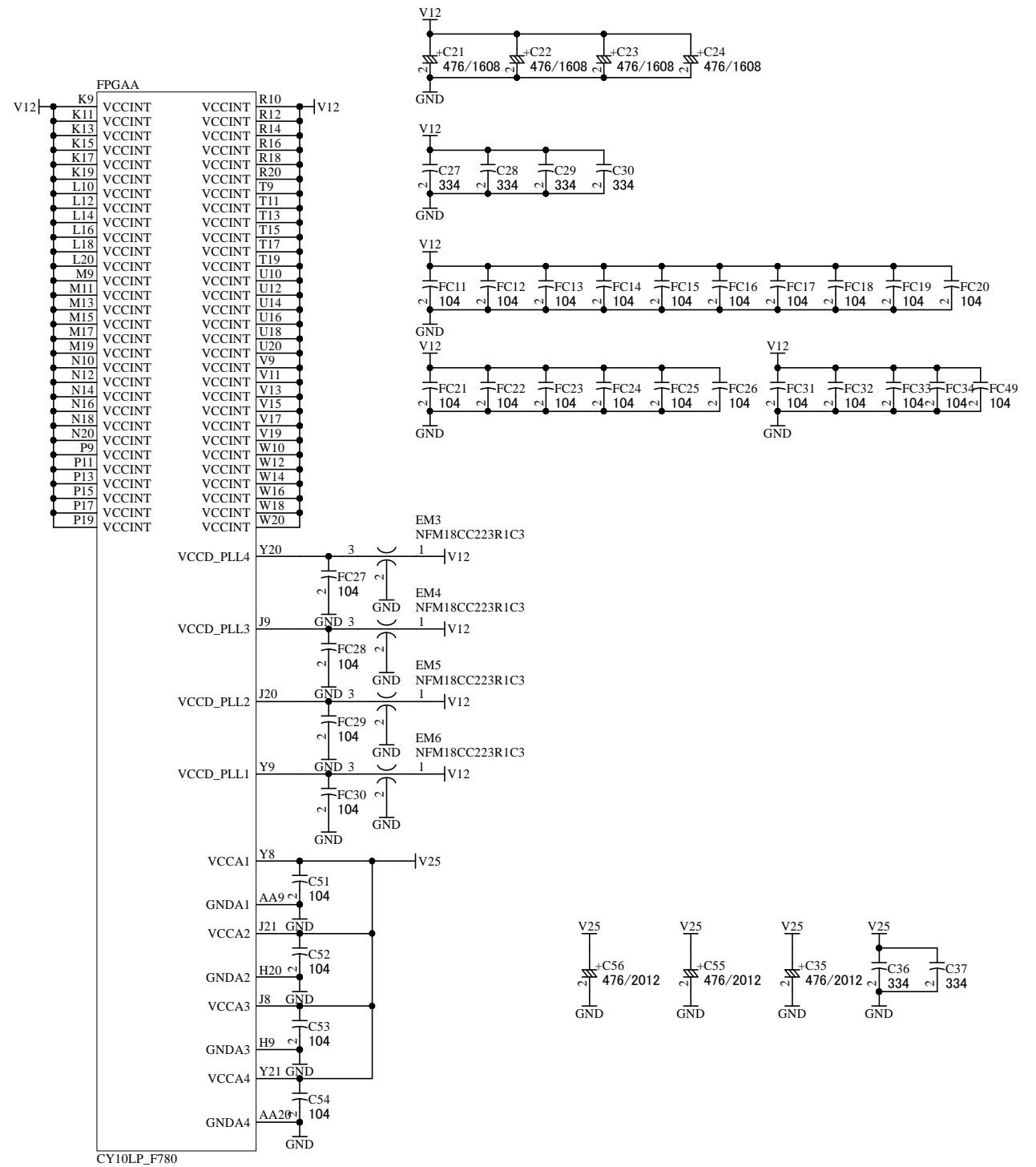
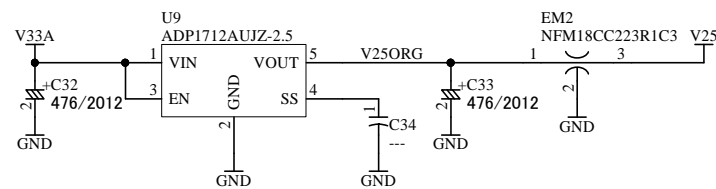
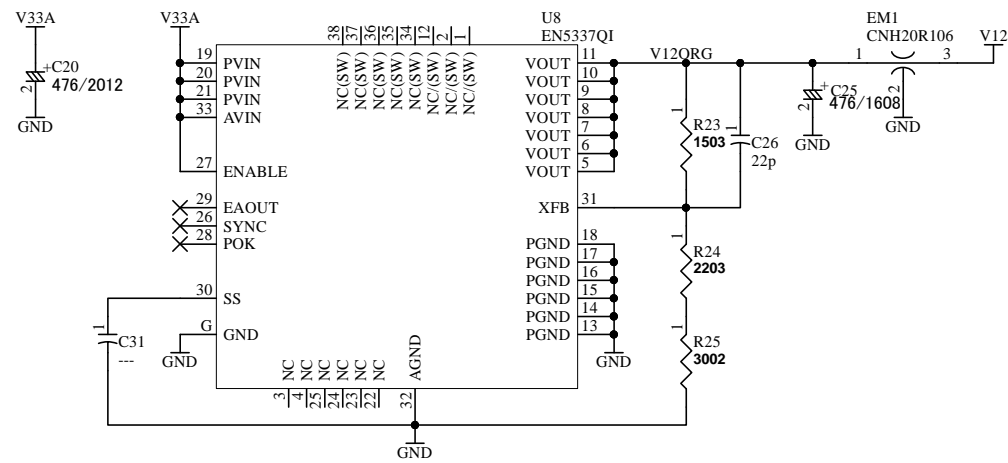
CND		GND		VIO(D)	
1	VIO(D)	2	VIO(D)	3	VIO(D)
3	VIO(D)	4	VIO(D)	4	VIO(D)
5	5V	6	5V	5	
7	5V	8	5V	7	
9	RSV	10	RSV	9	
11	CK	12	CK	11	
13	CK	14	CK	13	
15	RSV	16	RSV	15	
17	IOD0	18	IOD42	17	
19	IOD1	20	IOD43	19	
21	IOD2	22	IOD44	21	
23	IOD3	24	IOD45	23	
25	IOD4	26	IOD46	25	
27	IOD5	28	IOD47	27	
29	IOD6	30	IOD48	29	
31	IOD7	32	IOD49	31	
33	IOD8	34	IOD50	33	
35	IOD9	36	IOD51	35	
37	IOD10	38	IOD52	37	
39	IOD11	40	IOD53	39	
41	IOD12	42	IOD54	41	
43	IOD13	44	IOD55	43	
45	IOD14	46	IOD56	45	
47	IOD15	48	IOD57	47	
49	IOD16	50	IOD58	49	
51	IOD17	52	IOD59	51	
53	IOD18	54	IOD60	53	
55	IOD19	56	IOD61	55	
57	IOD20	58	IOD62	57	
59	IOD21	60	IOD63	59	
61	IOD22	62	IOD64	61	
63	IOD23	64	IOD65	63	
65	IOD24	66	IOD66	65	
67	IOD25	68	IOD67	67	
69	IOD26	70	IOD68	69	
71	IOD27	72	IOD69	71	
73	IOD28	74	IOD70	73	
75	IOD29	76	IOD71	75	
77	IOD30	78	IOD72	77	
79	IOD31	80	IOD73	79	
81	IOD32	82	IOD74	81	
83	IOD33	84	IOD75	83	
85	IOD34	86	IOD76	85	
87	IOD35	88	IOD77	87	
89	IOD36	90	IOD78	89	
91	IOD37	92	IOD79	91	
93	IOD38	94	IOD80	93	
95	IOD39	96	IOD81	95	
97	IOD40	98	IOD82	97	
99	IOD41	100	IOD83	99	



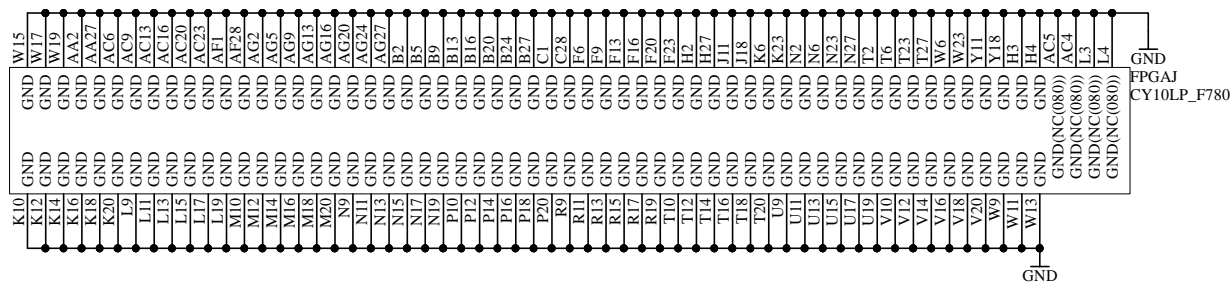
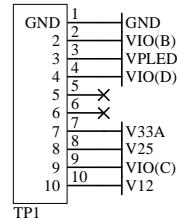
ACM208R1-SCH-B.pdf



DSN:	TITLE: Intel Cyclone 10 LP F484 FPGA Board
DOC. No:	ACM-208
FILE: ACM208.SchDoc	DATE: 2018/04/03 16:53:27
Sheet:	B / 7



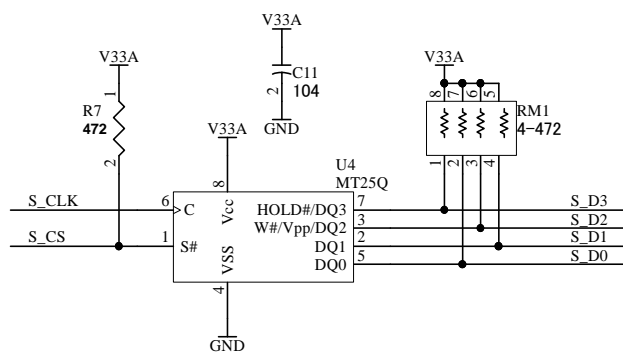
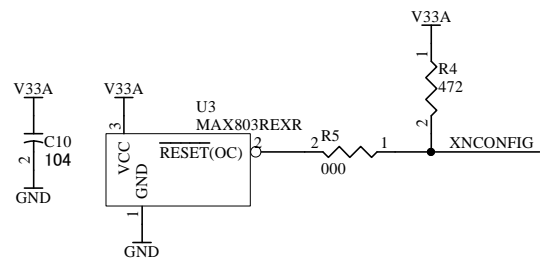
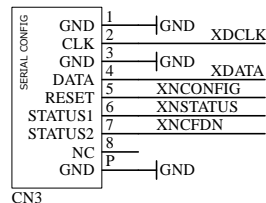
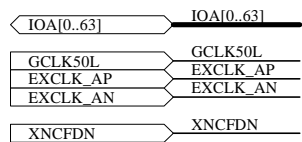
Power sense point



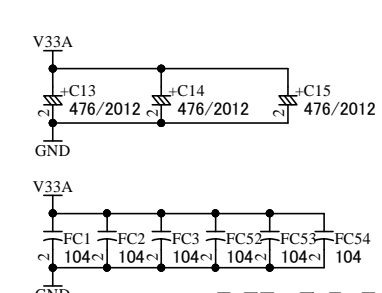
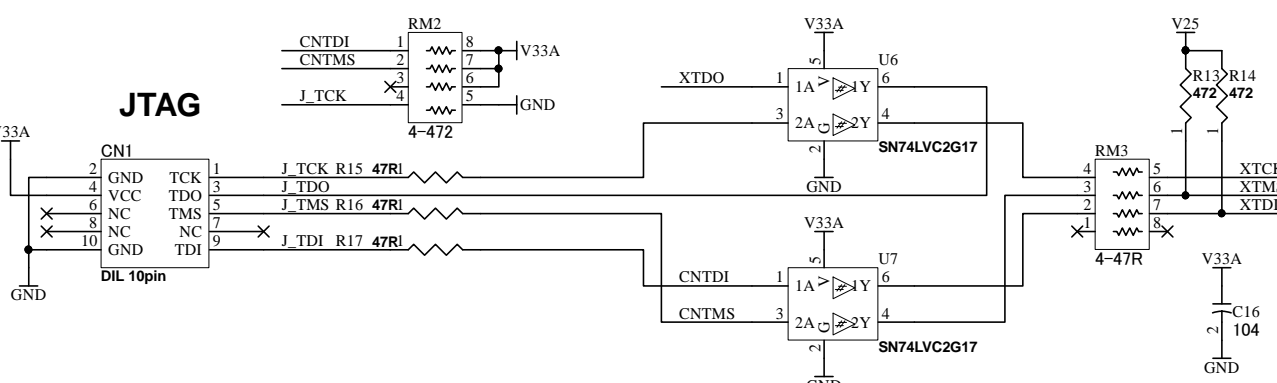
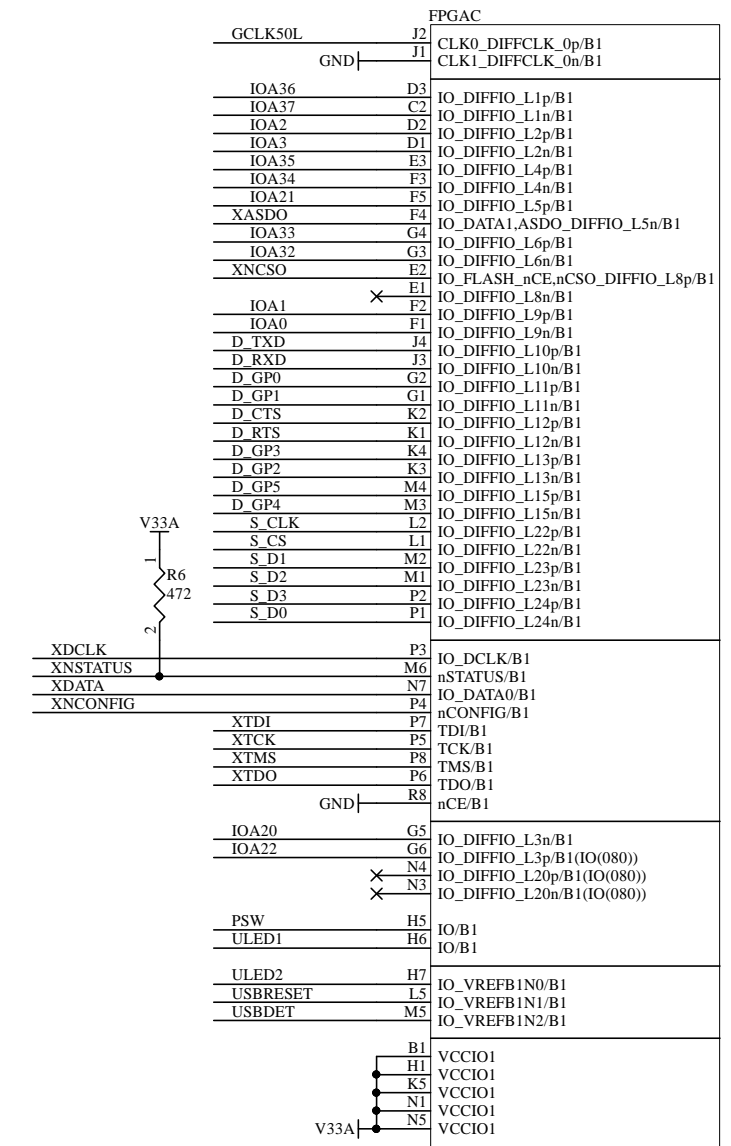
ACM208R1-SCH-B.pdf

HUMAN DATA
HuMANDATA LTD.
www.hdl.co.jp

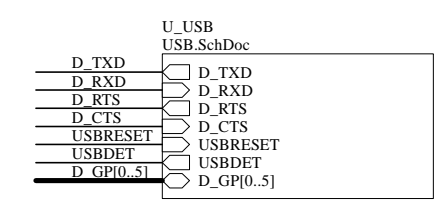
DSN:	TITLE: Intel Cyclone 10 LP F484 FPGA Board
DOC. No:	ACM-208
FILE: POWER.SchDoc	DATE: 2018/04/03 16:53:27
Sheet: 2 / 7	B



EXCLK_AN		FPGA1	
EXCLK_AN	A14	CLK10_DIFFCLK_4n/B8	A14
EXCLK_AP	B14	CLK11_DIFFCLK_4p/B8	B14
IOA41	C5	IO_PLL3_CLKOUTn/B8	C5
IOA40	D5	IO_PLL3_CLKOUTp/B8	D5
IOA24	C13	IO_DIFFIO_T31n/B8	C13
IOA25	D13	IO_DIFFIO_T31p/B8	D13
IOA30	C14	IO_DIFFIO_T30n/B8	C14
IOA31	D14	IO_DIFFIO_T30p/B8	D14
IOA28	C12	IO_DIFFIO_T30p/B8	C12
IOA29	D12	IO_DIFFIO_T29n/B8	D12
IOA19	A12	IO_DIFFIO_T29p/B8	A12
IOA18	B12	IO_DIFFIO_T27n/B8	B12
IOA27	F14	IO_DIFFIO_T26n/B8	F14
IOA26	E14	IO_DIFFIO_T26p/B8	E14
IOA17	A11	IO_DATA2_DIFFIO_T25n/B8	A11
IOA16	B11	IO_DATA3_DIFFIO_T25p/B8	B11
IOA15	A10	IO_DIFFIO_T23n/B8	A10
IOA14	B10	IO_DATA4_DIFFIO_T23p/B8	B10
IOA60	G13	IO_DIFFIO_T22n/B8	G13
IOA61	H13	IO_DIFFIO_T22p/B8	H13
IOA51	C10	IO_DIFFIO_T21n/B8	C10
IOA50	D10	IO_DIFFIO_T21p/B8	D10
IOA62	E12	IO_DIFFIO_T20n/B8	E12
IOA63	F12	IO_DIFFIO_T20p/B8	F12
IOA57	E11	IO_DIFFIO_T19n/B8	E11
IOA56	F11	IO_DIFFIO_T19p/B8	F11
IOA11	A7	IO_DIFFIO_T18n/B8	A7
IOA10	B7	IO_DATA5_DIFFIO_T18p/B8	B7
IOA9	A6	IO_DIFFIO_T17n/B8	A6
IOA8	B6	IO_DATA6_DIFFIO_T17p/B8	B6
IOA59	C11	IO_DATA7_DIFFIO_T15n/B8	C11
IOA58	D11	IO_DIFFIO_T15p/B8	D11
IOA49	C9	IO_DIFFIO_T14n/B8	C9
IOA48	D9	IO_DIFFIO_T14p/B8	D9
IOA13	A8	IO_DIFFIO_T11n/B8	A8
IOA12	B8	IO_DIFFIO_T11p/B8	B8
IOA47	C8	IO_DIFFIO_T10n/B8	C8
IOA46	D8	IO_DIFFIO_T10p/B8	D8
IOA45	C7	IO_DIFFIO_T9n/B8	C7
IOA44	D7	IO_DIFFIO_T9p/B8	D7
IOA55	F10	IO_DIFFIO_T13n/B8	F10
IOA54	E8	IO_DIFFIO_T7n/B8	E8
IOA52	E5	IO_DIFFIO_T7p/B8	E5
IOA53	E7	IO_DIFFIO_T8n/B8	E7
IOA7	A4	IO_DIFFIO_T4n/B8	A4
IOA6	B4	IO_DIFFIO_T4p/B8	B4
IOA4	B3	IO_DIFFIO_T3n/B8	B3
IOA5	A3	IO_DIFFIO_T3p/B8	A3
IOA39	C4	IO_DIFFIO_T2n/B8	C4
IOA38	D4	IO_DIFFIO_T2p/B8	D4
IOA42	D6	IO_DIFFIO_T8p/B8(IO(080))	D6
IOA23	E4	IO_DIFFIO_T1n/B8(IO(080))	E4
IOA43	E10	IO/B8(IO(080))	E10
	C6	IO/B8	C6
	G14	IO_VREFB8N0/B8	G14
	G12	IO_VREFB8N1/B8	G12
	G9	IO_VREFB8N2/B8	G9
	A2	VCCIO8	A2
	A5	VCCIO8	A5
	A9	VCCIO8	A9
	A13	VCCIO8	A13
	E6	VCCIO8	E6
	E9	VCCIO8	E9
	E13	VCCIO8	E13
	H11	VCCIO8	H11



Bank = 1x476, 1x104

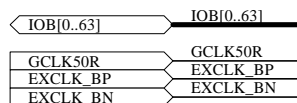


Bank Group A(3.3V)

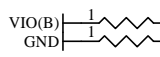
ACM208R1-SCH-B.pdf



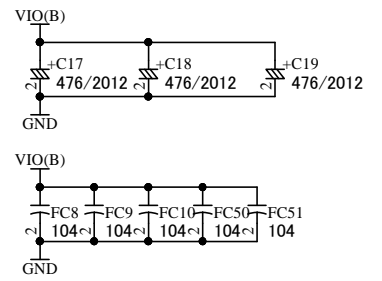
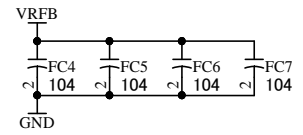
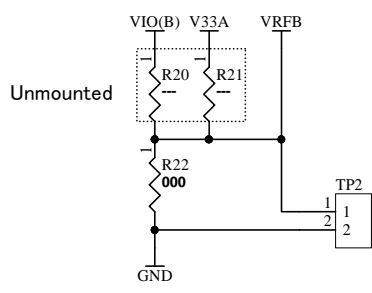
DSN:	TITLE: Intel Cyclone 10 LP F780 FPGA Board
DOC. No: ACM-208	B
FILE: IOA.SchDoc	DATE: 2018/04/03 16:53:27
Sheet: 3 / 7	



FPGA		FPGA	
EXCLK_BP	Y2	CLK2_DIFFCLK_1p/B2	
EXCLK_BN	Y1	CLK3_DIFFCLK_1n/B2	
IOB33	R2	IO_DIFFIO_L25p/B2	
IOB32	R1	IO_DIFFIO_L25n/B2	
IOB2	U3	IO_DIFFIO_L27p/B2	
IOB3	U4	IO_DIFFIO_L27n/B2	
	X R3	IO_DIFFIO_L28p/B2	
	X R4	IO_DIFFIO_L28n/B2	
IOB0	T4	IO_DIFFIO_L29p/B2	
IOB1	T3	IO_DIFFIO_L29n/B2	
IOB35	U2	IO_DIFFIO_L30p/B2	
IOB34	U1	IO_DIFFIO_L30n/B2	
	X V4	IO_DIFFIO_L31p/B2	
	X V3	IO_DIFFIO_L31n/B2	
IOB4	V2	IO_DIFFIO_L32p/B2	
IOB5	V1	IO_DIFFIO_L32n/B2	
IOB37	AB2	IO_DIFFIO_L33p/B2	
IOB36	AB1	IO_DIFFIO_L33n/B2	
IOB6	W2	IO_DIFFIO_L34p/B2	
IOB7	W1	IO_DIFFIO_L34n/B2	
	X U6	IO_DIFFIO_L35p/B2	
	X U5	IO_DIFFIO_L35n/B2	
	X Y4	IO_DIFFIO_L36p/B2	
	X Y3	IO_DIFFIO_L36n/B2	
IOB39	AC2	IO_DIFFIO_L37p/B2	
IOB38	AC1	IO_DIFFIO_L37n/B2	
	X AC3	IO_DIFFIO_L38p/B2	
	X AD3	IO_DIFFIO_L38n/B2	
IOB41	AD2	IO_DIFFIO_L39p/B2	
IOB40	AD1	IO_DIFFIO_L39n/B2	
IOB9	AA4	IO_DIFFIO_L40p/B2	
IOB8	AA3	IO_DIFFIO_L40n/B2	
IOB43	AE2	IO_DIFFIO_L41p/B2	
IOB42	AE1	IO_DIFFIO_L41n/B2	
	X AE3	IO_DIFFIO_L48p/B2	
	X AF2	IO_DIFFIO_L48n/B2	
	X R6	IO_DIFFIO_L26n/B2(IO(080))	
	X W3	IO_DIFFIO_L44n/B2(IO(080))	
	X AB6	IO_DIFFIO_L50p/B2(IO(080))	
	X AB5	IO_DIFFIO_L50n/B2(IO(080))	
	X R5	IO/B2	
	X AB3	IO/B2	
	U7	IO_RUP1/B2	
	U8	IO_RDN1/B2	
	T7	IO_VREFB2N0/B2	
	T8	IO_VREFB2N1/B2	
	AB4	IO_VREFB2N2/B2	
	AA1	VCCIO2	
	AG1	VCCIO2	
	T1	VCCIO2	
	T5	VCCIO2	
	W5	VCCIO2	



FPGA		FPGA	
GCLK50R	AG14	CLK15_DIFFCLK_6p/B3	
	AH14	CLK14_DIFFCLK_6n/B3	
	GND		
IOB16	AE5	IO_PLL1_CLKOUTp/B3	
IOB17	AF5	IO_PLL1_CLKOUTn/B3	
IOB18	AD5	IO_DIFFIO_B1p/B3	
IOB19	AE6	IO_DIFFIO_B1n/B3	
IOB15	AD4	IO_DIFFIO_B2p/B3	
IOB14	AF4	IO_DIFFIO_B2n/B3	
	X AE4	IO_DIFFIO_B3p/B3	
	X AG3	IO_DIFFIO_B3n/B3	
IOB45	AH3	IO_DIFFIO_B4p/B3	
IOB44	AF3	IO_DIFFIO_B4n/B3	
IOB46	AG4	IO_DIFFIO_B5p/B3	
IOB47	AH4	IO_DIFFIO_B5n/B3	
	X AD8	IO_DIFFIO_B6p/B3	
	X AC7	IO_DIFFIO_B6n/B3	
IOB48	AG6	IO_DIFFIO_B7p/B3	
IOB49	AH6	IO_DIFFIO_B7n/B3	
IOB10	AB9	IO_DIFFIO_B8p/B3	
IOB11	AB8	IO_DIFFIO_B8n/B3	
IOB50	AG7	IO_DIFFIO_B9p/B3	
IOB51	AH7	IO_DIFFIO_B9n/B3	
IOB12	AB7	IO_DIFFIO_B10p/B3	
IOB13	AC8	IO_DIFFIO_B10n/B3	
	X AA8	IO_DIFFIO_B11p/B3	
	X AA10	IO_DIFFIO_B11n/B3	
IOB52	AG8	IO_DIFFIO_B12p/B3	
IOB53	AH8	IO_DIFFIO_B12n/B3	
IOB20	AE7	IO_DIFFIO_B13p/B3	
IOB21	AF7	IO_DIFFIO_B13n/B3	
IOB23	AE8	IO_DIFFIO_B14p/B3	
IOB22	AF8	IO_DIFFIO_B14n/B3	
IOB25	AE10	IO_DIFFIO_B15p/B3	
IOB24	AF10	IO_DIFFIO_B15n/B3	
IOB54	AG10	IO_DIFFIO_B16p/B3	
IOB55	AH10	IO_DIFFIO_B16n/B3	
IOB30	AE12	IO_DIFFIO_B17p/B3	
IOB31	AF12	IO_DIFFIO_B17n/B3	
IOB27	AE11	IO_DIFFIO_B18p/B3	
IOB26	AF11	IO_DIFFIO_B18n/B3	
IOB28	AC11	IO_DIFFIO_B24p/B3	
IOB29	AD11	IO_DIFFIO_B24n/B3	
IOB56	AG11	IO_DIFFIO_B20p/B3	
IOB57	AH11	IO_DIFFIO_B20n/B3	
IOB61	AE13	IO_DIFFIO_B21p/B3	
IOB60	AF13	IO_DIFFIO_B21n/B3	
IOB63	AE14	IO_DIFFIO_B23p/B3	
IOB62	AF14	IO_DIFFIO_B23n/B3	
IOB59	AG12	IO_DIFFIO_B28p/B3	
IOB58	AH12	IO_DIFFIO_B28n/B3	
	X AD7	IO/B3	
	X AF6	IO/B3	
	X AD10	IO/B3	
	X AF9	IO/B3	
	X AE9	IO/B3	
	X AD12	IO/B3	
	Y10	IO_VREFB3N2/B3	
	AB11	IO_VREFB3N1/B3	
	AB13	IO_VREFB3N0/B3	
	AA11	VCCIO3	
	AD6	VCCIO3	
	AD9	VCCIO3	
	AD13	VCCIO3	
	AH2	VCCIO3	
	AH5	VCCIO3	
	AH9	VCCIO3	
	AH13	VCCIO3	

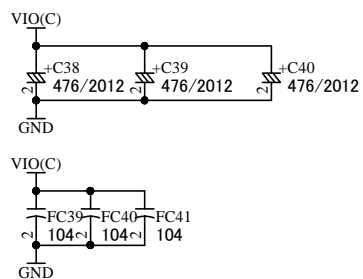
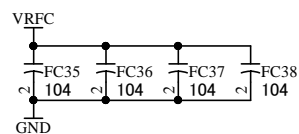
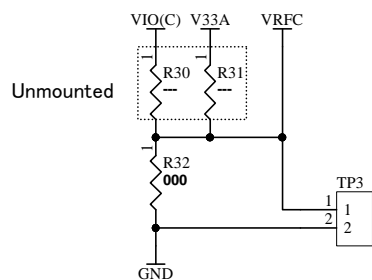
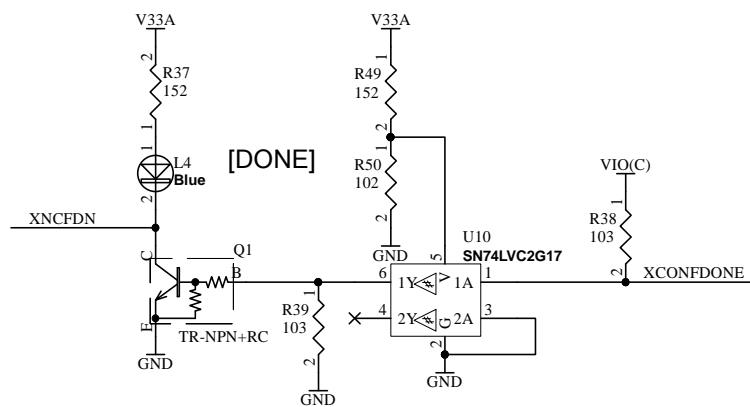
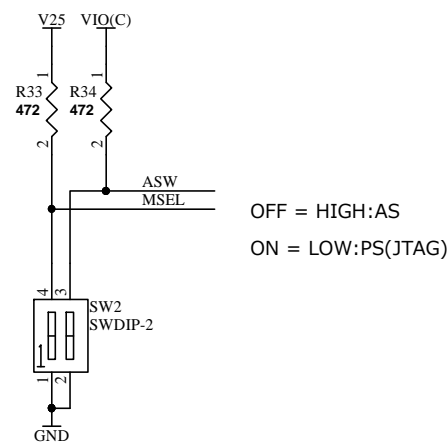
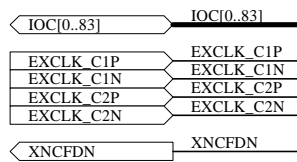


BankGroupB

ACM208R1-SCH-B.pdf

HUMAN DATA
HuMANDATA LTD.
www.hdl.co.jp

DSN:	TITLE: Intel Cyclone 10 LP F780 FPGA Board
DOC. No:	ACM-208
FILE: IOB.SchDoc	DATE: 2018/04/03 16:53:27
Sheet: 4 / 7	



FPGA H	
EXCLK_C1N	A15
EXCLK_C1P	B15
IOC32	C23
IOC33	D23
IOC65	C26
IOC64	B26
IOC57	D22
IOC56	E22
IOC13	A26
IOC12	A25
IOC30	E21
IOC31	F21
IOC67	D25
IOC66	C25
IOC10	A23
IOC11	B23
IOC60	C24
IOC61	D24
IOC55	C22
IOC54	D21
IOC8	A22
IOC9	B22
IOC6	A21
IOC7	B21
IOC62	E18
IOC63	F18
IOC49	C18
IOC48	D18
IOC53	C20
IOC52	D20
IOC51	C19
IOC50	D19
IOC47	C17
IOC46	D17
IOC4	A19
IOC5	B19
IOC2	A18
IOC3	B18
IOC59	E25
IOC58	E24
IOC45	C16
IOC44	D16
IOC0	A17
IOC1	B17
IOC42	C15
IOC43	D15
F15	IO_DIFFIO_T33n/B7
G16	IO_DIFFIO_T39n/B7
F17	IO_DIFFIO_T38n/B7
G18	IO_DIFFIO_T40p/B7
B25	IO/B7
C21	IO/B7
E17	IO_DIFFIO_T38p/B7(IO(080))
E15	IO_DIFFIO_T33p/B7(IO(080))
F19	IO_RUP4/B7
E19	IO_RDN4/B7
F22	IO_VREFB7N0/B7
G17	IO_VREFB7N1/B7
G15	IO_VREFB7N2/B7
A16	VCCIO6
A20	VCCIO7
A24	VCCIO7
A27	VCCIO7
E16	VCCIO7
E20	VCCIO7
E23	VCCIO7
H18	VCCIO7

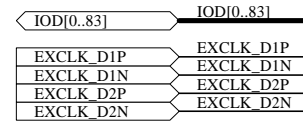
FPGA G	
EXCLK_C2N	J28
EXCLK_C2P	J27
IOC80	P26
IOC81	P25
IOC29	P28
IOC28	P27
IOC78	N26
IOC79	N25
IOC27	M28
IOC26	M27
IOC76	M26
IOC77	M25
IOC25	L28
IOC24	L27
IOC83	L24
IOC82	L23
IOC23	K28
IOC22	K27
IOC75	J26
IOC74	J25
IOC21	G28
IOC20	G27
IOC41	K26
IOC40	K25
IOC19	F28
IOC18	F27
IOC17	E28
IOC16	E27
IOC71	F26
IOC70	E26
IOC73	H26
IOC72	H25
IOC15	D28
IOC14	D27
IOC34	C27
IOC35	D26
IOC37	H24
IOC36	H23
IOC39	G26
IOC38	G25
IOC69	F25
IOC68	F24
XCONFDONE	P24
MSEL	N22
GND	P23
V25	M22
V25	P22
ASW	M24
	L26
	L25
VRFC	N21
	M21
	J22
	B28
	H28
	K24
	N24
	N28

BankGroupC

ACM208R1-SCH-B.pdf

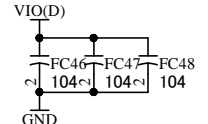
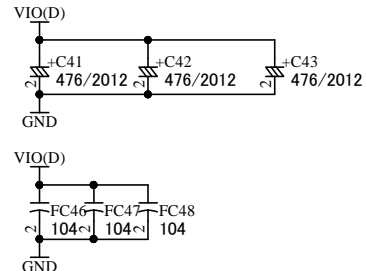
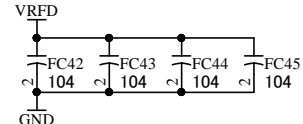
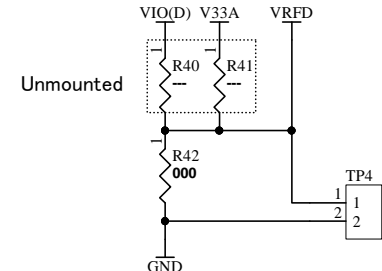
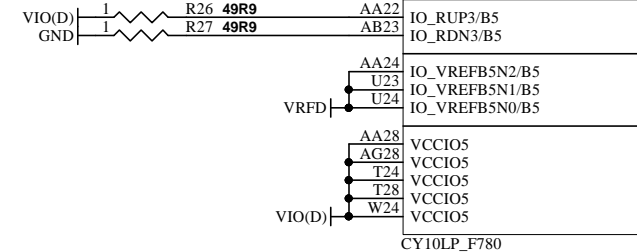
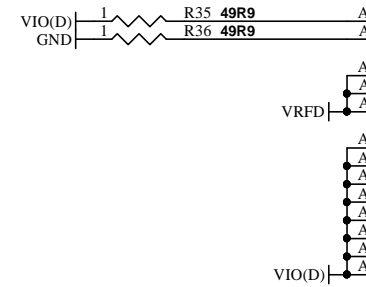
HUMAN DATA
HuMANDATA LTD.
www.hdl.co.jp

DSN:	TITLE: Intel Cyclone 10 LP F780 FPGA Board
DOC. No:	ACM-208
FILE: IOC.SchDoc	DATE: 2018/04/03 16:53:28
Sheet:	B / 7



FPGA	
EXCLK_D1P	AG15
EXCLK_D1N	AH15
IOD13	AE23
IOD12	AF23
IOD2	AC15
IOD3	AD15
IOD74	AE15
IOD75	AF15
IOD43	AG17
IOD42	AH17
×	AA16
×	AB16
IOD76	AE17
IOD77	AF17
IOD45	AG18
IOD44	AH18
IOD47	AG19
IOD46	AH19
IOD0	AC17
IOD1	AD17
IOD49	AG21
IOD48	AH21
IOD79	AE18
IOD78	AF18
IOD51	AG22
IOD50	AH22
IOD53	AG23
IOD52	AH23
IOD5	AE19
IOD4	AF19
IOD14	AF24
IOD15	AF25
IOD7	AE20
IOD6	AF20
IOD9	AE21
IOD8	AF21
IOD16	AE25
IOD17	AF26
IOD55	AG25
IOD54	AH25
IOD11	AE22
IOD10	AF22
IOD19	AD25
IOD18	AE24
IOD80	AC21
IOD81	AD21
IOD57	AG26
IOD56	AH26
×	AF16
×	AE16
×	Y19
×	AD18
×	AD24
AA17	AA17
AB17	AB17
AA15	AA15
AC18	AC18
AB20	AB20
VRFD	VRFD
AA18	AA18
AD16	AD16
AD20	AD20
AD23	AD23
AH16	AH16
AH20	AH20
AH24	AH24
AH27	AH27
VIO(D)	VIO(D)

FPGA	
EXCLK_D2N	Y28
EXCLK_D2P	Y27
IOD83	AC25
IOD82	AC24
IOD20	AD26
IOD21	AC26
IOD59	AE28
IOD58	AE27
IOD61	AD28
IOD60	AD27
IOD39	Y24
IOD38	Y23
IOD63	AC28
IOD62	AC27
IOD23	AB26
IOD22	AB25
IOD25	AA26
IOD24	AA25
IOD65	AB28
IOD64	AB27
IOD26	Y26
IOD27	Y25
IOD28	W26
IOD29	W25
IOD66	W27
IOD67	W28
IOD69	V28
IOD68	V27
IOD30	V26
IOD31	V25
IOD41	V24
IOD40	V23
IOD70	U27
IOD71	U28
IOD32	U26
IOD33	U25
×	T26
×	T25
×	T22
IOD37	T22
IOD36	T21
IOD35	R26
IOD34	R25
IOD72	R28
IOD73	R27
×	AB24
×	R24
×	AF27
AA22	AA22
AB23	AB23
AA24	AA24
U23	U23
U24	U24
VRFD	VRFD
AA28	AA28
AG28	AG28
T24	T24
T28	T28
W24	W24
VIO(D)	VIO(D)
CY10LP_F780	CY10LP_F780

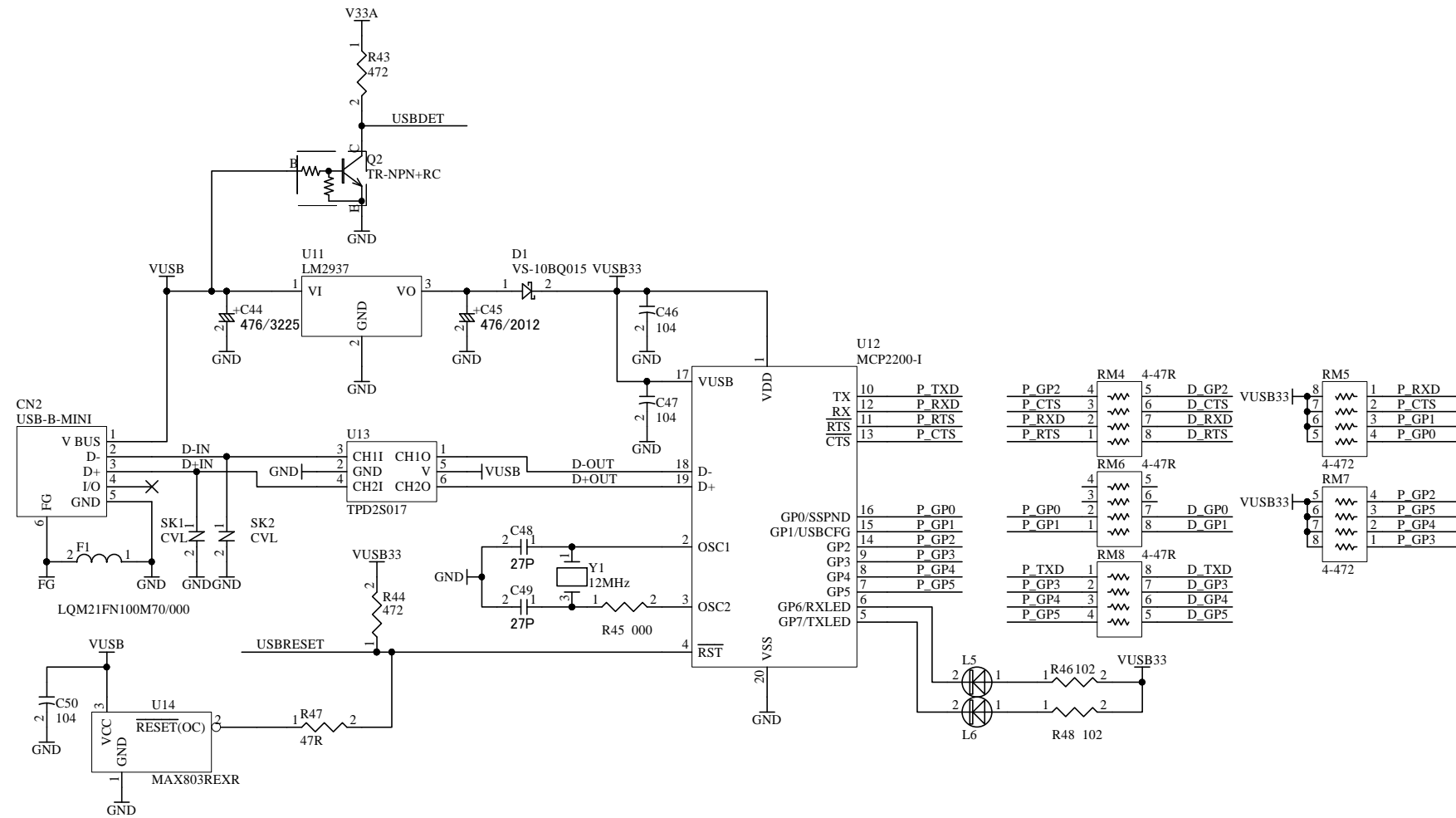


BankGroupD
ACM208R1-SCH-B.pdf

HUMAN DATA
HuMANDATA LTD.
www.hdl.co.jp

DSN:	TITLE: Intel Cyclone 10 LP F780 FPGA Board
DOC. No:	ACM-208
FILE: IOD.SchDoc	DATE: 2018/04/03 16:53:28
Sheet: 6 / 7	B

D_TXD	D_TXD
D_RXD	D_RXD
D_RTS	D_RTS
D_CTS	D_CTS
D_GP[0..5]	D_GP[0..5]
USBRESET	USBRESET
USBDET	USBDET



AA23	IO/B5(NC(080))
AA22	IO_DIFFFIO_R50p/B5(NC(080))
AA21	IO_DIFFFIO_R25p/B5(NC(080))
AA20	IO_DIFFFIO_R25m/B5(NC(080))
AA19	IO_DIFFFIO_R26p/B5(NC(080))
AA18	IO_DIFFFIO_R26m/B5(NC(080))
AA17	IO/B5(NC(080))
AA16	IO_DIFFFIO_R34p/B5(NC(080))
AA15	IO_DIFFFIO_R35p/B5(NC(080))
AA14	IO_DIFFFIO_R34m/B5(NC(080))
AA13	IO_DIFFFIO_R35m/B5(NC(080))
AA12	IO/B5(NC(080))
AA11	IO_DIFFFIO_R7p/B6(NC(080))
AA10	IO_DIFFFIO_R7m/B6(NC(080))
AA09	IO/B6(NC(080))
AA08	IO_DIFFFIO_T40m/B7(NC(080))
AA07	IO_DIFFFIO_T43m/B7(NC(080))
AA06	IO_DIFFFIO_T43p/B7(NC(080))
AA05	IO_DIFFFIO_T44p/B7(NC(080))
AA04	IO_DIFFFIO_T34m/B7(NC(080))
AA03	IO_DIFFFIO_T34p/B7(NC(080))
AA02	IO_DIFFFIO_T39p/B7(NC(080))
AA01	IO_DIFFFIO_T42m/B7(NC(080))
AA00	IO_DIFFFIO_T42p/B7(NC(080))
AA99	IO_DIFFFIO_T37m/B7(NC(080))
AA98	IO_DIFFFIO_T37p/B7(NC(080))
AA97	IO/B7(NC(080))
AA96	IO/B8(NC(080))
AA95	IO_DIFFFIO_T5p/B8(NC(080))
AA94	IO_DIFFFIO_T5m/B8(NC(080))
AA93	IO_DIFFFIO_T5p/B8(NC(080))
AA92	IO_DIFFFIO_T13p/B8(NC(080))
AA91	IO_DIFFFIO_T13m/B8(NC(080))
AA90	IO_DIFFFIO_T16m/B8(NC(080))
AA89	IO_DIFFFIO_T16p/B8(NC(080))
AA88	IO_DIFFFIO_T12m/B8(NC(080))
AA87	IO_DIFFFIO_T12p/B8(NC(080))
AA86	IO_DIFFFIO_T24m/B8(NC(080))
AA85	IO_DIFFFIO_T24p/B8(NC(080))
AA84	IO_DIFFFIO_T28p/B8(NC(080))
AA83	IO_DIFFFIO_T28m/B8(NC(080))
AA82	IO_DIFFFIO_T28p/B8(NC(080))
AA81	IO_DIFFFIO_T28m/B8(NC(080))
AA80	IO_DIFFFIO_T28p/B8(NC(080))
AA79	IO_DIFFFIO_T28m/B8(NC(080))
AA78	IO_DIFFFIO_T28p/B8(NC(080))
AA77	IO_DIFFFIO_T28m/B8(NC(080))
AA76	IO_DIFFFIO_T28p/B8(NC(080))
AA75	IO_DIFFFIO_T28m/B8(NC(080))
AA74	IO_DIFFFIO_T28p/B8(NC(080))
AA73	IO_DIFFFIO_T28m/B8(NC(080))
AA72	IO_DIFFFIO_T28p/B8(NC(080))
AA71	IO_DIFFFIO_T28m/B8(NC(080))
AA70	IO_DIFFFIO_T28p/B8(NC(080))
AA69	IO_DIFFFIO_T28m/B8(NC(080))
AA68	IO_DIFFFIO_T28p/B8(NC(080))
AA67	IO_DIFFFIO_T28m/B8(NC(080))
AA66	IO_DIFFFIO_T28p/B8(NC(080))
AA65	IO_DIFFFIO_T28m/B8(NC(080))
AA64	IO_DIFFFIO_T28p/B8(NC(080))
AA63	IO_DIFFFIO_T28m/B8(NC(080))
AA62	IO_DIFFFIO_T28p/B8(NC(080))
AA61	IO_DIFFFIO_T28m/B8(NC(080))
AA60	IO_DIFFFIO_T28p/B8(NC(080))
AA59	IO_DIFFFIO_T28m/B8(NC(080))
AA58	IO_DIFFFIO_T28p/B8(NC(080))
AA57	IO_DIFFFIO_T28m/B8(NC(080))
AA56	IO_DIFFFIO_T28p/B8(NC(080))
AA55	IO_DIFFFIO_T28m/B8(NC(080))
AA54	IO_DIFFFIO_T28p/B8(NC(080))
AA53	IO_DIFFFIO_T28m/B8(NC(080))
AA52	IO_DIFFFIO_T28p/B8(NC(080))
AA51	IO_DIFFFIO_T28m/B8(NC(080))
AA50	IO_DIFFFIO_T28p/B8(NC(080))
AA49	IO_DIFFFIO_T28m/B8(NC(080))
AA48	IO_DIFFFIO_T28p/B8(NC(080))
AA47	IO_DIFFFIO_T28m/B8(NC(080))
AA46	IO_DIFFFIO_T28p/B8(NC(080))
AA45	IO_DIFFFIO_T28m/B8(NC(080))
AA44	IO_DIFFFIO_T28p/B8(NC(080))
AA43	IO_DIFFFIO_T28m/B8(NC(080))
AA42	IO_DIFFFIO_T28p/B8(NC(080))
AA41	IO_DIFFFIO_T28m/B8(NC(080))
AA40	IO_DIFFFIO_T28p/B8(NC(080))
AA39	IO_DIFFFIO_T28m/B8(NC(080))
AA38	IO_DIFFFIO_T28p/B8(NC(080))
AA37	IO_DIFFFIO_T28m/B8(NC(080))
AA36	IO_DIFFFIO_T28p/B8(NC(080))
AA35	IO_DIFFFIO_T28m/B8(NC(080))
AA34	IO_DIFFFIO_T28p/B8(NC(080))
AA33	IO_DIFFFIO_T28m/B8(NC(080))
AA32	IO_DIFFFIO_T28p/B8(NC(080))
AA31	IO_DIFFFIO_T28m/B8(NC(080))
AA30	IO_DIFFFIO_T28p/B8(NC(080))
AA29	IO_DIFFFIO_T28m/B8(NC(080))
AA28	IO_DIFFFIO_T28p/B8(NC(080))
AA27	IO_DIFFFIO_T28m/B8(NC(080))
AA26	IO_DIFFFIO_T28p/B8(NC(080))
AA25	IO_DIFFFIO_T28m/B8(NC(080))
AA24	IO_DIFFFIO_T28p/B8(NC(080))
AA23	IO_DIFFFIO_T28m/B8(NC(080))
AA22	IO_DIFFFIO_T28p/B8(NC(080))
AA21	IO_DIFFFIO_T28m/B8(NC(080))
AA20	IO_DIFFFIO_T28p/B8(NC(080))
AA19	IO_DIFFFIO_T28m/B8(NC(080))
AA18	IO_DIFFFIO_T28p/B8(NC(080))
AA17	IO_DIFFFIO_T28m/B8(NC(080))
AA16	IO_DIFFFIO_T28p/B8(NC(080))
AA15	IO_DIFFFIO_T28m/B8(NC(080))
AA14	IO_DIFFFIO_T28p/B8(NC(080))
AA13	IO_DIFFFIO_T28m/B8(NC(080))
AA12	IO_DIFFFIO_T28p/B8(NC(080))
AA11	IO_DIFFFIO_T28m/B8(NC(080))
AA10	IO_DIFFFIO_T28p/B8(NC(080))
AA09	IO_DIFFFIO_T28m/B8(NC(080))
AA08	IO_DIFFFIO_T28p/B8(NC(080))
AA07	IO_DIFFFIO_T28m/B8(NC(080))
AA06	IO_DIFFFIO_T28p/B8(NC(080))
AA05	IO_DIFFFIO_T28m/B8(NC(080))
AA04	IO_DIFFFIO_T28p/B8(NC(080))
AA03	IO_DIFFFIO_T28m/B8(NC(080))
AA02	IO_DIFFFIO_T28p/B8(NC(080))
AA01	IO_DIFFFIO_T28m/B8(NC(080))
AA00	IO_DIFFFIO_T28p/B8(NC(080))

FPGA
CY10LP_F780

DSN:	TITLE: Intel Cyclone 10 LP F780 FPGA Board
DOC. No:	ACM-208
FILE: USB.SchDoc	DATE: 2018/04/03 16:53:28
Sheet: 7 / 7	B