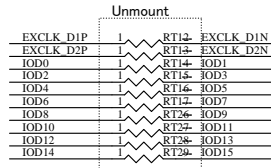
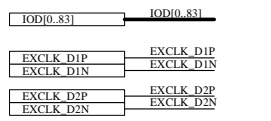
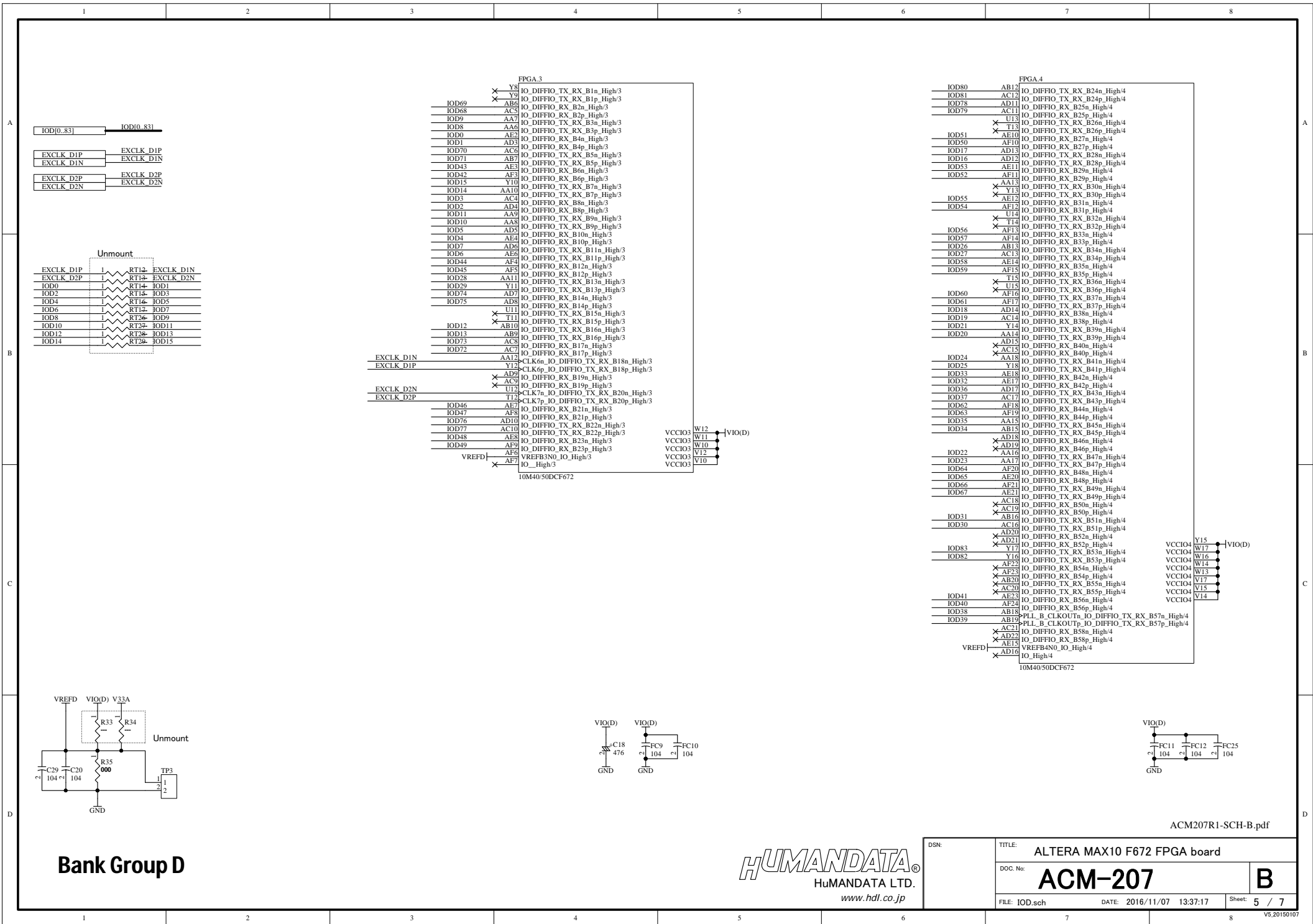


ACM207R1-SCH-B.pdf



DSN:	TITLE: ALTERA MAX10 F672 FPGA board
DOC. No:	ACM-207
FILE: ACM207A.sch	DATE: 2016/11/07 13:37:16
Sheet: 1 / 7	B

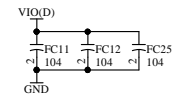
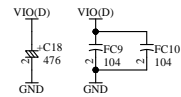
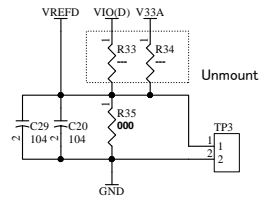


FPGA 3

Y8	IO_DIFFIO_TX_RX_B1n_High/3
Y9	IO_DIFFIO_TX_RX_B1p_High/3
AB6	IO_DIFFIO_RX_B2n_High/3
AC5	IO_DIFFIO_RX_B2p_High/3
AA7	IO_DIFFIO_TX_RX_B3n_High/3
AA7	IO_DIFFIO_RX_B4n_High/3
AE7	IO_DIFFIO_TX_RX_B3p_High/3
AD3	IO_DIFFIO_RX_B4p_High/3
AC6	IO_DIFFIO_TX_RX_B5n_High/3
AB7	IO_DIFFIO_TX_RX_B5p_High/3
AE3	IO_DIFFIO_RX_B6n_High/3
AF3	IO_DIFFIO_RX_B6p_High/3
Y10	IO_DIFFIO_TX_RX_B7n_High/3
AA10	IO_DIFFIO_TX_RX_B7p_High/3
AC4	IO_DIFFIO_RX_B8n_High/3
AD4	IO_DIFFIO_RX_B8p_High/3
AA9	IO_DIFFIO_TX_RX_B9n_High/3
AA8	IO_DIFFIO_TX_RX_B9p_High/3
AD5	IO_DIFFIO_RX_B10n_High/3
AD6	IO_DIFFIO_RX_B10p_High/3
AE6	IO_DIFFIO_TX_RX_B11n_High/3
AF4	IO_DIFFIO_TX_RX_B11p_High/3
AF4	IO_DIFFIO_RX_B12n_High/3
AF5	IO_DIFFIO_RX_B12p_High/3
AA11	IO_DIFFIO_TX_RX_B13n_High/3
Y11	IO_DIFFIO_TX_RX_B13p_High/3
AD7	IO_DIFFIO_TX_RX_B14n_High/3
AD8	IO_DIFFIO_TX_RX_B14p_High/3
U11	IO_DIFFIO_TX_RX_B15n_High/3
AB11	IO_DIFFIO_TX_RX_B15p_High/3
AB9	IO_DIFFIO_TX_RX_B16n_High/3
AC8	IO_DIFFIO_TX_RX_B16p_High/3
AC7	IO_DIFFIO_RX_B17p_High/3
AA12	CLK6n_IO_DIFFIO_TX_RX_B18n_High/3
Y12	CLK6p_IO_DIFFIO_TX_RX_B18p_High/3
AD9	IO_DIFFIO_RX_B19n_High/3
AC9	IO_DIFFIO_RX_B19p_High/3
U12	CLK7n_IO_DIFFIO_TX_RX_B20n_High/3
Y12	CLK7p_IO_DIFFIO_TX_RX_B20p_High/3
AE8	IO_DIFFIO_RX_B21n_High/3
AD10	IO_DIFFIO_TX_RX_B22n_High/3
AC10	IO_DIFFIO_TX_RX_B22p_High/3
AE8	IO_DIFFIO_RX_B23n_High/3
AF9	IO_DIFFIO_RX_B23p_High/3
AF6	VREFB3N0_IO_High/3
AF7	IO_High/3

FPGA 4

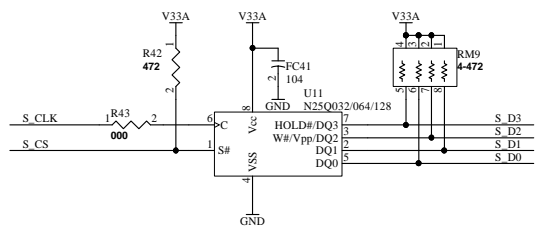
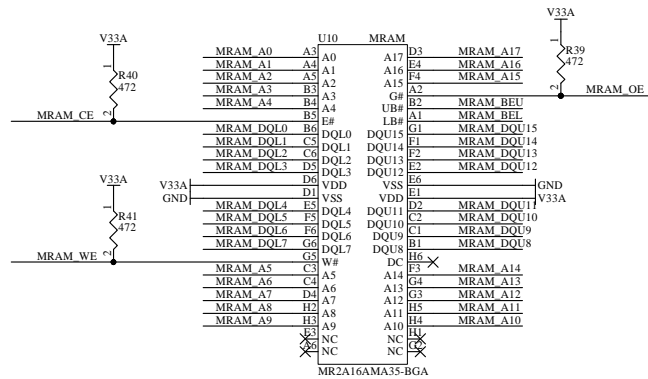
AB12	IO_DIFFIO_TX_RX_B24n_High/4
AC12	IO_DIFFIO_TX_RX_B24p_High/4
AD11	IO_DIFFIO_RX_B25n_High/4
AC11	IO_DIFFIO_TX_RX_B25p_High/4
U13	IO_DIFFIO_TX_RX_B26n_High/4
T13	IO_DIFFIO_TX_RX_B26p_High/4
AE10	IO_DIFFIO_TX_RX_B26p_High/4
AF10	IO_DIFFIO_RX_B27p_High/4
AD13	IO_DIFFIO_TX_RX_B28n_High/4
AD12	IO_DIFFIO_TX_RX_B27n_High/4
AE11	IO_DIFFIO_TX_RX_B28p_High/4
AF11	IO_DIFFIO_RX_B29n_High/4
AE12	IO_DIFFIO_TX_RX_B30n_High/4
Y13	IO_DIFFIO_TX_RX_B30p_High/4
AE12	IO_DIFFIO_RX_B31n_High/4
AF12	IO_DIFFIO_TX_RX_B31p_High/4
U14	IO_DIFFIO_TX_RX_B32n_High/4
T14	IO_DIFFIO_TX_RX_B32p_High/4
AF13	IO_DIFFIO_RX_B33n_High/4
AF14	IO_DIFFIO_TX_RX_B33p_High/4
AB13	IO_DIFFIO_TX_RX_B34n_High/4
AC13	IO_DIFFIO_TX_RX_B34p_High/4
AE14	IO_DIFFIO_RX_B35n_High/4
AF15	IO_DIFFIO_TX_RX_B35p_High/4
T15	IO_DIFFIO_TX_RX_B36n_High/4
U15	IO_DIFFIO_TX_RX_B36p_High/4
AF16	IO_DIFFIO_TX_RX_B37n_High/4
AF17	IO_DIFFIO_TX_RX_B37p_High/4
AD14	IO_DIFFIO_RX_B38n_High/4
AC14	IO_DIFFIO_TX_RX_B38p_High/4
Y14	IO_DIFFIO_TX_RX_B39n_High/4
AA14	IO_DIFFIO_TX_RX_B39p_High/4
AD15	IO_DIFFIO_RX_B40n_High/4
AC15	IO_DIFFIO_TX_RX_B40p_High/4
AA18	IO_DIFFIO_TX_RX_B41n_High/4
Y18	IO_DIFFIO_TX_RX_B41p_High/4
AE18	IO_DIFFIO_RX_B42n_High/4
AE17	IO_DIFFIO_TX_RX_B42p_High/4
AD17	IO_DIFFIO_RX_B43n_High/4
AC17	IO_DIFFIO_TX_RX_B43p_High/4
AF18	IO_DIFFIO_TX_RX_B44n_High/4
AF19	IO_DIFFIO_TX_RX_B44p_High/4
AA15	IO_DIFFIO_TX_RX_B45n_High/4
AB15	IO_DIFFIO_TX_RX_B45p_High/4
AD19	IO_DIFFIO_RX_B46n_High/4
AA16	IO_DIFFIO_TX_RX_B46p_High/4
AA17	IO_DIFFIO_TX_RX_B47n_High/4
AD17	IO_DIFFIO_TX_RX_B47p_High/4
AF20	IO_DIFFIO_RX_B48n_High/4
AE20	IO_DIFFIO_TX_RX_B48p_High/4
AF21	IO_DIFFIO_RX_B49n_High/4
AE21	IO_DIFFIO_TX_RX_B49p_High/4
AC18	IO_DIFFIO_RX_B50n_High/4
AC19	IO_DIFFIO_TX_RX_B50p_High/4
AB16	IO_DIFFIO_TX_RX_B51n_High/4
AC16	IO_DIFFIO_TX_RX_B51p_High/4
AD21	IO_DIFFIO_RX_B52n_High/4
Y17	IO_DIFFIO_TX_RX_B52p_High/4
Y17	IO_DIFFIO_TX_RX_B53n_High/4
Y16	IO_DIFFIO_TX_RX_B53p_High/4
AF22	IO_DIFFIO_RX_B54n_High/4
AF23	IO_DIFFIO_TX_RX_B54p_High/4
AB20	IO_DIFFIO_TX_RX_B55n_High/4
AC20	IO_DIFFIO_TX_RX_B55p_High/4
AF24	IO_DIFFIO_RX_B56n_High/4
AF24	IO_DIFFIO_TX_RX_B56p_High/4
AB18	PLL_B_CLKOUTn_IO_DIFFIO_TX_RX_B57n_High/4
AB19	PLL_B_CLKOUTp_IO_DIFFIO_TX_RX_B57p_High/4
AC21	IO_DIFFIO_RX_B58n_High/4
AD22	IO_DIFFIO_TX_RX_B58p_High/4
AE15	VREFB4N0_IO_High/4
AD16	IO_High/4



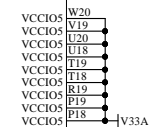
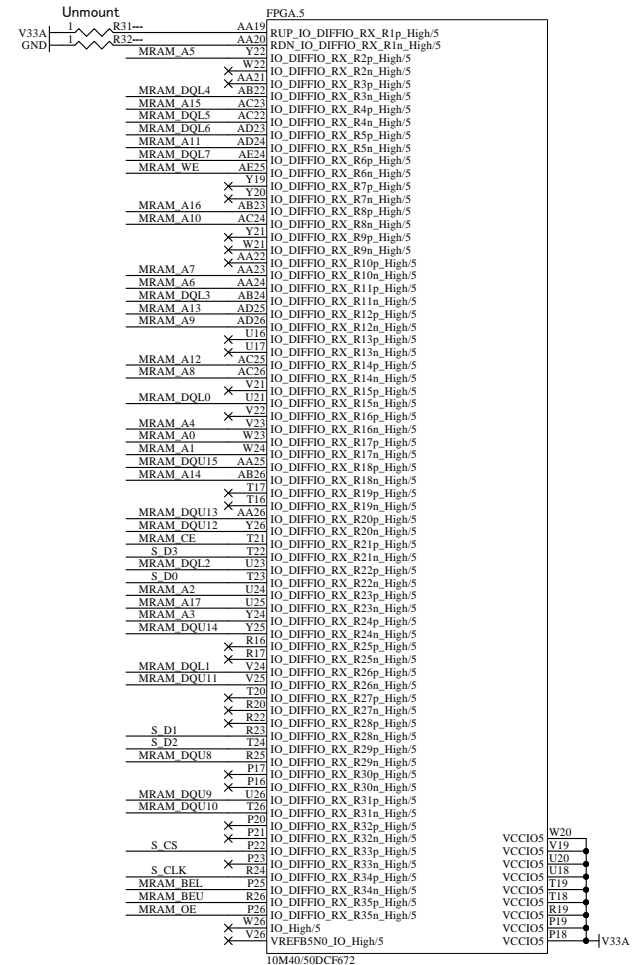
Bank Group D

HUMAN DATA
HuMANDATA LTD.
www.hdl.co.jp

DSN:	TITLE: ALTERA MAX10 F672 FPGA board
DOC. No:	ACM-207
FILE: IOD.sch	DATE: 2016/11/07 13:37:17
Sheet: 5 / 7	



MRAM_A0	MRAM_A17
MRAM_A1	MRAM_A16
MRAM_A2	MRAM_A15
MRAM_A3	MRAM_BEU
MRAM_A4	MRAM_BEL
MRAM_CE	MRAM_DQU15
MRAM_DQ0	MRAM_DQU14
MRAM_DQ1	MRAM_DQU13
MRAM_DQ2	MRAM_DQU12
MRAM_DQ3	MRAM_DQU11
MRAM_DQ4	MRAM_DQU10
MRAM_DQ5	MRAM_DQU9
MRAM_DQ6	MRAM_DQU8
MRAM_DQ7	MRAM_WE
MRAM_DQ10	MRAM_A14
MRAM_DQ11	MRAM_A13
MRAM_DQ12	MRAM_A12
MRAM_DQ13	MRAM_A11
MRAM_DQ14	MRAM_A10
MRAM_DQ15	



DSN:	TITLE: ALTERA MAX10 F672 FPGA board
DOC. No: ACM-207	B
FILE: MEMORY.SchDoc	DATE: 2016/11/07 13:37:17
Sheet: 7 / 7	V5.20150107