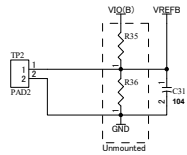


Table mapping I/O pins to FPGA pins for EPF10K10. Includes sections for EPIC1 and EPF10K10, listing pin numbers, FPGA pin numbers, and I/O directions.



(*) NOTICE

GE30/CE40: Power
CE115 : User I/O

Table mapping I/O pins to FPGA pins for EPF10K10. Includes sections for EPIC2, EPF10K10, and EPIC3, listing pin numbers, FPGA pin numbers, and I/O directions.

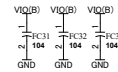
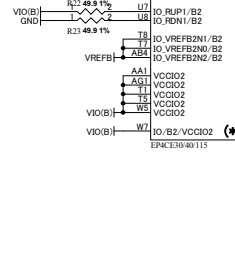
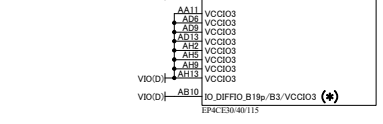
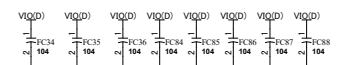


Table mapping I/O pins to FPGA pins for EPF10K10. Includes sections for EPIC4, EPF10K10, and EPIC5, listing pin numbers, FPGA pin numbers, and I/O directions.



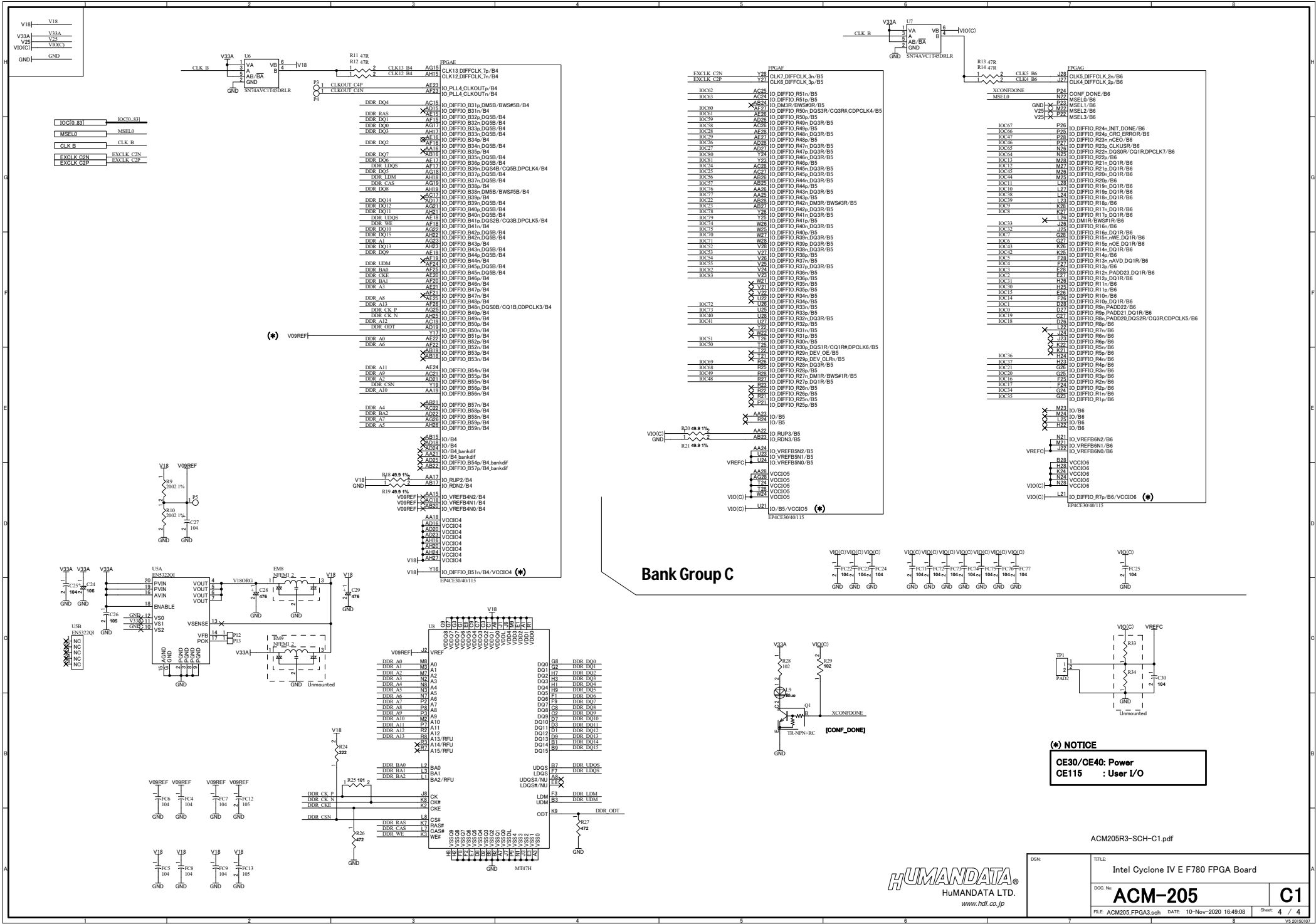
Bank Group D



ACM205R3-SCH-C1.pdf



Table with DSN, TITLE, DOC. NO. (ACM-205), and C1 information.



Bank Group C

(*) NOTICE
CE30/CE15 : Power
CE115 : User I/O

ACM205R3-SCH-C1.pdf



DSN	TITLE	Intel Cyclone IV E F780 FPGA Board
DOC No	ACM-205	C1
FILE	ACM205_FPGA3.sch	DATE: 10-Nov-2020 16:49:08 Sheet 4 / 4