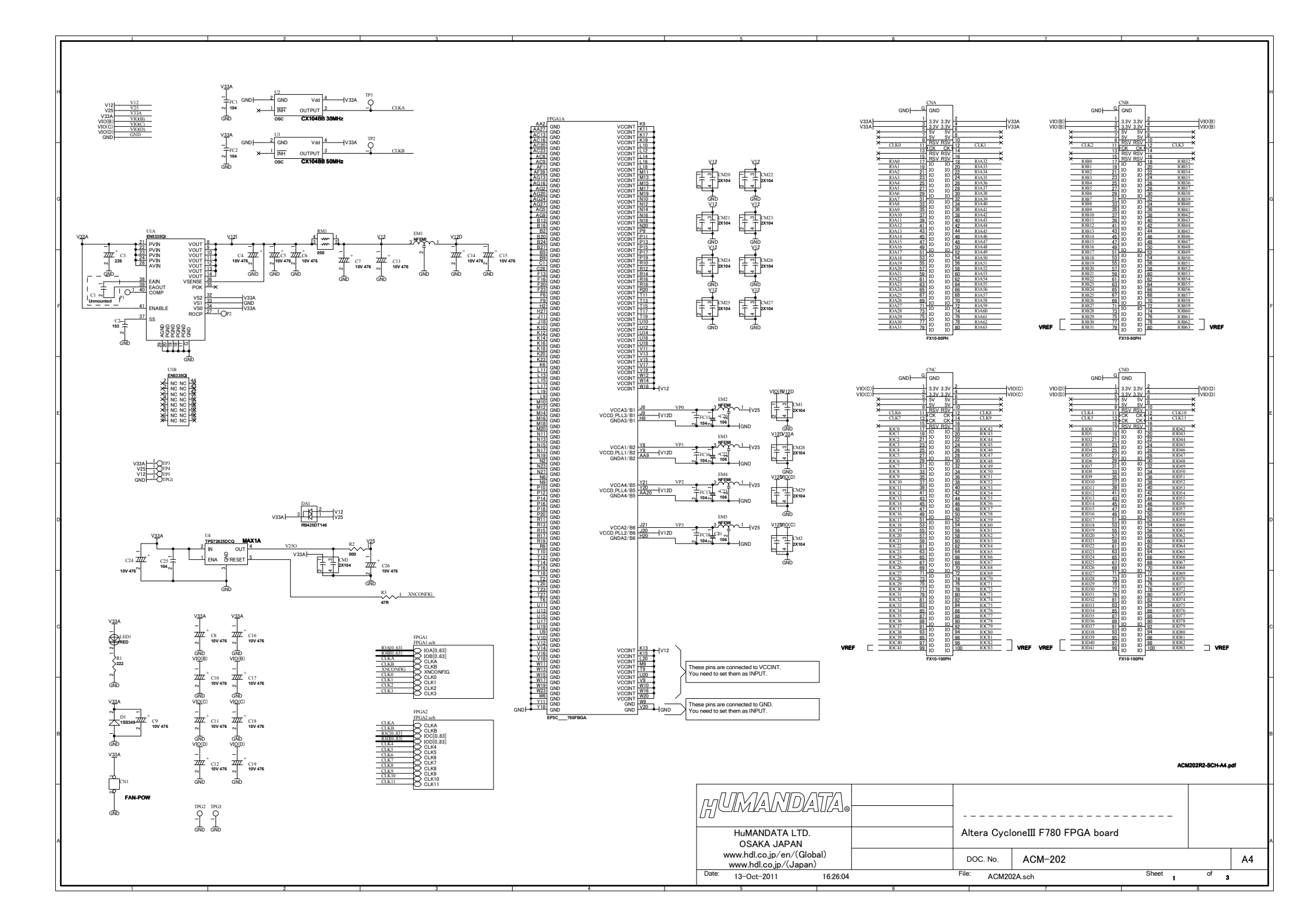


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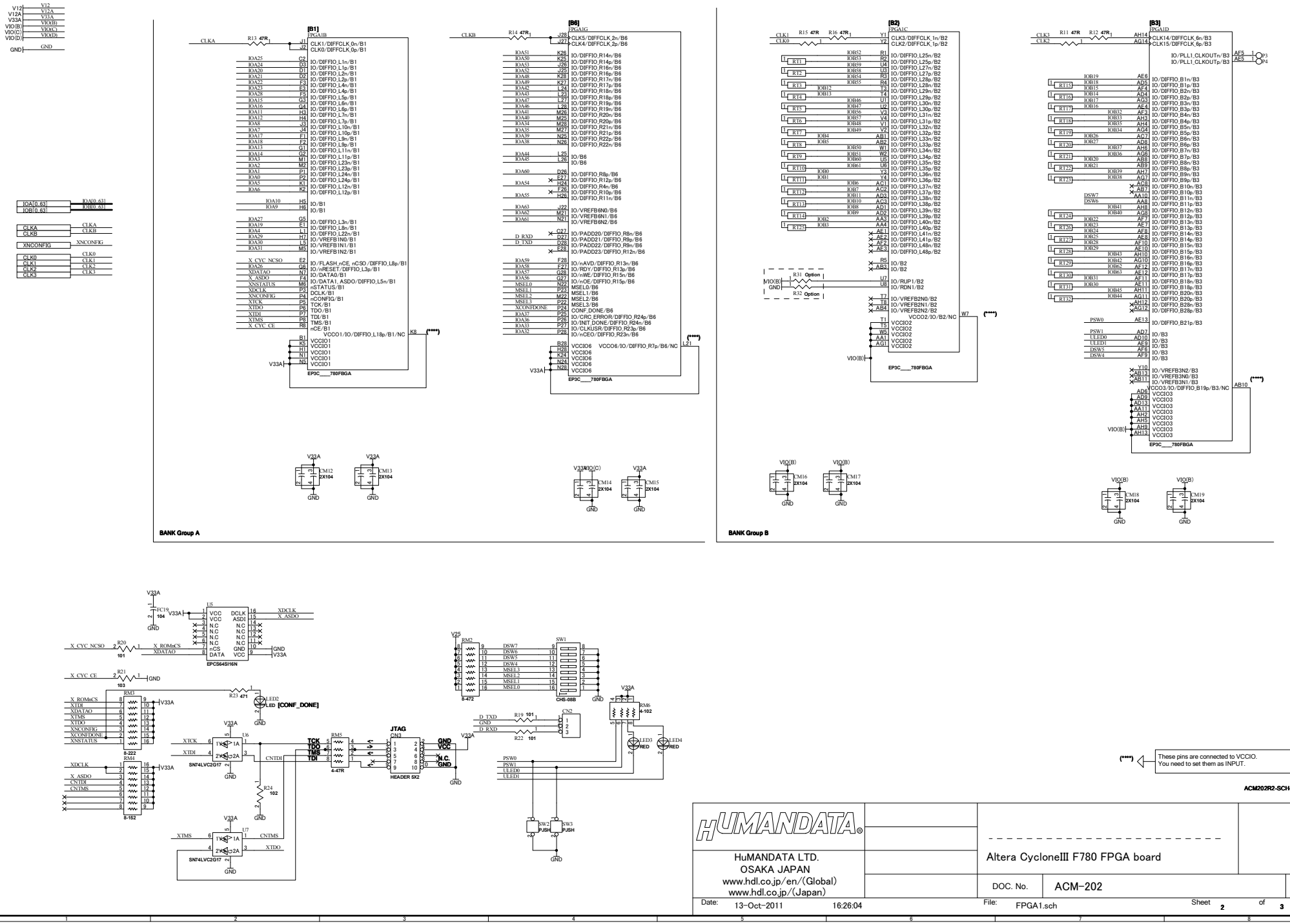
[B1]
FPGA1B
[B6]
FPGA1G
[B2]
FPGA1C
[B3]
FPGA1D
EP3C___780FBGA
BANK Group A
BANK Group B
EPCS64SI16N
SN74LVC2G17
CHS-08B
HEADER 5X2
JTAG
LED [CONF_DONE]
LED RED
PUSH
Option
These pins are connected to VCCIO.
You need to set them as INPUT.
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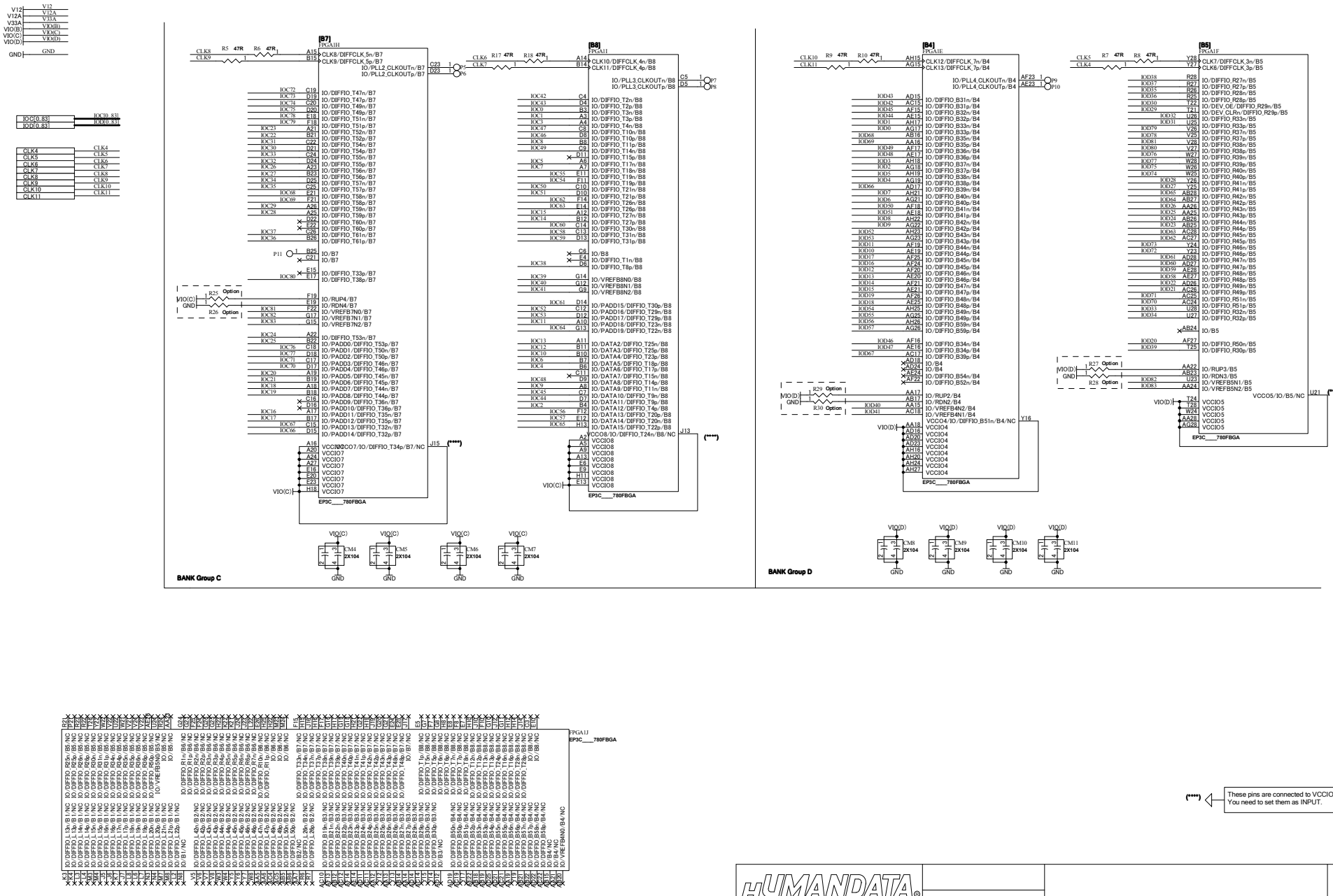
Sheet 2 of 3

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| Date: 13-Oct-2011 16:26:05 | | File: FPGA2.sch | Sheet 3 of 3 |