

BANK Group A

BANK Group B

BANK Group C

(+) : BANK Group B

These pins are connected to V12. Smaller FPGAs use them as general-purpose I/Os, but larger FPGAs use them as GND or Power. Therefore, they must be set as "As input tri-stated". If you forget to set the pins properly, large current will flow and damage FPGA and peripheral circuit.

These pins are connected to GND. Smaller FPGAs use them as general-purpose I/Os, but larger FPGAs use them as GND or Power. Therefore, they must be set as "As input tri-stated". If you forget to set the pins properly, large current will flow and damage FPGA and peripheral circuit.

ACM201R3-SCH-C5.pdf

DOC No: **ACM-201**

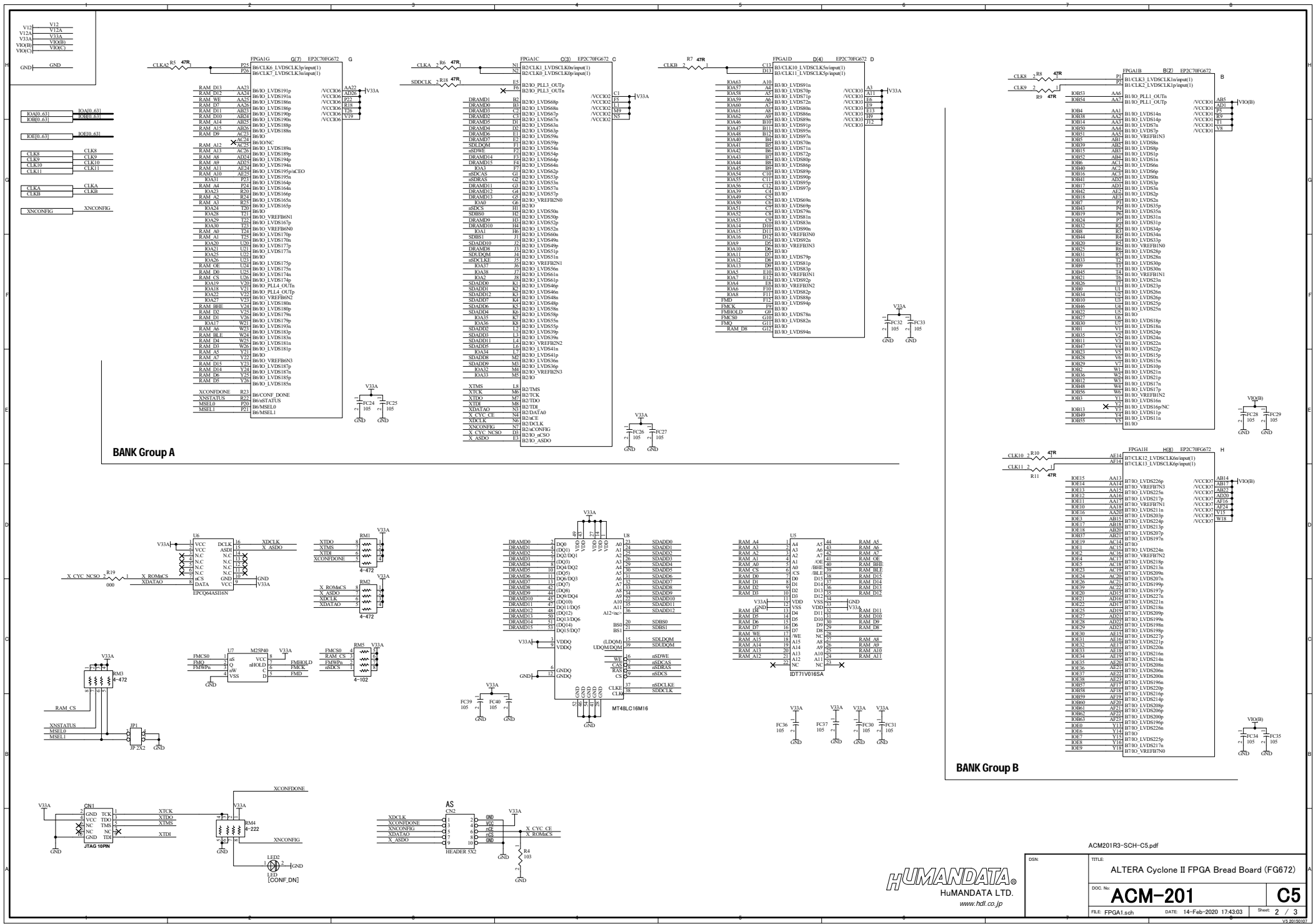
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ALTERA Cyclone II FPGA Bread Board (FG672)

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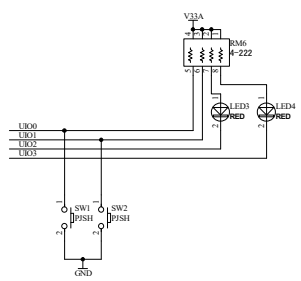
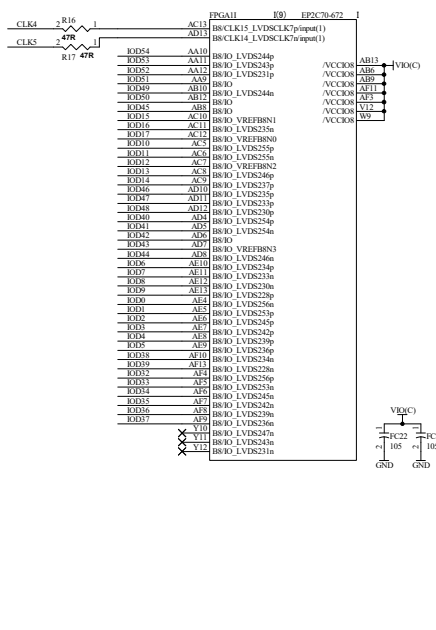
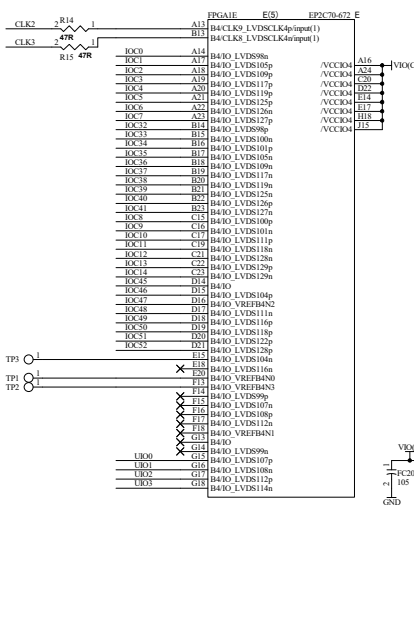
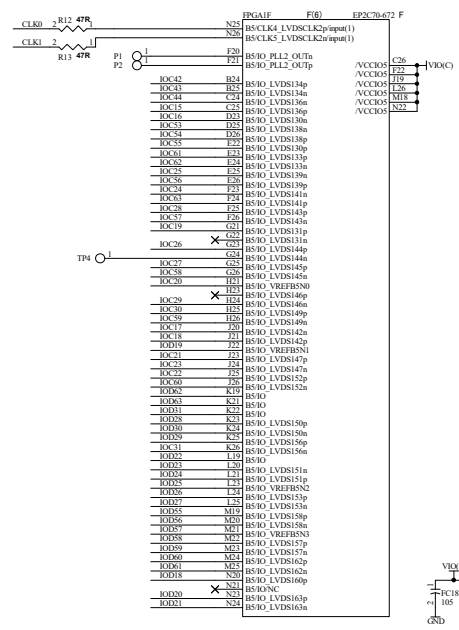




V12	V12
V12A	V12A
V33A	V33A
V33B	V33B
V33C	V33C
V33D	V33D
V33E	V33E
V33F	V33F
V33G	V33G
V33H	V33H
V33I	V33I
V33J	V33J
V33K	V33K
V33L	V33L
V33M	V33M
V33N	V33N
V33O	V33O
V33P	V33P
V33Q	V33Q
V33R	V33R
V33S	V33S
V33T	V33T
V33U	V33U
V33V	V33V
V33W	V33W
V33X	V33X
V33Y	V33Y
V33Z	V33Z
GND	GND

IO00	IO00
IO01	IO01
IO02	IO02
IO03	IO03
IO04	IO04
IO05	IO05
IO06	IO06
IO07	IO07
IO08	IO08
IO09	IO09
IO10	IO10
IO11	IO11
IO12	IO12
IO13	IO13
IO14	IO14
IO15	IO15
IO16	IO16
IO17	IO17
IO18	IO18
IO19	IO19
IO20	IO20
IO21	IO21
IO22	IO22
IO23	IO23
IO24	IO24
IO25	IO25
IO26	IO26
IO27	IO27
IO28	IO28
IO29	IO29
IO30	IO30
IO31	IO31
IO32	IO32
IO33	IO33
IO34	IO34
IO35	IO35
IO36	IO36
IO37	IO37
IO38	IO38
IO39	IO39
IO40	IO40
IO41	IO41
IO42	IO42
IO43	IO43
IO44	IO44
IO45	IO45
IO46	IO46
IO47	IO47
IO48	IO48
IO49	IO49

CLK0	CLK0
CLK1	CLK1
CLK2	CLK2
CLK3	CLK3
CLK4	CLK4
CLK5	CLK5



DSN	TITLE
ACM-201	ALTERA Cyclone II FPGA Bread Board (FG672)
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