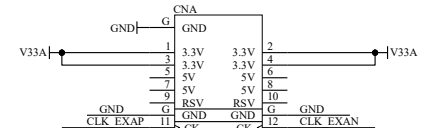
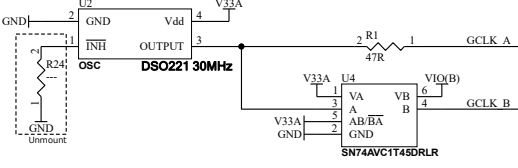
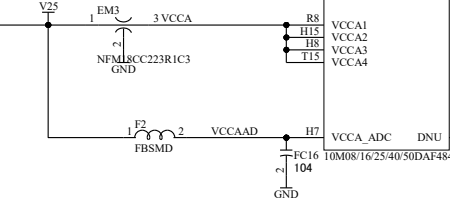
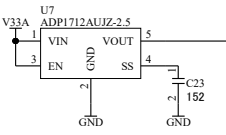
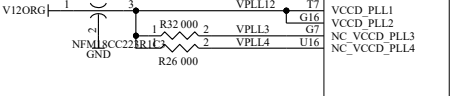
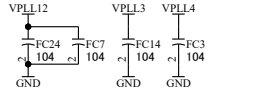
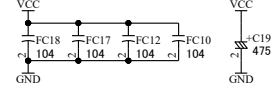
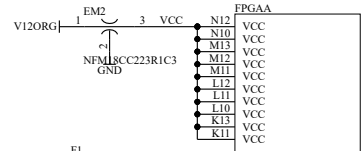
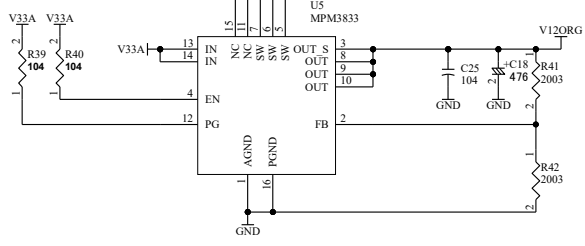
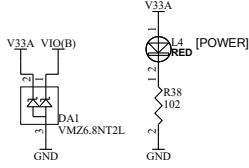
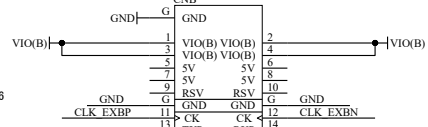


V33A	V33A
VIO(B)	VIO(B)
V25	V25
V12ORIG	V12ORIG
GND	GND



GND	G	CNA	GND	G	GND
1	3.3V	1	2	3.3V	4
3	3.3V	3	4	3.3V	5
5	5V	5	6	5V	7
7	5V	7	8	5V	9
9	RSV	9	10	RSV	11
GND	G	GND	G	GND	G
11	CLK_EXAP	11	12	CLK_EXAN	12
13	CK	13	14	CK	14
15	TXP	15	16	RXP	16
17	TXN	17	18	RXN	18
19	IO	19	20	IOA32	20
21	IOA1	21	22	IOA33	22
23	IOA2	23	24	IOA34	24
25	IOA3	25	26	IOA35	26
27	IOA4	27	28	IOA36	28
29	IOA5	29	30	IOA37	30
31	IOA6	31	32	IOA38	32
33	IOA7	33	34	IOA39	34
35	IOA8	35	36	IOA40	36
37	IOA9	37	38	IOA41	38
39	IOA10	39	40	IOA42	40
41	IOA11	41	42	IOA43	42
43	IOA12	43	44	IOA44	44
45	IOA13	45	46	IOA45	46
47	IOA14	47	48	IOA46	48
49	IOA15	49	50	IOA47	50
51	IOA16	51	52	IOA48	52
53	IOA17	53	54	IOA49	54
55	IOA18	55	56	IOA50	56
57	IOA19	57	58	IOA51	58
59	IOA20	59	60	IOA52	60
61	IOA21	61	62	IOA53	62
63	IOA22	63	64	IOA54	64
65	IOA23	65	66	IOA55	66
67	IOA24	67	68	IOA56	68
69	IOA25	69	70	IOA57	70
71	IOA26	71	72	IOA58	72
73	IOA27	73	74	IOA59	74
75	IOA28	75	76	IOA60	76
77	IOA29	77	78	IOA61	78
79	IOA30	79	80	IOA62	80
81	IOA31	81	82	IOA63	82

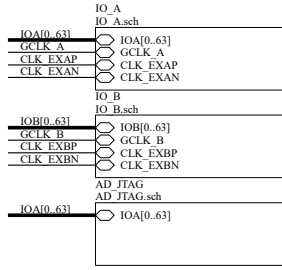


GND	G	CNB	GND	G	GND
1	VIO(B)	1	2	VIO(B)	2
3	VIO(B)	3	4	VIO(B)	4
5	5V	5	6	5V	6
7	5V	7	8	5V	8
9	RSV	9	10	RSV	10
GND	G	GND	G	GND	G
11	CLK_EXBP	11	12	CLK_EXBN	12
13	TXP	13	14	RXP	14
15	TXN	15	16	RXN	16
17	IOB0	17	18	IOB32	18
19	IOB1	19	20	IOB33	20
21	IOB2	21	22	IOB34	22
23	IOB3	23	24	IOB35	24
25	IOB4	25	26	IOB36	26
27	IOB5	27	28	IOB37	28
29	IOB6	29	30	IOB38	30
31	IOB7	31	32	IOB39	32
33	IOB8	33	34	IOB40	34
35	IOB9	35	36	IOB41	36
37	IOB10	37	38	IOB42	38
39	IOB11	39	40	IOB43	40
41	IOB12	41	42	IOB44	42
43	IOB13	43	44	IOB45	44
45	IOB14	45	46	IOB46	46
47	IOB15	47	48	IOB47	48
49	IOB16	49	50	IOB48	50
51	IOB17	51	52	IOB49	52
53	IOB18	53	54	IOB50	54
55	IOB19	55	56	IOB51	56
57	IOB20	57	58	IOB52	58
59	IOB21	59	60	IOB53	60
61	IOB22	61	62	IOB54	62
63	IOB23	63	64	IOB55	64
65	IOB24	65	66	IOB56	66
67	IOB25	67	68	IOB57	68
69	IOB26	69	70	IOB58	70
71	IOB27	71	72	IOB59	72
73	IOB28	73	74	IOB60	74
75	IOB29	75	76	IOB61	76
77	IOB30	77	78	IOB62	78
79	IOB31	79	80	IOB63	80

Power sense point

1	GND
2	V12ORIG
3	V25
4	V33A
5	VIO(B)
6	V25REF_MONIT
7	
8	
9	
10	GND

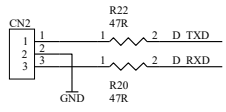
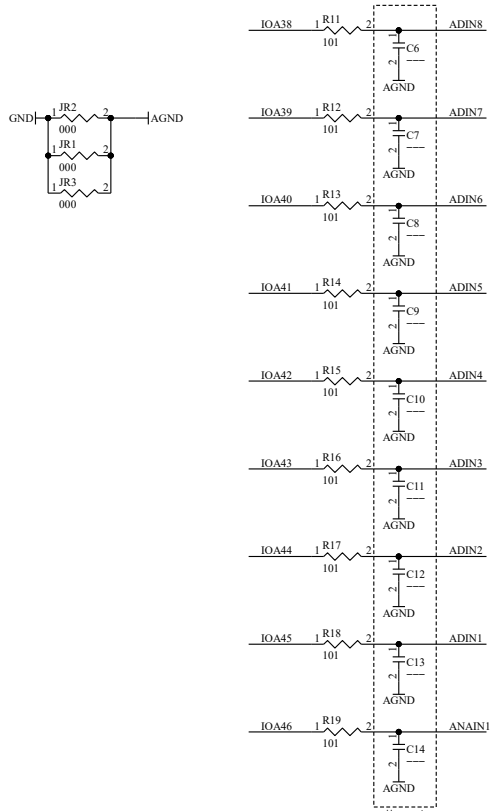
F6	GND	NC	K12
F5	GND	NC	K10
K3	GND	GND	J6
Y9	GND	GND	J2
Y15	GND	GND	J19
W21	GND	GND	J16
V6	GND	GND	J18
V2	GND	GND	J8
U13	GND	GND	J6
V19	GND	GND	J18
U10	GND	GND	J18
T8	GND	GND	J18
T4	GND	GND	J18
T16	GND	GND	J18
T14	GND	GND	J18
R21	GND	GND	J18
R19	GND	GND	J18
P6	GND	GND	J18
P2	GND	GND	J18
P17	GND	GND	J18
N13	GND	GND	J18
N11	GND	GND	J18
M7	GND	GND	J18
M19	GND	GND	J18
M16	GND	GND	J18
M10	GND	GND	J18
L5	GND	GND	J18
L21	GND	GND	J18
L17	GND	GND	J18
L13	GND	GND	J18



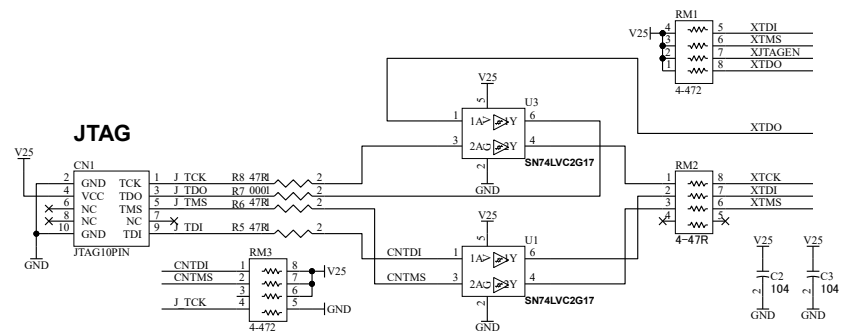
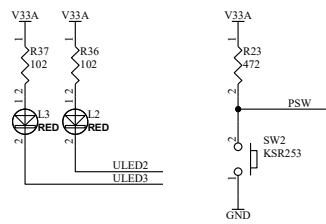
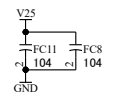
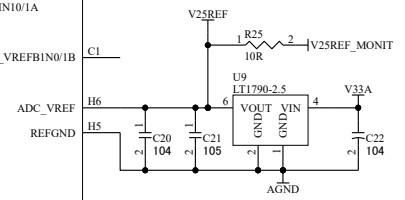
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DOC. No:	ACM-110
FILE: ACM110C.sch	DATE: 2022/05/13 15:56:48
Sheet: 1 / 4	C

V33A	V33A
VIO(B)	VIO(B)
V25	V25
V12ORG	V12ORG
GND	GND

IOA[0..63] IOA[0..63]



FPGAC		
ANAIN1	G5	ANAIN1
ADIN1	F5	NC ANAIN2
ADIN2	F4	IO_DIFFIO_RX_L1n_ADC1N1/1A
ADIN3	E4	IO_DIFFIO_RX_L1p_ADC1N2/1A
ADIN4	E3	IO_DIFFIO_RX_L2n_ADC1N6/1A
	J8	IO_DIFFIO_RX_L3n_ADC1N3/1A
	J9	IO_DIFFIO_RX_L3p_ADC1N4/1A
	G4	IO_DIFFIO_RX_L4n_ADC1N11/1A
	F3	IO_DIFFIO_RX_L4p_ADC1N12/1A
ADIN5	J4	IO_DIFFIO_RX_L5n_ADC1N8/1A
ADIN6	H3	IO_DIFFIO_RX_L5p_ADC1N6/1A
	H4	IO_DIFFIO_RX_L6n_ADC1N13/1A
ADIN7	G3	IO_DIFFIO_RX_L6p_ADC1N4/1A
ADIN8	K5	IO_DIFFIO_RX_L7n_ADC1N7/1A
	K6	IO_DIFFIO_RX_L7p_ADC1N8/1A
	K4	IO_DIFFIO_RX_L8n_ADC1N15/1A
	J3	IO_DIFFIO_RX_L8p_ADC1N10/1A
D_TXD	K8	IO_DIFFIO_RX_L9n/1B
D_RXD	D3	IO_DIFFIO_RX_L10n/1B
	D2	IO_DIFFIO_RX_L10p/1BIO_VREFB1N0/1B
	K2	IO_DIFFIO_RX_L13n/1B
	L2	IO_DIFFIO_RX_L13p/1B
	L8	IO_DIFFIO_RX_L14n/1B
	L9	IO_DIFFIO_RX_L14p/1B
	E1	IO_DIFFIO_RX_L15n/1B
	F2	IO_DIFFIO_RX_L15p/1B
	H1	IO_DIFFIO_RX_L16n/1B
	J1	IO_DIFFIO_RX_L16p/1B
PSW	G1	IO_DIFFIO_RX_L17n/1B
ULED2	F1	IO_DIFFIO_RX_L17p/1B
ULED3	D1	IO/1B
XJTAGEN	K9	IO_DIFFIO_RX_L9p_JTAGEN/1B
XTMS	H2	IO_DIFFIO_RX_L11n_TMS/1B
XTCK	G2	IO_DIFFIO_RX_L11p_TCK/1B
XTDI	L4	IO_DIFFIO_RX_L12n_TDI/1B
XTDO	M5	IO_DIFFIO_RX_L12p_TDO/1B



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DOC. No:		ACM-110	
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V33A V33A
 VIO(B) VIO(B)
 V25 V25
 V12ORG V12ORG
 GND GND

IOA[0..63] IOA[0..63]
 GCLK_A GCLK_A
 CLK_EXAP CLK_EXAP
 CLK_EXAN CLK_EXAN

FPGA			
IOA61	N4	IO DIFFIO_RX_L18n_CLK0n/2	NC
GCLK_A	N5	IO DIFFIO_RX_L18p_CLK0p/2	NC
IOA34	P4	IO DIFFIO_RX_L19n/2	NC
IOA35	P5	IO DIFFIO_RX_L19p/2	NC
IOA62	M8	IO DIFFIO_RX_L20n_CLK1n/2	NC
IOA63	M9	IO DIFFIO_RX_L20p_CLK1p/2	NC
IOA21	N3	IO DIFFIO_RX_L21n/2	NC
IOA20	N2	IO DIFFIO_RX_L21p/2	NC
CLK_EXAN	P3	IO DIFFIO_RX_L22n_DPCLK0/2	NC
CLK_EXAP	R3	IO DIFFIO_RX_L22p_DPCLK1/2	NC
IOA33	R4	IO DIFFIO_RX_L23n/2	NC
IOA32	R5	IO DIFFIO_RX_L23p/2	NC
IOA1	T1	IO DIFFIO_RX_L24n/2	NC
IOA0	T2	IO DIFFIO_RX_L24p/2	NC
IOA36	N8	IO DIFFIO_RX_L25n/2	NC
IOA37	N9	IO DIFFIO_RX_L25p/2	NC
IOA4	P1	IO DIFFIO_RX_L26n/2	NC
IOA5	P1	IO DIFFIO_RX_L26p/2	NC
IOA30	T5	IO DIFFIO_RX_L27n_PLL_L_CLKOUTn/2	NC
IOA31	T6	IO DIFFIO_RX_L27p_PLL_L_CLKOUTp/2	NC
IOA3	R1	IO DIFFIO_RX_L28n/2	NC
IOA2	R2	IO DIFFIO_RX_L28p/2	NC
IOA59	M1	IO DIFFIO_RX_L28nD_VREFB2N0/2	NC
IOA58	M1	IO DIFFIO_RX_L28pD_VREFB2P0/2	NC

FPGA			
IOA48	E10	IO DIFFIO_RX_T12n/8	NC
IOA49	E11	IO DIFFIO_RX_T12p/8	NC
MRAM_A0	C7	IO DIFFIO_RX_T13n/8	NC
MRAM_A1	C8	IO DIFFIO_RX_T13p/8	NC
MRAM_D0L0	H11	IO DIFFIO_RX_T14n/8	NC
MRAM_A5	J10	IO DIFFIO_RX_T14p/8	NC
IOA42	A6	IO DIFFIO_RX_T15n/8	NC
IOA43	B7	IO DIFFIO_RX_T15p/8	NC
IOA25	D9	IO DIFFIO_RX_T16n_DEV_CLKn/8	NC
IOA24	D8	IO DIFFIO_RX_T16p_DEV_CLKn/8	NC
IOA40	A4	IO DIFFIO_RX_T16n/8	NC
IOA41	D4	IO DIFFIO_RX_T17n/8	NC
IOA27	E9	IO DIFFIO_RX_T18n/8	NC
IOA26	D10	IO DIFFIO_RX_T18p_DEV_OE/8	NC
IOA39	A3	IO DIFFIO_RX_T19n/8	NC
IOA38	A2	IO DIFFIO_RX_T19p/8	NC
IOA11	B4	IO DIFFIO_RX_T20n/8	NC
IOA10	B3	IO DIFFIO_RX_T20p/8	NC
IOA12	C4	IO DIFFIO_RX_T21n/8	NC
IOA13	B5	IO DIFFIO_RX_T21p/8	NC
IOA29	F7	IO DIFFIO_RX_T22n_CRC_ERROR/8	NC
IOA28	E8	IO DIFFIO_RX_T22p_CRC_ERROR/8	NC
IOA14	D5	IO DIFFIO_RX_T23n/8	NC
IOA15	C5	IO DIFFIO_RX_T23p/8	NC
XCONFONE	F8	IO DIFFIO_RX_T24n_CONF_DONE/8	NC
XNSTATUS	G9	IO DIFFIO_RX_T24p_nSTATUS/8	NC
IOA8	B1	IO DIFFIO_RX_T25n/8	NC
IOA9	B2	IO DIFFIO_RX_T25p/8	NC
MRAM_A2	E6	IO DIFFIO_RX_T26n/8	NC
MRAM_CE	D6	IO DIFFIO_RX_T26p/8	NC
IOA6	C2	IO DIFFIO_RX_T27n/8	NC
IOA7	C3	IO DIFFIO_RX_T27p/8	NC
IOA88	C6	IO_VREFB8N0/8	NC
XCONFIGSEL	H10	IO_CONFIG_SEL/8	NC

FPGA			
IOA57	E15	IO DIFFIO_RX_T11n/7	NC
IOA56	E16	IO DIFFIO_RX_T11p/7	NC
IOA18	C17	IO DIFFIO_RX_T2n/7	NC
IOA19	D17	IO DIFFIO_RX_T2p/7	NC
MRAM_A6	J13	IO DIFFIO_RX_T3n/7	NC
MRAM_A4	H14	IO DIFFIO_RX_T3p/7	NC
IOA53	C13	IO DIFFIO_RX_T4n/7	NC
IOA52	C14	IO DIFFIO_RX_T4p/7	NC
IOA17	B14	IO DIFFIO_RX_T5n/7	NC
IOA16	A14	IO DIFFIO_RX_T5p/7	NC
IOA55	E13	IO DIFFIO_RX_T6n/7	NC
IOA54	D14	IO DIFFIO_RX_T6p/7	NC
IOA51	D13	IO DIFFIO_RX_T7n/7	NC
IOA50	E12	IO DIFFIO_RX_T7p/7	NC
MRAM_D0L2	H11	IO DIFFIO_RX_T8n/7	NC
MRAM_D0L1	H12	IO DIFFIO_RX_T8p/7	NC
IOA45	A8	IO DIFFIO_RX_T9n/7	NC
IOA44	A7	IO DIFFIO_RX_T9p/7	NC
IOA22	C9	IO DIFFIO_RX_T10n/7	NC
IOA23	B10	IO DIFFIO_RX_T10p/7	NC
IOA46	B8	IO DIFFIO_RX_T10p/7	NC
IOA47	A9	IO DIFFIO_RX_T11n/7	NC
MRAM_OE	A15	IO DIFFIO_RX_T11p/7	NC

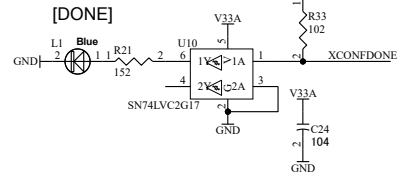
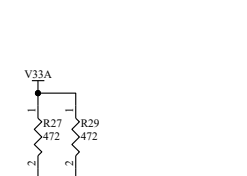
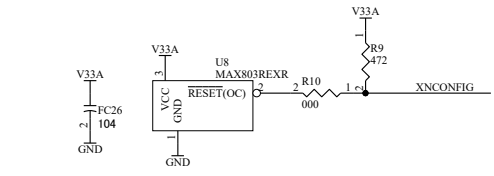
FPGA			
MRAM_A16	U18	IO DIFFIO_RX_R1p/5	NC
MRAM_D0L5	U17	IO DIFFIO_RX_R1n/5	NC
MRAM_D0L3	AA22	IO DIFFIO_RX_R2n/5	NC
MRAM_D0L4	AA21	IO DIFFIO_RX_R2p/5	NC
MRAM_A15	R14	IO DIFFIO_RX_R3n/5	NC
MRAM_A7	R15	IO DIFFIO_RX_R3p/5	NC
MRAM_WE	T22	IO DIFFIO_RX_R4n/5	NC
MRAM_A11	T21	IO DIFFIO_RX_R4p/5	NC
MRAM_A17	T18	IO DIFFIO_RX_R5n/5	NC
MRAM_D0U13	T19	IO DIFFIO_RX_R5p/5	NC
MRAM_D0U14	R20	IO DIFFIO_RX_R6n/5	NC
MRAM_D0U12	T20	IO DIFFIO_RX_R6p/5	NC
MRAM_D0L7	U22	IO DIFFIO_RX_R7n/5	NC
MRAM_D0L6	U21	IO DIFFIO_RX_R7p/5	NC
MRAM_A3	P14	IO DIFFIO_RX_R8n/5	NC
MRAM_BE1	P15	IO DIFFIO_RX_R8p/5	NC
MRAM_A12	N22	IO DIFFIO_RX_R9n/5	NC
MRAM_A13	P21	IO DIFFIO_RX_R9p/5	NC
MRAM_D0U10	P18	IO DIFFIO_RX_R10n/5	NC
MRAM_D0U11	F18	IO DIFFIO_RX_R10p/5	NC
MRAM_D0U8	P19	IO DIFFIO_RX_R11n/5	NC
MRAM_D0U9	P20	IO DIFFIO_RX_R11p/5	NC
MRAM_D0U15	L22	IO DIFFIO_RX_R12n/5	NC
MRAM_A8	M21	IO DIFFIO_RX_R12p/5	NC
MRAM_A14	M22	IO DIFFIO_RX_R13n/5	NC
MRAM_A9	N21	IO DIFFIO_RX_R13p/5	NC
MRAM_A10	R22	IO DIFFIO_RX_R14n/5	NC
MRAM_A10	R22	IO DIFFIO_RX_R14p/5	NC

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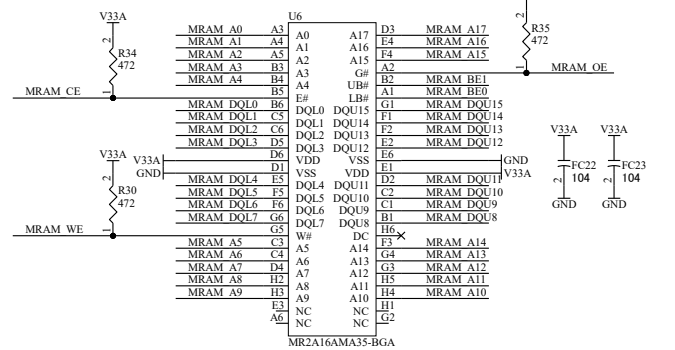
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- MRAM_A0 MRAM_A17
- MRAM_A1 MRAM_A16
- MRAM_A2 MRAM_A15
- MRAM_A3 MRAM_OE
- MRAM_A4 MRAM_BE1
- MRAM_CE MRAM_BE0
- MRAM_D0L0 MRAM_D0U15
- MRAM_D0L1 MRAM_D0U13
- MRAM_D0L2 MRAM_D0U14
- MRAM_D0L3 MRAM_D0U12
- MRAM_D0L4 MRAM_D0U11
- MRAM_D0L5 MRAM_D0U10
- MRAM_D0L6 MRAM_D0U9
- MRAM_D0L7 MRAM_D0U8
- MRAM_WE MRAM_A14
- MRAM_A5 MRAM_A13
- MRAM_A6 MRAM_A12
- MRAM_A7 MRAM_A11
- MRAM_A8 MRAM_A10
- MRAM_A9 MRAM_A10



Bank Group A

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DOC. No:	ACM-110
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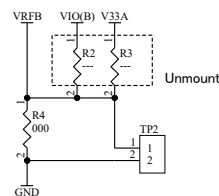
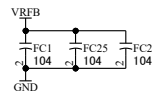
V7.20200731

V33A V33A
VIO(B) VIO(B)
V25 V25
V12ORG V12ORG
GND GND

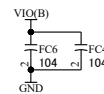
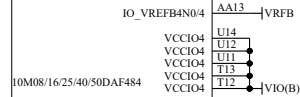
IOB[0..63] IOB[0..63]
GCLK_B GCLK_B
CLK_EXBP CLK_EXBP
CLK_EXBN CLK_EXBN

FPGA E			
IOB8	W5	IO DIFFIO TX_RX B1n/3	NC
IOB9	W6	IO DIFFIO TX_RX B1p/3	NC
IOB2	V4	IO DIFFIO RX_B2n/3	NC
IOB3	V5	IO DIFFIO RX_B2p/3	NC
IOB0	U6	IO DIFFIO RX_B3p/3	NC
IOB1	U7	IO DIFFIO TX_RX B3p/3	NC
IOB32	Y1	IO DIFFIO RX_B4n/3	NC
IOB33	Y2	IO DIFFIO RX_B4p/3	NC
IOB5	W4	IO DIFFIO TX_RX B5n/3	NC
IOB4	W3	IO DIFFIO TX_RX B5p/3	NC
IOB34	AA1	IO DIFFIO TX_RX B6p/3	NC
IOB35	AA2	IO DIFFIO RX_B6n/3	NC
IOB12	W7	IO DIFFIO TX_RX B7n/3	NC
IOB13	W8	IO DIFFIO TX_RX B7p/3	NC
IOB6	Y3	IO DIFFIO RX_B8n/3	NC
IOB7	Y4	IO DIFFIO RX_B8p/3	NC
IOB16	F10	IO DIFFIO TX_RX B9n/3	NC
IOB17	F10	IO DIFFIO TX_RX B9p/3	NC
IOB10	AA6	IO DIFFIO TX_RX B10n/3	NC
IOB11	AA7	IO DIFFIO TX_RX B10p/3	NC
IOB36	AA5	IO DIFFIO RX_B11n/3	NC
IOB37	AB5	IO DIFFIO RX_B11p/3	NC
IOB14	V9	IO DIFFIO TX_RX B12n/3	NC
IOB15	V10	IO DIFFIO TX_RX B12p/3	NC
IOB38	AB6	IO DIFFIO RX_B13n/3	NC
IOB39	AB7	IO DIFFIO RX_B13p/3	NC
IOB53	B11	IO DIFFIO TX_RX B14n/3	NC
IOB52	P11	IO DIFFIO TX_RX B14p/3	NC
IOB40	AA8	IO DIFFIO TX_RX B15n/3	IO_VREFB3N0/3
IOB41	AB8	IO DIFFIO RX_B15p/3	IO_VREFB3N0/3
IOB44	Y10	IO DIFFIO TX_RX B16n/3	VCCIO3
IOB45	AA10	IO DIFFIO TX_RX B16p/3	VCCIO3
IOB42	AA9	IO DIFFIO RX_B17n/3	VCCIO3
IOB43	AB9	IO DIFFIO RX_B17p/3	VCCIO3
IO3	AB4	IO3	VCCIO3

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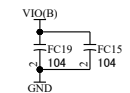


FPGA F			
IOB26	V13	IO DIFFIO TX_RX B18n/4	NC
IOB27	W14	IO DIFFIO TX_RX B18p/4	NC
IOB28	W15	IO DIFFIO RX_B19n/4	NC
IOB29	W15	IO DIFFIO RX_B19p/4	NC
IOB55	R13	IO DIFFIO TX_RX B20n/4	NC
IOB54	P13	IO DIFFIO TX_RX B20p/4	NC
IOB18	V13	IO DIFFIO TX_RX B21n/4	NC
IOB19	Y14	IO DIFFIO TX_RX B21p/4	NC
IOB46	AA14	IO DIFFIO RX_B22n/4	NC
IOB47	AB15	IO DIFFIO RX_B22p/4	NC
IOB22	V14	IO DIFFIO TX_RX B23n/4	NC
IOB23	W15	IO DIFFIO TX_RX B23p/4	NC
IOB20	AA15	IO DIFFIO RX_B24n/4	NC
IOB21	Y16	IO DIFFIO RX_B24p/4	NC
IOB30	U15	IO DIFFIO TX_RX B25n/4	NC
IOB31	V16	IO DIFFIO TX_RX B25p/4	NC
IOB48	AB16	IO DIFFIO RX_B26n/4	NC
IOB49	AA16	IO DIFFIO RX_B26p/4	NC
IOB24	V17	IO DIFFIO TX_RX B27n/4	NC
IOB25	V17	IO DIFFIO TX_RX B27p/4	NC
IOB50	AB17	IO DIFFIO RX_B28n/4	NC
IOB51	AB18	IO DIFFIO RX_B28p/4	NC
	AB14	IO3	NC



FPGA G			
	N15	IO DIFFIO RX_R14n_CLK2n/6	NC
GCLK_B	N14	IO DIFFIO RX_R14p_CLK2p/6	NC
IOB56	H21	IO DIFFIO RX_R15n/6	NC
IOB57	H22	IO DIFFIO RX_R15p/6	NC
IOB60	K21	IO DIFFIO RX_R16n_CLK3n/6	NC
IOB61	K22	IO DIFFIO RX_R16p_CLK3p/6	NC
IOB58	J21	IO DIFFIO RX_R17n/6	NC
IOB59	J22	IO DIFFIO RX_R17p/6	NC
	G19	IO DIFFIO RX_R18n/6	NC
	G20	IO DIFFIO RX_R18p/6	NC
	F22	IO DIFFIO RX_R19n/6	NC
	G22	IO DIFFIO RX_R19p/6	NC
	M14	IO DIFFIO RX_R20n/6	NC
	M15	IO DIFFIO RX_R20p/6	NC
	E21	IO DIFFIO RX_R21n/6	NC
	E22	IO DIFFIO RX_R21p/6	NC
	N19	IO DIFFIO RX_R22n/6	NC
	N18	IO DIFFIO RX_R22p/6	NC
	N20	IO DIFFIO RX_R23n/6	NC
	M20	IO DIFFIO RX_R23p/6	NC
	F20	IO DIFFIO RX_R24n/6	NC
	F21	IO DIFFIO RX_R24p/6	NC
	C22	IO DIFFIO RX_R25n/6	NC
	D22	IO DIFFIO RX_R25p/6	NC
CLK_EXBN	L15	IO DIFFIO RX_R26n_DPCLK2/6	NC
CLK_EXBP	L14	IO DIFFIO RX_R26p_DPCLK3/6	NC
	L18	IO DIFFIO RX_R27n/6	NC
	L18	IO DIFFIO RX_R27p/6	NC
	L20	IO DIFFIO RX_R28n/6	NC
	L19	IO DIFFIO RX_R28p/6	NC
	F18	IO DIFFIO RX_R29n/6	NC
	E19	IO DIFFIO RX_R29p/6	NC
	K15	IO DIFFIO RX_R31n/6	NC
	K14	IO DIFFIO RX_R31p/6	NC
	D19	IO DIFFIO RX_R32n/6	NC
	C20	IO DIFFIO RX_R32p/6	NC
IOB62	G17	IO DIFFIO RX_R33n_PLL_R_CLKOUTn/6	NC
IOB63	H17	IO DIFFIO RX_R33p_PLL_R_CLKOUTp/6	NC
	E18	IO DIFFIO RX_R34n/6	NC
	D18	IO DIFFIO RX_R34p/6	NC
	C21	IO3	NC

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Bank Group B

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