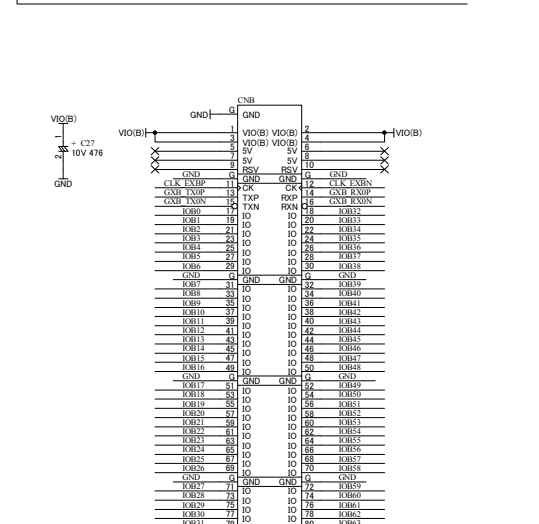
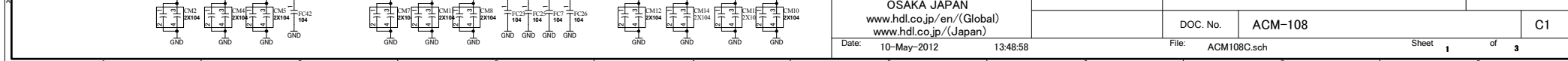



Bank Group A



Bank Group B





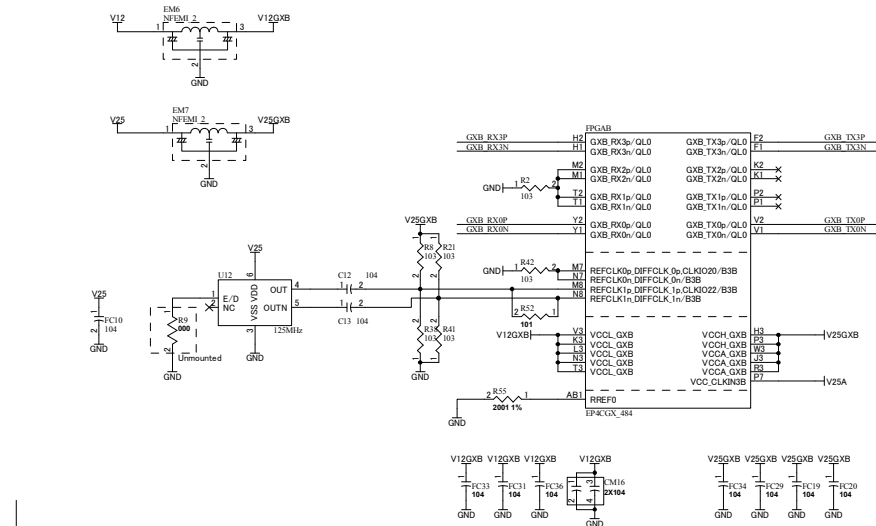
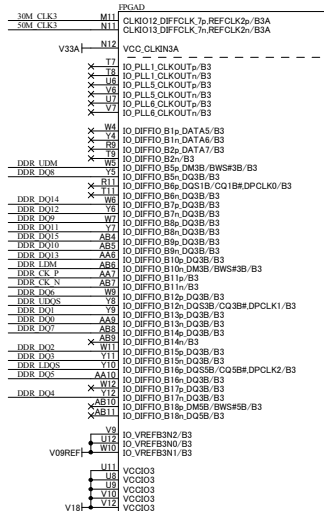
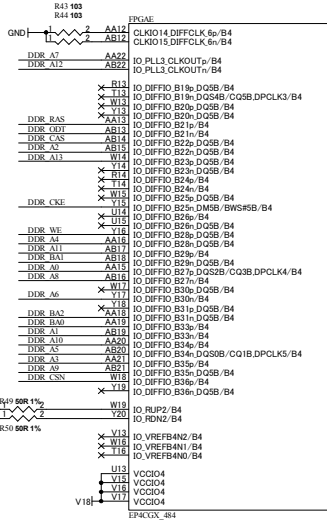
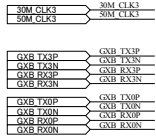
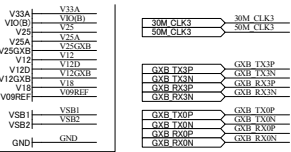
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ALTERA Cyclone IV GX F484 FPGA board

Doc. No. ACM-108

Date: 10-May-2012

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BOTTOM EDGE [BANK 3/4] for DDR2 SDRAM

LEFT EDGE for HI-SPEED TRASCEIVER

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