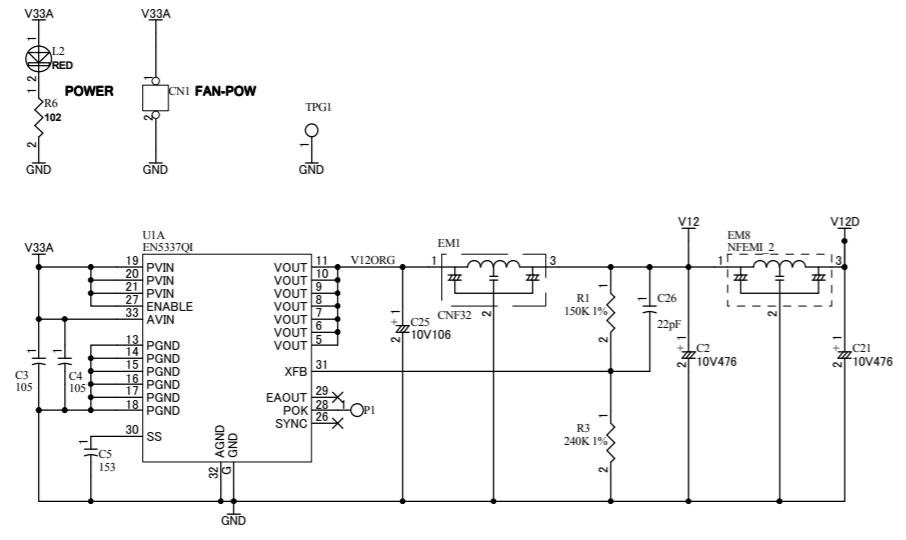
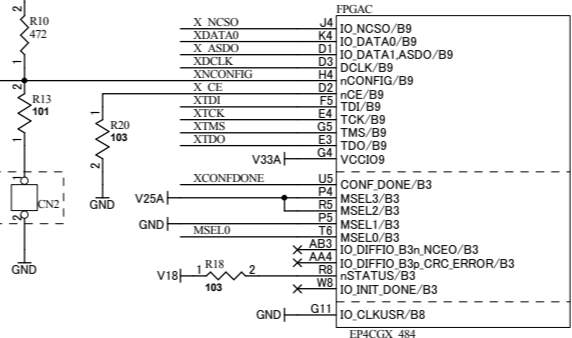
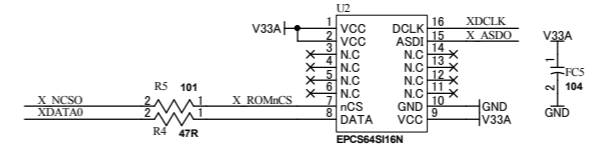
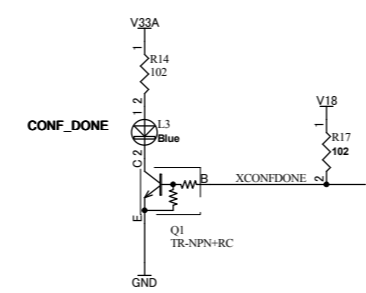
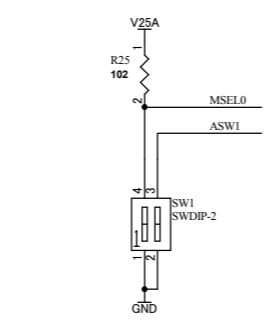
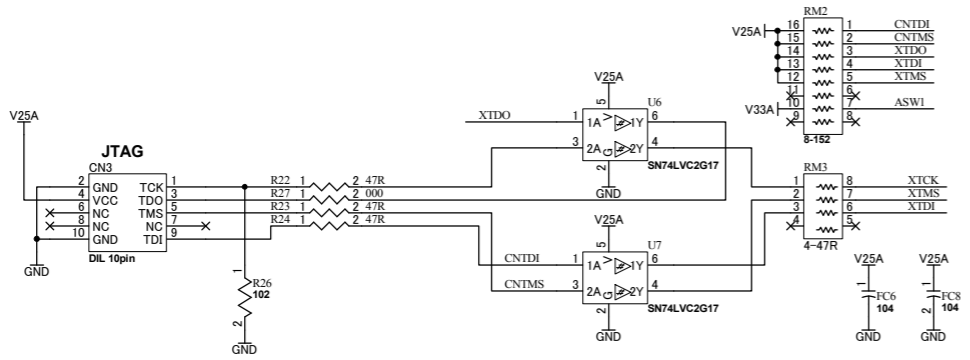
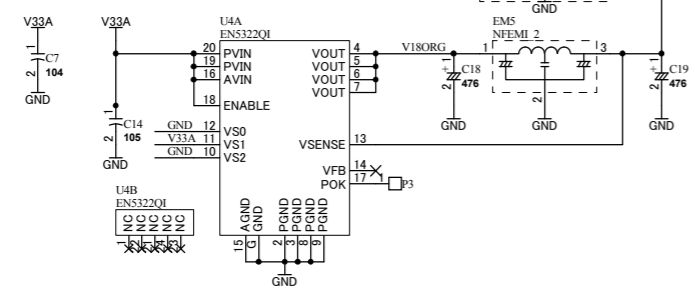
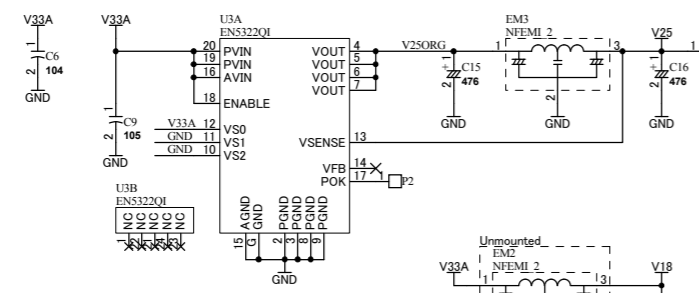


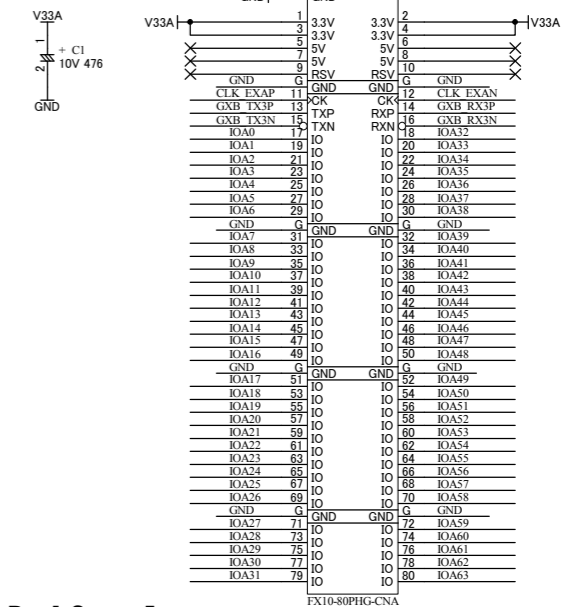
V33A	V33A
VIO(B)	V25
V25	V25A
V25A	V25GXB
V25GXB	V12
V12	V12D
V12D	V12GXB
V12GXB	V18
V18	V09REF
V09REF	VSB1
VSB1	VSB2
VSB2	VREFB
VREFB	GND
GND	GND



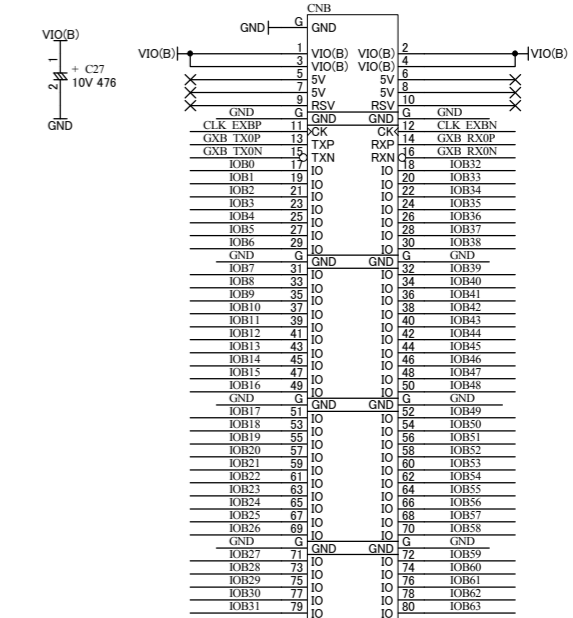
U11B	NC/SW	30
	NC/SW	31
	NC/SW	32
	NC/SW	33
	NC/SW	34
	NC/SW	35
	NC/SW	36
	NC/SW	37
	NC/SW	38
	NC/SW	39
	NC/SW	40
	NC/SW	41
	NC/SW	42
	NC/SW	43
	NC/SW	44
	NC/SW	45
	NC/SW	46
	NC/SW	47
	NC/SW	48
	NC/SW	49
	NC/SW	50
	NC/SW	51
	NC/SW	52
	NC/SW	53
	NC/SW	54
	NC/SW	55
	NC/SW	56
	NC/SW	57
	NC/SW	58
	NC/SW	59
	NC/SW	60
	NC/SW	61
	NC/SW	62
	NC/SW	63
	NC/SW	64
	NC/SW	65
	NC/SW	66
	NC/SW	67
	NC/SW	68
	NC/SW	69
	NC/SW	70
	NC/SW	71
	NC/SW	72
	NC/SW	73
	NC/SW	74
	NC/SW	75
	NC/SW	76
	NC/SW	77
	NC/SW	78
	NC/SW	79
	NC/SW	80
	NC/SW	81
	NC/SW	82
	NC/SW	83
	NC/SW	84
	NC/SW	85
	NC/SW	86
	NC/SW	87
	NC/SW	88
	NC/SW	89
	NC/SW	90
	NC/SW	91
	NC/SW	92
	NC/SW	93
	NC/SW	94
	NC/SW	95
	NC/SW	96
	NC/SW	97
	NC/SW	98
	NC/SW	99
	NC/SW	100



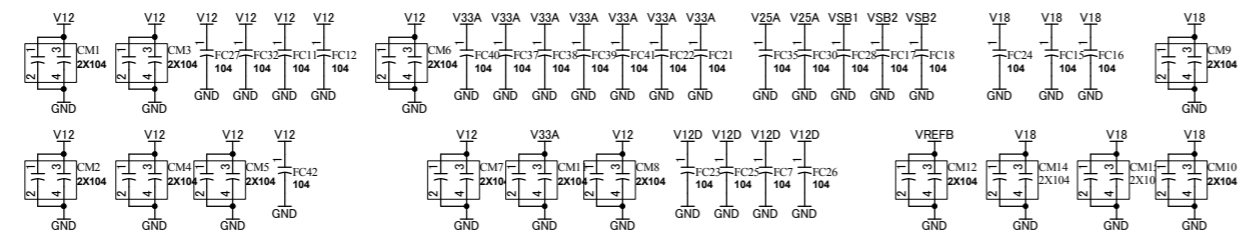
IOA10_63I	IOA[0..63]	30M_CLK3	30M_CLK3
IOB0_63I	IOB[0..63]	50M_CLK3	50M_CLK3
CLK_EXAP	CLK_EXAP	GXB_TX3P	GXB_TX3P
CLK_EXAN	CLK_EXAN	GXB_TX3N	GXB_TX3N
CLK_EXBP	CLK_EXBP	GXB_RX3P	GXB_RX3P
CLK_EXBN	CLK_EXBN	GXB_RX3N	GXB_RX3N
ASW1	ASW1	GXB_TX0P	GXB_TX0P
ASW2	ASW2	GXB_TX0N	GXB_TX0N
30M_CLK3	30M_CLK3	GXB_RX0P	GXB_RX0P
50M_CLK3	50M_CLK3	GXB_RX0N	GXB_RX0N
ASW1	ASW1		



Bank Group A



Bank Group B



HuMANDATA LTD.  
OSAKA JAPAN  
www.hdl.co.jp/en/(Global)  
www.hdl.co.jp/(Japan)

ALTERA Cyclone IV GX F484 FPGA board

DOC. No.	ACM-108	B1
Date:	21-Nov-2011 13:00:00	Sheet 1 of 3

ACM108B1-SCH-B1



