

These pins are connected to VCCINT.
You need to set them as INPUT.

These pins are connected to GND.
You need to set them as INPUT.

BANK-A

BANK-B

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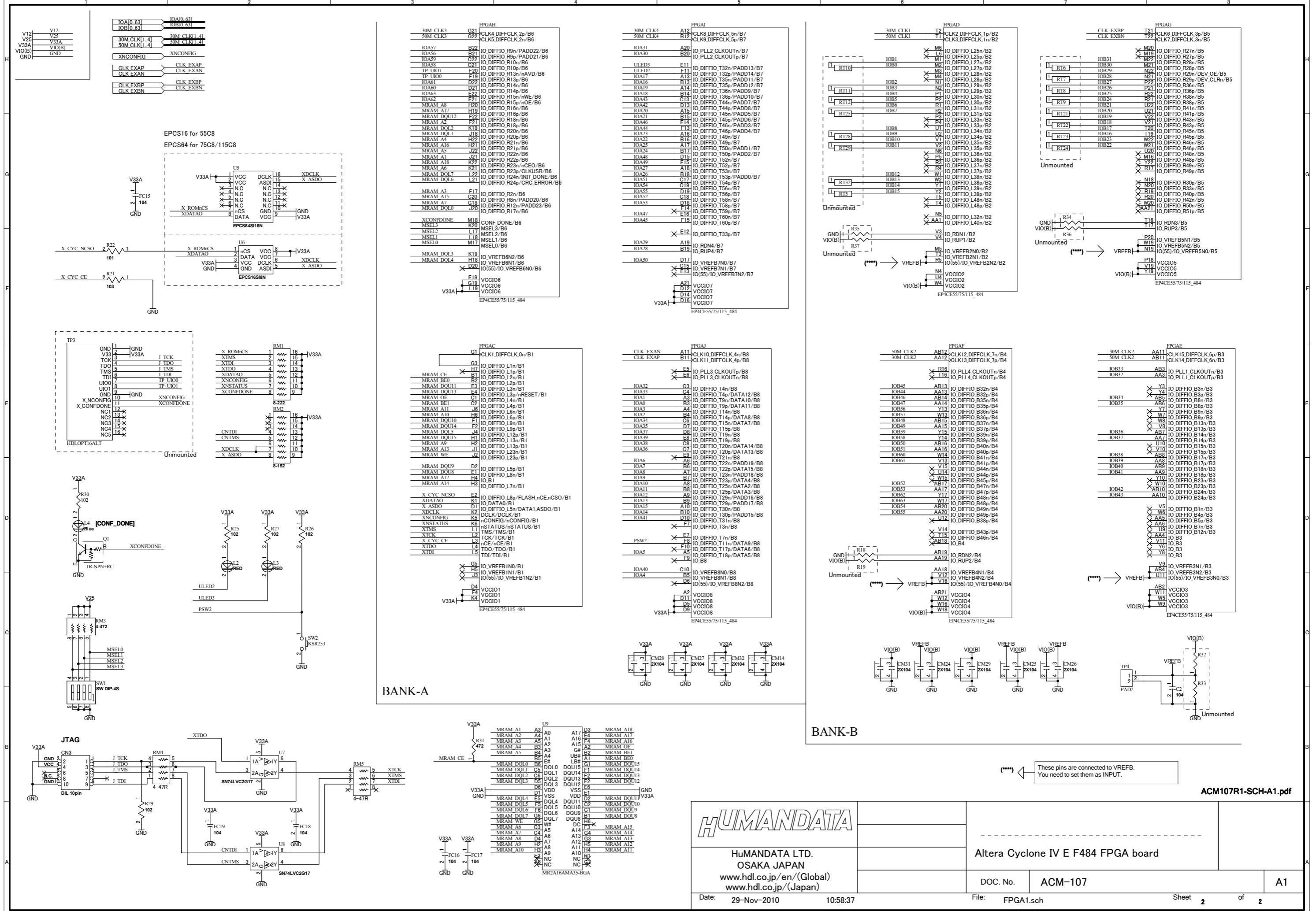
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Altera Cyclone IV E F484 FPGA board

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