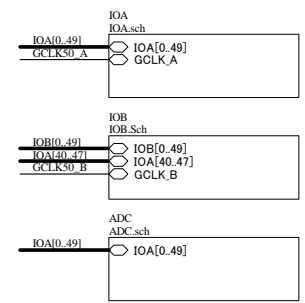


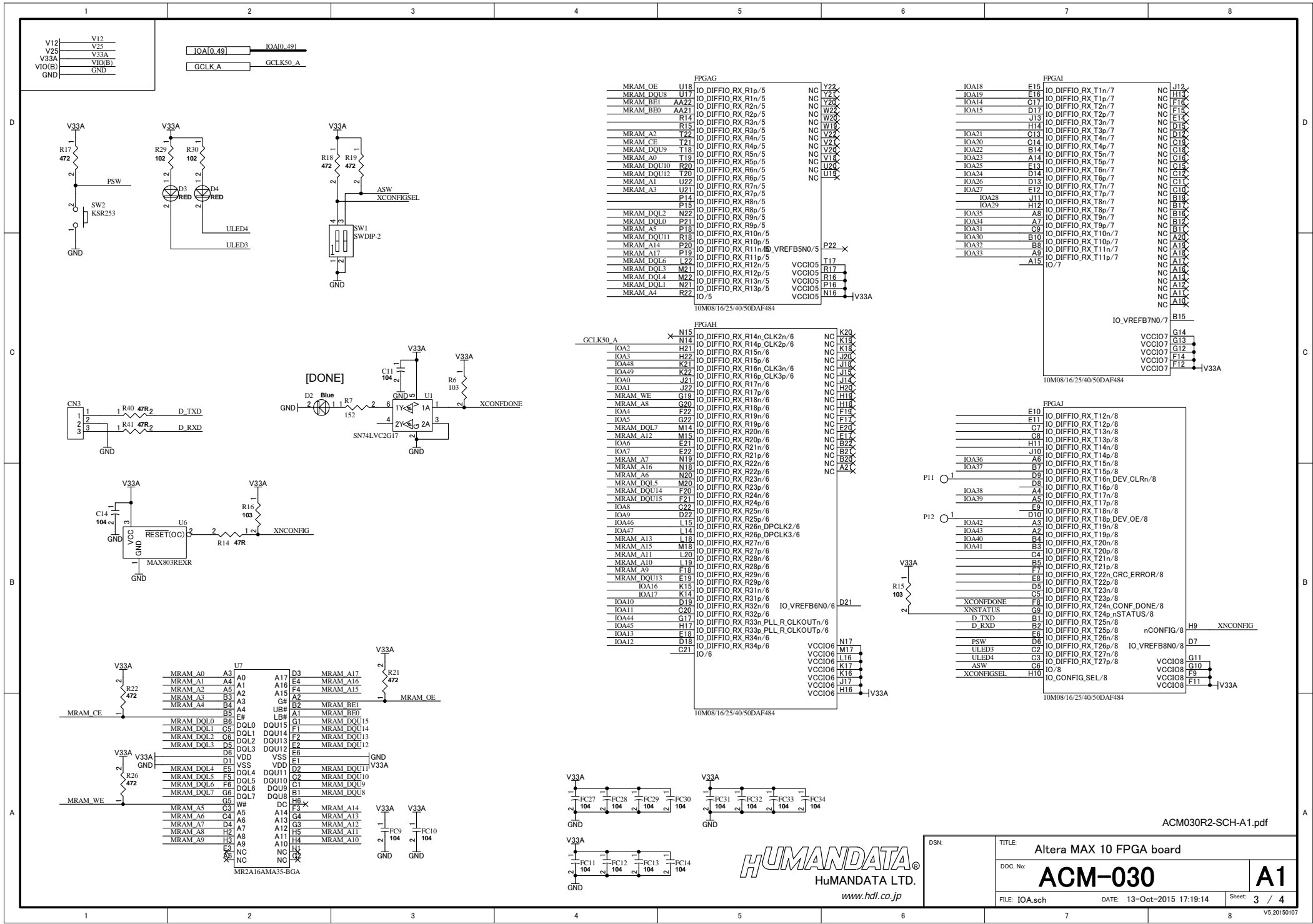
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1	VIO(A)	1	VIO(B)
2	VIO(A)	2	VIO(B)
3	5V	3	GND
4	GND	4	GND
5	GND	5	GND
6	GND	6	GND
7	IOA0	7	IOB0
8	IOA1	8	IOB1
9	IOA2	9	IOB2
10	IOA3	10	IOB3
11	IOA4	11	IOB4
12	IOA5	12	IOB5
13	IOA6	13	IOB6
14	IOA7	14	IOB7
15	GND	15	GND
16	GND	16	GND
17	IOA8	17	IOB8
18	IOA9	18	IOB9
19	IOA10	19	IOB10
20	IOA11	20	IOB11
21	IOA12	21	IOB12
22	IOA13	22	IOB13
23	IOA14	23	IOB14
24	IOA15	24	IOB15
25	GND	25	GND
26	GND	26	GND
27	IOA16	27	IOB16
28	IOA17	28	IOB17
29	IOA18	29	IOB18
30	IOA19	30	IOB19
31	IOA20	31	IOB20
32	IOA21	32	IOB21
33	IOA22	33	IOB22
34	IOA23	34	IOB23
35	GND	35	GND
36	GND	36	GND
37	IOA24	37	IOB24
38	IOA25	38	IOB25
39	IOA26	39	IOB26
40	IOA27	40	IOB27
41	IOA28	41	IOB28
42	IOA29	42	IOB29
43	IOA30	43	IOB30
44	IOA31	44	IOB31
45	GND	45	GND
46	GND	46	GND
47	IOA32	47	IOB32
48	IOA33	48	IOB33
49	IOA34	49	IOB34
50	IOA35	50	IOB35
51	IOA36	51	IOB36
52	IOA37	52	IOB37
53	IOA38	53	IOB38
54	IOA39	54	IOB39
55	GND	55	GND
56	GND	56	GND
57	IOA40	57	IOB40
58	IOA41	58	IOB41
59	IOA42	59	IOB42
60	IOA43	60	IOB43
61	IOA44	61	IOB44
62	IOA45	62	IOB45
63	IOA46	63	IOB46
64	IOA47	64	IOB47
65	IOA48	65	IOB48
66	IOA49	66	IOB49



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FPGA		
MRAM OE	U19	IO DIFFIO_RX R1p/5
MRAM DQ08	U17	IO DIFFIO_RX R1n/5
MRAM BE1	AA22	IO DIFFIO_RX R2n/5
MRAM BE0	AA21	IO DIFFIO_RX R2p/5
	R14	IO DIFFIO_RX R3n/5
MRAM A2	R15	IO DIFFIO_RX R3p/5
MRAM CE	T22	IO DIFFIO_RX R4n/5
MRAM DQ09	T18	IO DIFFIO_RX R4p/5
MRAM A0	T19	IO DIFFIO_RX R5n/5
MRAM DQ010	R20	IO DIFFIO_RX R5p/5
MRAM DQ012	T20	IO DIFFIO_RX R6n/5
MRAM A1	U22	IO DIFFIO_RX R6p/5
MRAM A3	U21	IO DIFFIO_RX R7n/5
	P14	IO DIFFIO_RX R7p/5
MRAM DQ02	N22	IO DIFFIO_RX R8n/5
MRAM DQ00	P21	IO DIFFIO_RX R8p/5
MRAM A5	P18	IO DIFFIO_RX R9n/5
MRAM DQ011	R18	IO DIFFIO_RX R9p/5
MRAM A4	P20	IO DIFFIO_RX R10n/5
MRAM A17	P19	IO DIFFIO_RX R10p/5
MRAM DQ06	L22	IO DIFFIO_RX R11p/5
MRAM DQ03	M21	IO DIFFIO_RX R12n/5
MRAM DQ04	M22	IO DIFFIO_RX R12p/5
MRAM DQ01	N21	IO DIFFIO_RX R13n/5
MRAM A4	R22	IO DIFFIO_RX R13p/5
	IO/5	

FPGA		
IOA18	E15	IO DIFFIO_RX T1n/7
IOA19	E16	IO DIFFIO_RX T1p/7
IOA14	C17	IO DIFFIO_RX T2n/7
IOA15	D17	IO DIFFIO_RX T2p/7
	D13	IO DIFFIO_RX T3n/7
	H14	IO DIFFIO_RX T3p/7
IOA21	G13	IO DIFFIO_RX T4n/7
IOA20	C14	IO DIFFIO_RX T4p/7
IOA22	B14	IO DIFFIO_RX T5n/7
IOA23	A14	IO DIFFIO_RX T5p/7
IOA25	E13	IO DIFFIO_RX T6n/7
IOA24	D14	IO DIFFIO_RX T6p/7
IOA26	D13	IO DIFFIO_RX T7n/7
IOA27	E12	IO DIFFIO_RX T7p/7
IOA28	J11	IO DIFFIO_RX T8n/7
IOA29	H12	IO DIFFIO_RX T8p/7
IOA35	A8	IO DIFFIO_RX T9n/7
IOA34	A7	IO DIFFIO_RX T9p/7
IOA31	C8	IO DIFFIO_RX T10n/7
IOA30	B10	IO DIFFIO_RX T10p/7
IOA32	A9	IO DIFFIO_RX T11n/7
IOA33	B8	IO DIFFIO_RX T11p/7
	IO/7	

FPGA		
IOA18	E15	IO DIFFIO_RX T12n/8
IOA19	E16	IO DIFFIO_RX T12p/8
IOA14	C17	IO DIFFIO_RX T13n/8
IOA15	D17	IO DIFFIO_RX T13p/8
	D13	IO DIFFIO_RX T14n/8
	H14	IO DIFFIO_RX T14p/8
IOA21	G13	IO DIFFIO_RX T15n/8
IOA20	C14	IO DIFFIO_RX T15p/8
IOA22	B14	IO DIFFIO_RX T16n/8
IOA23	A14	IO DIFFIO_RX T16p/8
IOA25	E13	IO DIFFIO_RX T17n/8
IOA24	D14	IO DIFFIO_RX T17p/8
IOA26	D13	IO DIFFIO_RX T18n/8
IOA27	E12	IO DIFFIO_RX T18p/8
IOA28	J11	IO DIFFIO_RX T19n/8
IOA29	H12	IO DIFFIO_RX T19p/8
IOA35	A8	IO DIFFIO_RX T20n/8
IOA34	A7	IO DIFFIO_RX T20p/8
IOA31	C8	IO DIFFIO_RX T21n/8
IOA30	B10	IO DIFFIO_RX T21p/8
IOA32	A9	IO DIFFIO_RX T22n/8
IOA33	B8	IO DIFFIO_RX T22p/8
	IO/8	

FPGA		
MRAM OE	U19	IO DIFFIO_RX R14n_CLK2n/6
MRAM DQ08	U17	IO DIFFIO_RX R14p_CLK2p/6
MRAM BE1	AA22	IO DIFFIO_RX R15n/6
MRAM BE0	AA21	IO DIFFIO_RX R15p/6
	K21	IO DIFFIO_RX R16n_CLK3n/6
	J21	IO DIFFIO_RX R16p_CLK3p/6
MRAM A1	J22	IO DIFFIO_RX R17n/6
MRAM WE	G19	IO DIFFIO_RX R17p/6
MRAM A8	G20	IO DIFFIO_RX R18n/6
MRAM A7	F22	IO DIFFIO_RX R18p/6
MRAM A5	G22	IO DIFFIO_RX R19n/6
MRAM DQ07	M14	IO DIFFIO_RX R19p/6
MRAM A12	M15	IO DIFFIO_RX R20n/6
IOA6	E22	IO DIFFIO_RX R20p/6
MRAM A7	N19	IO DIFFIO_RX R21n/6
MRAM A16	N18	IO DIFFIO_RX R21p/6
MRAM A6	N20	IO DIFFIO_RX R22n/6
MRAM DQ05	M20	IO DIFFIO_RX R22p/6
MRAM DQ014	F21	IO DIFFIO_RX R23n/6
IOA8	C22	IO DIFFIO_RX R23p/6
IOA9	D22	IO DIFFIO_RX R24n/6
IOA46	L15	IO DIFFIO_RX R24p/6
IOA47	L14	IO DIFFIO_RX R25n/6
MRAM A13	L18	IO DIFFIO_RX R25p/6
MRAM A15	M18	IO DIFFIO_RX R26n/6
MRAM A11	L20	IO DIFFIO_RX R26p/6
MRAM A10	F18	IO DIFFIO_RX R27n/6
MRAM DQ013	E19	IO DIFFIO_RX R27p/6
MRAM A9	F18	IO DIFFIO_RX R28n/6
IOA16	K15	IO DIFFIO_RX R28p/6
IOA17	K14	IO DIFFIO_RX R29n/6
IOA10	D19	IO DIFFIO_RX R29p/6
IOA11	C20	IO DIFFIO_RX R30n/6
IOA44	G17	IO DIFFIO_RX R30p/6
IOA45	H17	IO DIFFIO_RX R31n/6
IOA13	E18	IO DIFFIO_RX R31p/6
IOA12	D18	IO DIFFIO_RX R32n/6
	D21	IO DIFFIO_RX R32p/6
	IO_VREFB6N0/6	
	IO/6	

FPGA		
MRAM OE	U19	IO DIFFIO_RX R14n_CLK2n/6
MRAM DQ08	U17	IO DIFFIO_RX R14p_CLK2p/6
MRAM BE1	AA22	IO DIFFIO_RX R15n/6
MRAM BE0	AA21	IO DIFFIO_RX R15p/6
	K21	IO DIFFIO_RX R16n_CLK3n/6
	J21	IO DIFFIO_RX R16p_CLK3p/6
MRAM A1	J22	IO DIFFIO_RX R17n/6
MRAM WE	G19	IO DIFFIO_RX R17p/6
MRAM A8	G20	IO DIFFIO_RX R18n/6
MRAM A7	F22	IO DIFFIO_RX R18p/6
MRAM A5	G22	IO DIFFIO_RX R19n/6
MRAM DQ07	M14	IO DIFFIO_RX R19p/6
MRAM A12	M15	IO DIFFIO_RX R20n/6
IOA6	E22	IO DIFFIO_RX R20p/6
MRAM A7	N19	IO DIFFIO_RX R21n/6
MRAM A16	N18	IO DIFFIO_RX R21p/6
MRAM A6	N20	IO DIFFIO_RX R22n/6
MRAM DQ05	M20	IO DIFFIO_RX R22p/6
MRAM DQ014	F21	IO DIFFIO_RX R23n/6
IOA8	C22	IO DIFFIO_RX R23p/6
IOA9	D22	IO DIFFIO_RX R24n/6
IOA46	L15	IO DIFFIO_RX R24p/6
IOA47	L14	IO DIFFIO_RX R25n/6
MRAM A13	L18	IO DIFFIO_RX R25p/6
MRAM A15	M18	IO DIFFIO_RX R26n/6
MRAM A11	L20	IO DIFFIO_RX R26p/6
MRAM A10	F18	IO DIFFIO_RX R27n/6
MRAM DQ013	E19	IO DIFFIO_RX R27p/6
MRAM A9	F18	IO DIFFIO_RX R28n/6
IOA16	K15	IO DIFFIO_RX R28p/6
IOA17	K14	IO DIFFIO_RX R29n/6
IOA10	D19	IO DIFFIO_RX R29p/6
IOA11	C20	IO DIFFIO_RX R30n/6
IOA44	G17	IO DIFFIO_RX R30p/6
IOA45	H17	IO DIFFIO_RX R31n/6
IOA13	E18	IO DIFFIO_RX R31p/6
IOA12	D18	IO DIFFIO_RX R32n/6
	D21	IO_VREFB6N0/6
	IO/6	

FPGA		
MRAM OE	U19	IO DIFFIO_RX R14n_CLK2n/6
MRAM DQ08	U17	IO DIFFIO_RX R14p_CLK2p/6
MRAM BE1	AA22	IO DIFFIO_RX R15n/6
MRAM BE0	AA21	IO DIFFIO_RX R15p/6
	K21	IO DIFFIO_RX R16n_CLK3n/6
	J21	IO DIFFIO_RX R16p_CLK3p/6
MRAM A1	J22	IO DIFFIO_RX R17n/6
MRAM WE	G19	IO DIFFIO_RX R17p/6
MRAM A8	G20	IO DIFFIO_RX R18n/6
MRAM A7	F22	IO DIFFIO_RX R18p/6
MRAM A5	G22	IO DIFFIO_RX R19n/6
MRAM DQ07	M14	IO DIFFIO_RX R19p/6
MRAM A12	M15	IO DIFFIO_RX R20n/6
IOA6	E22	IO DIFFIO_RX R20p/6
MRAM A7	N19	IO DIFFIO_RX R21n/6
MRAM A16	N18	IO DIFFIO_RX R21p/6
MRAM A6	N20	IO DIFFIO_RX R22n/6
MRAM DQ05	M20	IO DIFFIO_RX R22p/6
MRAM DQ014	F21	IO DIFFIO_RX R23n/6
IOA8	C22	IO DIFFIO_RX R23p/6
IOA9	D22	IO DIFFIO_RX R24n/6
IOA46	L15	IO DIFFIO_RX R24p/6
IOA47	L14	IO DIFFIO_RX R25n/6
MRAM A13	L18	IO DIFFIO_RX R25p/6
MRAM A15	M18	IO DIFFIO_RX R26n/6
MRAM A11	L20	IO DIFFIO_RX R26p/6
MRAM A10	F18	IO DIFFIO_RX R27n/6
MRAM DQ013	E19	IO DIFFIO_RX R27p/6
MRAM A9	F18	IO DIFFIO_RX R28n/6
IOA16	K15	IO DIFFIO_RX R28p/6
IOA17	K14	IO DIFFIO_RX R29n/6
IOA10	D19	IO DIFFIO_RX R29p/6
IOA11	C20	IO DIFFIO_RX R30n/6
IOA44	G17	IO DIFFIO_RX R30p/6
IOA45	H17	IO DIFFIO_RX R31n/6
IOA13	E18	IO DIFFIO_RX R31p/6
IOA12	D18	IO DIFFIO_RX R32n/6
	D21	IO_VREFB6N0/6
	IO/6	

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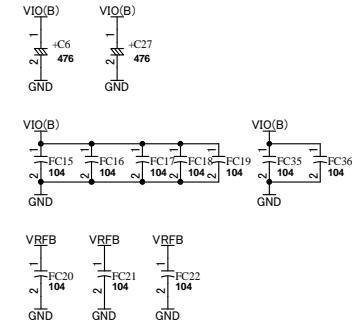
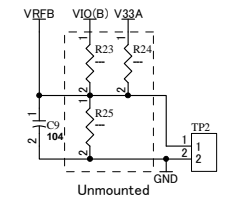
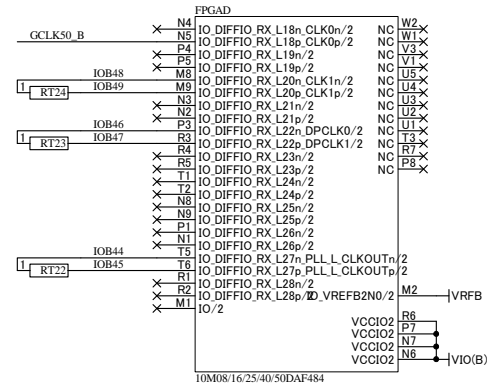
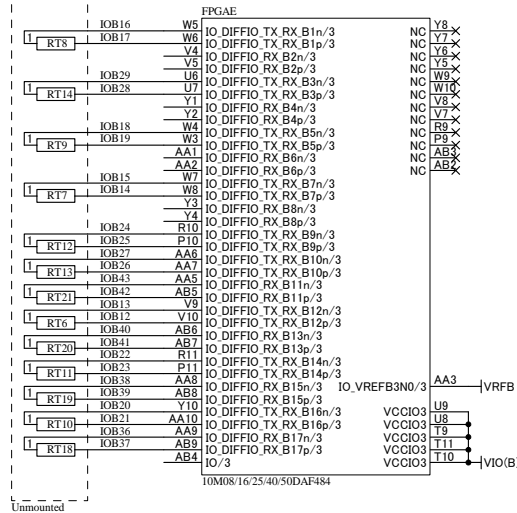
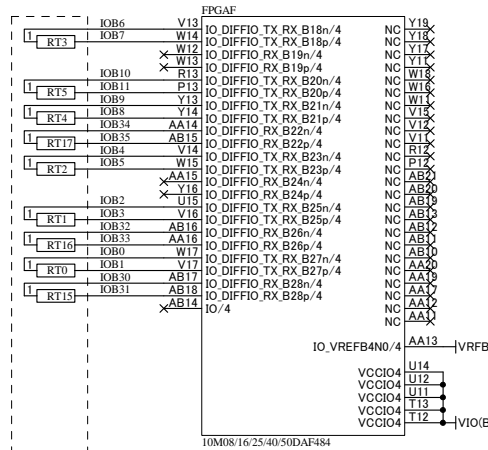
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V12 V12
 V25 V25
 V33A V33A
 VIO(B) VIO(B)
 GND GND

IOB[0..49] IOB[0..49]
 IOA[40..47] IOA[40..47]
 GCLK B GCLK50 B



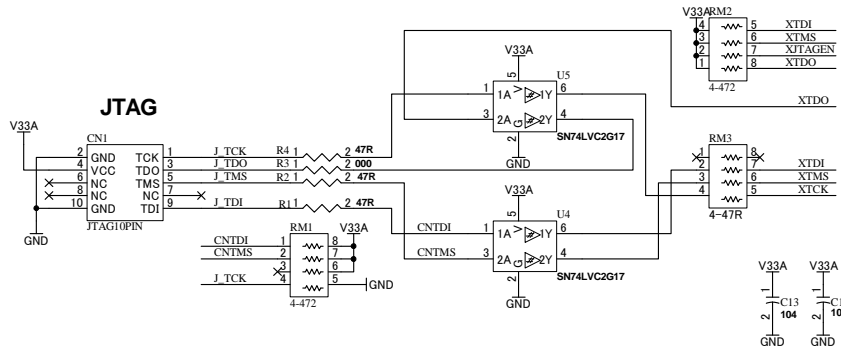
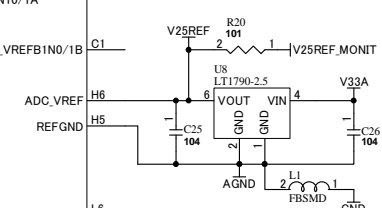
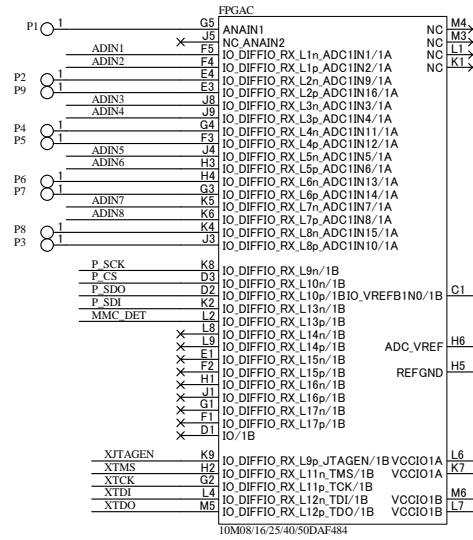
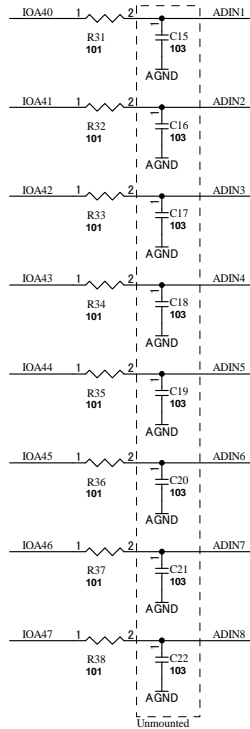
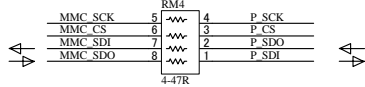
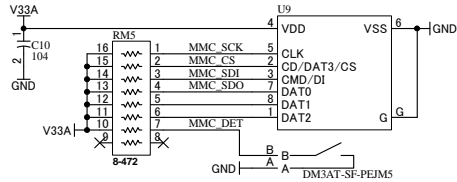
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V12 V12
V25 V25
V33A V33A
VIO(B) VIO(B)
GND GND

IOA[0..49] IOA[0..49]



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