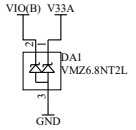

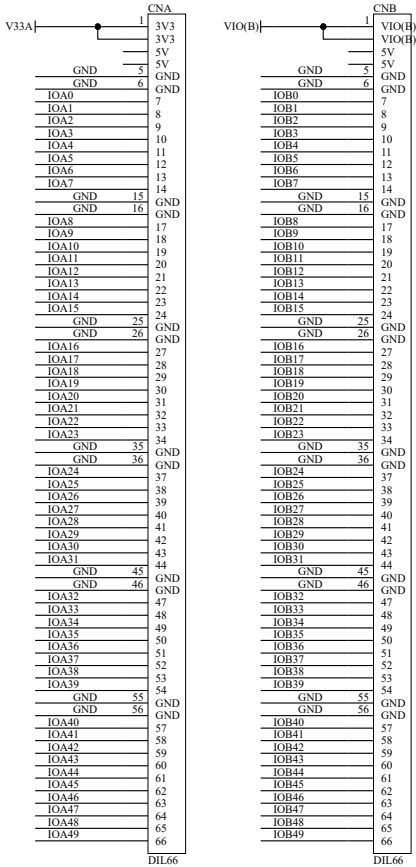

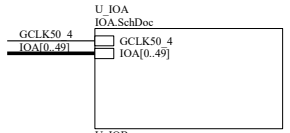

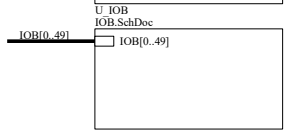

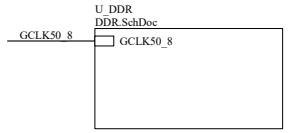

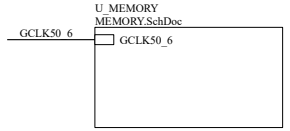

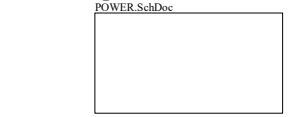

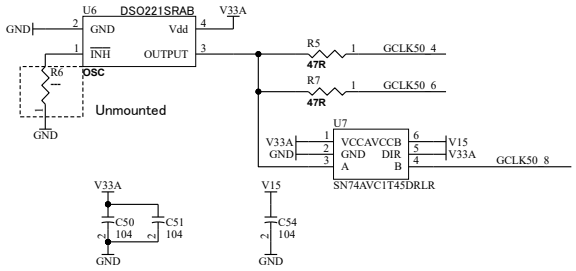

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	DOC. No: ACM-028	B3
	FILE: ACM028.sch DATE: 2022/04/13 11:52:49	Sheet: 1 / 8

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Bank Group A(3.3V)

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	DOC. No: ACM-028	B3
	FILE: IOA.SchDoc DATE: 2022/04/13 11:52:49	Sheet: 2 / 8

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Bank Group B

FPGAB

	AE10	IO, (DIFFIO_TX_B25p, DIFFOUT_B25p, DQ3B, DQ1B)
	AF10	IO, (DIFFIO_TX_B25n, DIFFOUT_B25n)
	AD12	IO, (DIFFIO_RX_B26p, DIFFOUT_B26p, DQ3B, DQ1B)
	AD13	IO, (DIFFIO_RX_B26n, DIFFOUT_B26n, DQ3B, DQ1B)
	V12	IO, (DIFFIO_RX_B27p, DIFFOUT_B27p, DQS3B, DQ1B)
	W12	IO, (DIFFIO_RX_B27n, DIFFOUT_B27n, DQSn3B, DQ1B)
	AJ1	IO, (DIFFIO_TX_B28p, DIFFOUT_B28p)
	AJ2	IO, (DIFFIO_TX_B28n, DIFFOUT_B28n, DQ3B, DQ1B)
	AJ3	IO, (DIFFIO_TX_B29p, DIFFOUT_B29p, DQ3B, DQ1B)
	AK3	IO, (DIFFIO_TX_B29n, DIFFOUT_B29n, DQ3B, DQ1B)
	AE12	IO, (DIFFIO_RX_B30p, DIFFOUT_B30p, DQ3B, DQ1B)
	AE13	IO, (DIFFIO_RX_B30n, DIFFOUT_B30n, DQ3B, DQ1B)
IOB43	AB12	IO, (DIFFIO_RX_B31p, DIFFOUT_B31p)
IOB42	AB13	IO, (DIFFIO_RX_B31n, DIFFOUT_B31n)
IOB29	AJ4	IO, (DIFFIO_TX_B32p, DIFFOUT_B32p, DQ3B, DQ1B)
IOB28	AJ5	IO, (DIFFIO_TX_B32n, DIFFOUT_B32n, DQ3B, DQ1B)
	AK5	IO, (DIFFIO_TX_B33p, DIFFOUT_B33p, DQ4B, DQ1B)
	AK6	IO, (DIFFIO_TX_B33n, DIFFOUT_B33n)
	AF13	IO, (DIFFIO_RX_B34p, DIFFOUT_B34p, DQ4B, DQ1B)
	AG12	IO, (DIFFIO_RX_B34n, DIFFOUT_B34n, DQ4B, DQ1B)
	Y12	IO, (DIFFIO_RX_B35p, DIFFOUT_B35p, DQS4B, DQS1B)
	AA13	IO, (DIFFIO_RX_B35n, DIFFOUT_B35n, DQSn4B, DQSn1B)
IOB27	AJ7	IO, (DIFFIO_TX_B36p, DIFFOUT_B36p)
IOB26	AK7	IO, (DIFFIO_TX_B36n, DIFFOUT_B36n, DQ4B, DQ1B)
IOB20	AJ8	IO, (DIFFIO_TX_B37p, DIFFOUT_B37p, DQ4B, DQ1B)
IOB21	AK8	IO, (DIFFIO_TX_B37n, DIFFOUT_B37n, DQ4B, DQ1B)
	AF11	IO, (DIFFIO_RX_B38p, DIFFOUT_B38p, DQ4B, DQ1B)
	AG11	IO, (DIFFIO_RX_B38n, DIFFOUT_B38n, DQ4B, DQ1B)
	AB14	IO, CLK0p, FPLL_BL_FBp, (DIFFIO_RX_B39p, DIFFOUT_B39p)
	AC14	IO, CLK0n, FPLL_BL_FBn, (DIFFIO_RX_B39n, DIFFOUT_B39n)
IOB34	AF9	IO, (DIFFIO_TX_B40p, DIFFOUT_B40p, DQ4B, DQ1B)
IOB35	AG9	IO, (DIFFIO_TX_B40n, DIFFOUT_B40n, DQ4B, DQ1B)
	AH9	IO, (DIFFIO_TX_B41p, DIFFOUT_B41p, DQ5B)
	AJ9	IO, (DIFFIO_TX_B41n, DIFFOUT_B41n)
IOB25	AH10	IO, (DIFFIO_RX_B42p, DIFFOUT_B42p, DQ5B)
IOB24	AJ10	IO, (DIFFIO_RX_B42n, DIFFOUT_B42n, DQ5B)
IOB45	Y13	IO, (DIFFIO_RX_B43p, DIFFOUT_B43p, DQS5B)
IOB44	AA14	IO, (DIFFIO_RX_B43n, DIFFOUT_B43n, DQSn5B)
IOB18	AK10	IO, (DIFFIO_TX_B44p, DIFFOUT_B44p)
IOB19	AK11	IO, (DIFFIO_TX_B44n, DIFFOUT_B44n, DQ5B)
IOB22	AH11	IO, FPLL_BL_CLKOUT0, FPLL_BL_CLKOUTp, FPLL_BL_FB, (DIFFIO_TX_B45p, ..p, DQ5B)
IOB23	AH12	IO, FPLL_BL_CLKOUT1, FPLL_BL_CLKOUTn, (DIFFIO_TX_B45n, DIFFOUT_B45n, DQ5B)
	AG13	IO, (DIFFIO_RX_B46p, DIFFOUT_B46p, DQ5B)
	AG14	IO, (DIFFIO_RX_B46n, DIFFOUT_B46n, DQ5B)
IOB46	Y15	IO, CLK1p, (DIFFIO_RX_B47p, DIFFOUT_B47p)
IOB47	AA15	IO, CLK1n, (DIFFIO_RX_B47n, DIFFOUT_B47n)
IOB16	AJ12	IO, (DIFFIO_TX_B48p, DIFFOUT_B48p, DQ5B)
IOB17	AK12	IO, (DIFFIO_TX_B48n, DIFFOUT_B48n, DQ5B)

VREFB3BN0	AJ13	VREFB
VCCPD3B4A	AC12	VCCPDB
VCCPD3B4A	AC17	
VCCPD3B4A	AC20	
VCCPD3B4A	AD15	
VCCPD3B4A	AE11	
VCCPD3B4A	AE21	
VCCIO3B	AA12	VIO(B)
VCCIO3B	AE14	
VCCIO3B	AG10	
VCCIO3B	AH13	
VCCIO3B	AK4	
VCCIO3B	AK9	

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FPGAA

	U11	IO, DATA8, (DIFFIO_RX_B1p, DIFFOUT_B1p, DQ1B)
	U12	IO, DATA6, (DIFFIO_RX_B1n, DIFFOUT_B1n, DQ1B)
	Y10	IO, DATA7, (DIFFIO_TX_B2p, DIFFOUT_B2p, DQ1B)
	AA10	IO, DATA5, (DIFFIO_TX_B2n, DIFFOUT_B2n)
	AA11	IO, DATA12, (DIFFIO_RX_B3p, DIFFOUT_B3p, DQS1B)
	Y11	IO, DATA10, (DIFFIO_RX_B3n, DIFFOUT_B3n, DQSn1B)
	AC9	IO, DATA11, (DIFFIO_TX_B4p, DIFFOUT_B4p)
	AD9	IO, DATA9, (DIFFIO_TX_B4n, DIFFOUT_B4n, DQ1B)
IOB39	T11	IO, CLKUSR, (DIFFIO_RX_B5p, DIFFOUT_B5p, DQ1B)
IOB38	R10	IO, DATA14, (DIFFIO_RX_B5n, DIFFOUT_B5n, DQ1B)
	V9	IO, DATA15, (DIFFIO_TX_B6p, DIFFOUT_B6p, DQ1B)
	W10	IO, DATA13, (DIFFIO_TX_B6n, DIFFOUT_B6n, DQ1B)
	V11	IO, PR_ERROR, (DIFFIO_RX_B7p, DIFFOUT_B7p)
	V10	IO, PR_DONE, (DIFFIO_RX_B7n, DIFFOUT_B7n)
	AF7	IO, (DIFFIO_TX_B8p, DIFFOUT_B8p, DQ1B)
	AF6	IO, PR_READY, (DIFFIO_TX_B8n, DIFFOUT_B8n, DQ1B)
	AA9	IO, (DIFFIO_TX_B9p, DIFFOUT_B9p, DQ2B)
	AB9	IO, (DIFFIO_TX_B9n, DIFFOUT_B9n)
IOB30	AG6	IO, (DIFFIO_RX_B10p, DIFFOUT_B10p, DQ2B)
IOB31	AH6	IO, (DIFFIO_RX_B10n, DIFFOUT_B10n, DQ2B)
IOB36	T9	IO, (DIFFIO_RX_B11p, DIFFOUT_B11p, DQS2B)
IOB37	U8	IO, (DIFFIO_RX_B11n, DIFFOUT_B11n, DQSn2B)
IOB32	AF8	IO, (DIFFIO_TX_B12p, DIFFOUT_B12p)
IOB33	AG8	IO, (DIFFIO_TX_B12n, DIFFOUT_B12n, DQ2B)
	AA8	IO, (DIFFIO_TX_B13p, DIFFOUT_B13p, DQ2B)
	AB8	IO, (DIFFIO_TX_B13n, DIFFOUT_B13n, DQ2B)
	AH4	IO, (DIFFIO_RX_B14p, DIFFOUT_B14p, DQ2B)
	AH5	IO, (DIFFIO_RX_B14n, DIFFOUT_B14n, DQ2B)
IOB40	T10	IO, (DIFFIO_RX_B15p, DIFFOUT_B15p)
IOB41	U9	IO, (DIFFIO_RX_B15n, DIFFOUT_B15n)
	AG7	IO, (DIFFIO_TX_B16p, DIFFOUT_B16p, DQ2B)
	AH7	IO, (DIFFIO_TX_B16n, DIFFOUT_B16n, DQ2B)

VREFB3AN0	AE8	VREFB
VCCPD3A	AB11	VCCPDB
VCCPD3A	AD10	
VCCIO3A	U10	VIO(B)
VCCIO3A	Y9	
VCCIO3A	AC8	
VCCIO3A	AD11	

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FPGAC

	AJ14	IO, (DIFFIO_TX_B49p, DIFFOUT_B49p, DQ6B)
	AK13	IO, RZQ_0, (DIFFIO_TX_B49n, DIFFOUT_B49n)
	AE16	IO, (DIFFIO_RX_B50p, DIFFOUT_B50p, DQ6B)
	AF15	IO, (DIFFIO_RX_B50n, DIFFOUT_B50n, DQ6B)
	Y16	IO, (DIFFIO_RX_B51p, DIFFOUT_B51p, DQS6B)
	AA16	IO, (DIFFIO_RX_B51n, DIFFOUT_B51n, DQSn6B)
	AH14	IO, (DIFFIO_TX_B52p, DIFFOUT_B52p)
	AH15	IO, (DIFFIO_TX_B52n, DIFFOUT_B52n, DQ6B)
IOB15	AJ15	IO, (DIFFIO_TX_B53p, DIFFOUT_B53p, DQ6B)
IOB14	AK15	IO, (DIFFIO_TX_B53n, DIFFOUT_B53n, DQ6B)
	AD17	IO, (DIFFIO_RX_B54p, DIFFOUT_B54p, DQ6B)
	AE17	IO, (DIFFIO_RX_B54n, DIFFOUT_B54n, DQ6B)
	AB16	IO, CLK2p, (DIFFIO_RX_B55p, DIFFOUT_B55p)
	AC15	IO, CLK2n, (DIFFIO_RX_B55n, DIFFOUT_B55n)
	AE15	IO, (DIFFIO_TX_B56p, DIFFOUT_B56p, DQ6B)
	AF14	IO, (DIFFIO_TX_B56n, DIFFOUT_B56n, DQ6B)
	AG17	IO, (DIFFIO_TX_B57p, DIFFOUT_B57p, DQ7B, DQ2B)
	AH17	IO, (DIFFIO_TX_B57n, DIFFOUT_B57n)
IOB12	AK16	IO, (DIFFIO_RX_B58p, DIFFOUT_B58p, DQ7B, DQ2B)
IOB13	AK17	IO, (DIFFIO_RX_B58n, DIFFOUT_B58n, DQ7B, DQ2B)
	Y17	IO, (DIFFIO_RX_B59p, DIFFOUT_B59p, DQS7B, DQ2B)
	Y18	IO, (DIFFIO_RX_B59n, DIFFOUT_B59n, DQSn7B, DQ2B)
	AJ17	IO, (DIFFIO_TX_B60p, DIFFOUT_B60p)
	AJ18	IO, (DIFFIO_TX_B60n, DIFFOUT_B60n, DQ7B, DQ2B)
IOB11	AJ19	IO, (DIFFIO_TX_B61p, DIFFOUT_B61p, DQ7B, DQ2B)
IOB10	AK18	IO, (DIFFIO_TX_B61n, DIFFOUT_B61n, DQ7B, DQ2B)
	AF16	IO, (DIFFIO_RX_B62p, DIFFOUT_B62p, DQ7B, DQ2B)
	AG16	IO, (DIFFIO_RX_B62n, DIFFOUT_B62n, DQ7B, DQ2B)
IOB49	AB17	IO, CLK3p, (DIFFIO_RX_B63p, DIFFOUT_B63p)
IOB48	AB18	IO, CLK3n, (DIFFIO_RX_B63n, DIFFOUT_B63n)
	AH19	IO, (DIFFIO_TX_B64p, DIFFOUT_B64p, DQ7B, DQ2B)
	AH20	IO, (DIFFIO_TX_B64n, DIFFOUT_B64n, DQ7B, DQ2B)
IOB9	AJ20	IO, (DIFFIO_TX_B65p, DIFFOUT_B65p, DQ8B, DQ2B)
IOB8	AK20	IO, (DIFFIO_TX_B65n, DIFFOUT_B65n)
	AD18	IO, (DIFFIO_RX_B66p, DIFFOUT_B66p, DQ8B, DQ2B)
	AE18	IO, (DIFFIO_RX_B66n, DIFFOUT_B66n, DQ8B, DQ2B)
	Y20	IO, (DIFFIO_RX_B67p, DIFFOUT_B67p, DQS8B, DQS2B)
	AA20	IO, (DIFFIO_RX_B67n, DIFFOUT_B67n, DQSn8B, DQSn2B)
IOB7	AK21	IO, (DIFFIO_TX_B68p, DIFFOUT_B68p)
IOB6	AK22	IO, (DIFFIO_TX_B68n, DIFFOUT_B68n, DQ8B, DQ2B)
	AH21	IO, (DIFFIO_TX_B69p, DIFFOUT_B69p, DQ8B, DQ2B)
	AJ22	IO, (DIFFIO_TX_B69n, DIFFOUT_B69n, DQ8B, DQ2B)
	AF18	IO, (DIFFIO_TX_B70p, DIFFOUT_B70p, DQ8B, DQ2B)
	AF19	IO, (DIFFIO_TX_B70n, DIFFOUT_B70n, DQ8B, DQ2B)
	AA18	IO, (DIFFIO_RX_B71p, DIFFOUT_B71p)
	AA19	IO, (DIFFIO_RX_B71n, DIFFOUT_B71n)
IOB4	AJ23	IO, (DIFFIO_TX_B72p, DIFFOUT_B72p, DQ8B, DQ2B)
IOB5	AK23	IO, (DIFFIO_TX_B72n, DIFFOUT_B72n, DQ8B, DQ2B)
	AH24	IO, (DIFFIO_TX_B73p, DIFFOUT_B73p, DQ9B, DQ3B)
	AJ24	IO, (DIFFIO_TX_B73n, DIFFOUT_B73n)
	AG18	IO, (DIFFIO_RX_B74p, DIFFOUT_B74p, DQ9B, DQ3B)
	AG19	IO, (DIFFIO_RX_B74n, DIFFOUT_B74n, DQ9B, DQ3B)
	AB19	IO, (DIFFIO_RX_B75p, DIFFOUT_B75p, DQS9B, DQ3B)
	AC19	IO, (DIFFIO_RX_B75n, DIFFOUT_B75n, DQSn9B, DQ3B)
IOB2	AJ25	IO, (DIFFIO_TX_B76p, DIFFOUT_B76p)
IOB3	AK25	IO, (DIFFIO_TX_B76n, DIFFOUT_B76n, DQ9B, DQ3B)
	AG24	IO, (DIFFIO_TX_B77p, DIFFOUT_B77p, DQ9B, DQ3B)
	AH25	IO, (DIFFIO_TX_B77n, DIFFOUT_B77n, DQ9B, DQ3B)
	AD19	IO, (DIFFIO_RX_B78p, DIFFOUT_B78p, DQ9B, DQ3B)
	AE20	IO, (DIFFIO_RX_B78n, DIFFOUT_B78n, DQ9B, DQ3B)
	AA21	IO, (DIFFIO_RX_B79p, DIFFOUT_B79p)
	AB21	IO, (DIFFIO_RX_B79n, DIFFOUT_B79n)
IOB0	AJ27	IO, (DIFFIO_TX_B80p, DIFFOUT_B80p, DQ9B, DQ3B)
IOB1	AK26	IO, (DIFFIO_TX_B80n, DIFFOUT_B80n, DQ9B, DQ3B)
	AK27	IO, (DIFFIO_TX_B81p, DIFFOUT_B81p, DQ10B, DQ3B)
	AK28	IO, (DIFFIO_TX_B81n, DIFFOUT_B81n)
	AF20	IO, (DIFFIO_RX_B82p, DIFFOUT_B82p, DQ10B, DQ3B)
	AG21	IO, (DIFFIO_RX_B82n, DIFFOUT_B82n, DQ10B, DQ3B)
	AC21	IO, (DIFFIO_RX_B83p, DIFFOUT_B83p, DQS10B, DQS3B)
	AD20	IO, (DIFFIO_RX_B83n, DIFFOUT_B83n, DQSn10B, DQSn3B)
	AG26	IO, (DIFFIO_TX_B84p, DIFFOUT_B84p)
	AH26	IO, (DIFFIO_TX_B84n, DIFFOUT_B84n, DQ10B, DQ3B)
	AE22	IO, (DIFFIO_TX_B85p, DIFFOUT_B85p, DQ10B, DQ3B)
	AF23	IO, (DIFFIO_TX_B85n, DIFFOUT_B85n, DQ10B, DQ3B)
	AF21	IO, (DIFFIO_RX_B86p, DIFFOUT_B86p, DQ10B, DQ3B)
	AG22	IO, (DIFFIO_RX_B86n, DIFFOUT_B86n, DQ10B, DQ3B)
	AB22	IO, (DIFFIO_TX_B87p, DIFFOUT_B87p)
	AC22	IO, (DIFFIO_TX_B87n, DIFFOUT_B87n)
	AG23	IO, (DIFFIO_TX_B88p, DIFFOUT_B88p, DQ10B, DQ3B)
	AH22	IO, (DIFFIO_TX_B88n, DIFFOUT_B88n, DQ10B, DQ3B)

VREFB3AN0	AH16	VREFB
VCCIO4A	AB15	VIO(B)
VCCIO4A	AC18	
VCCIO4A	AD21	
VCCIO4A	AF17	
VCCIO4A	AG20	
VCCIO4A	AH23	
VCCIO4A	AJ16	
VCCIO4A	AJ26	
VCCIO4A	AK19	

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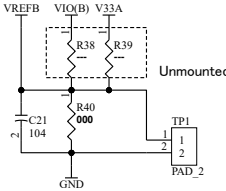

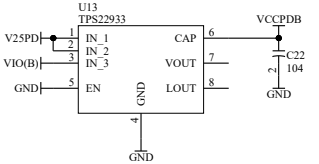

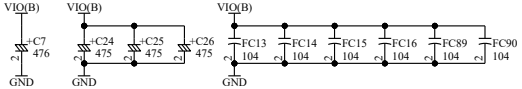

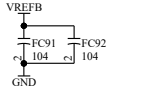

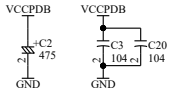

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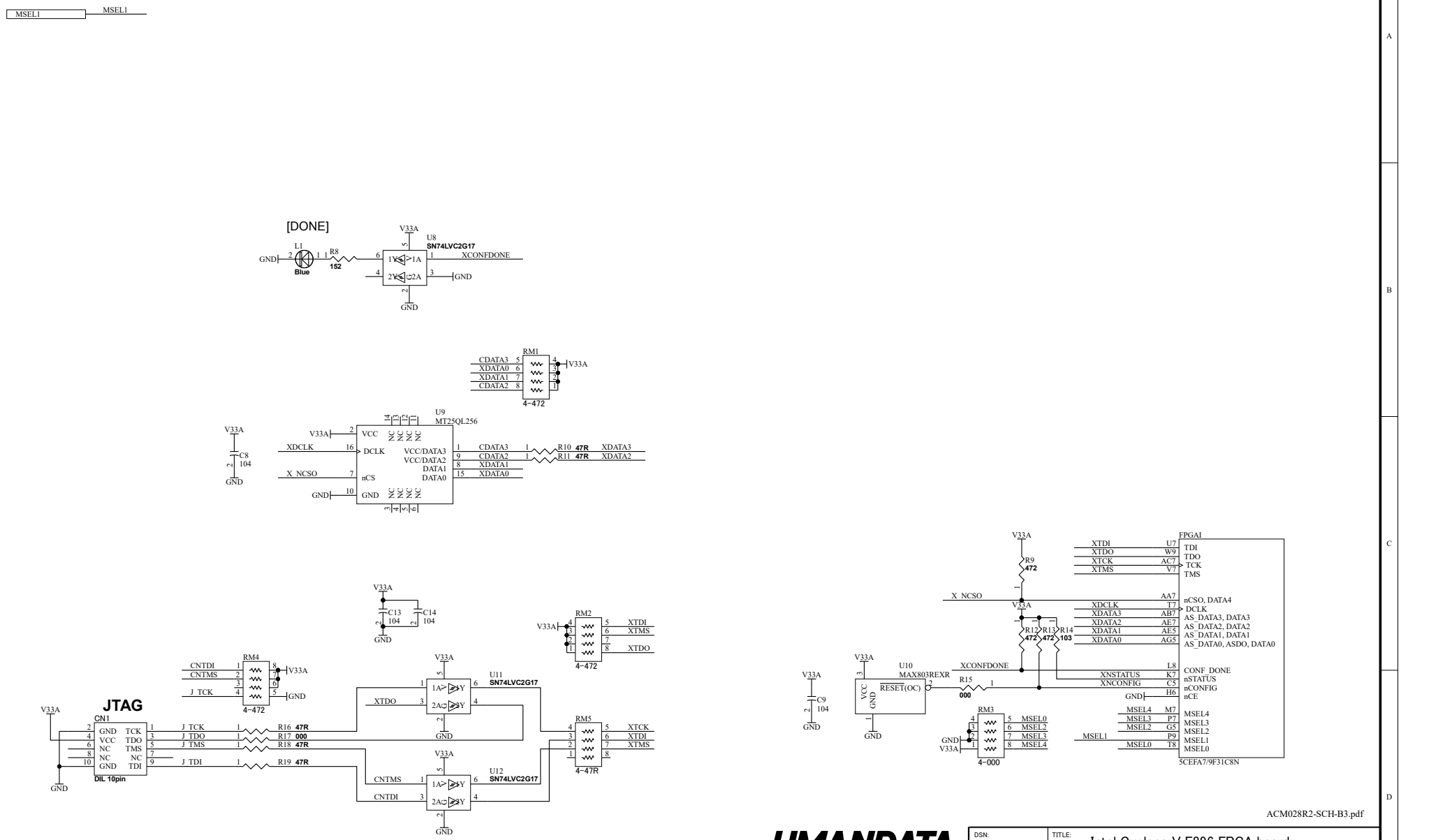

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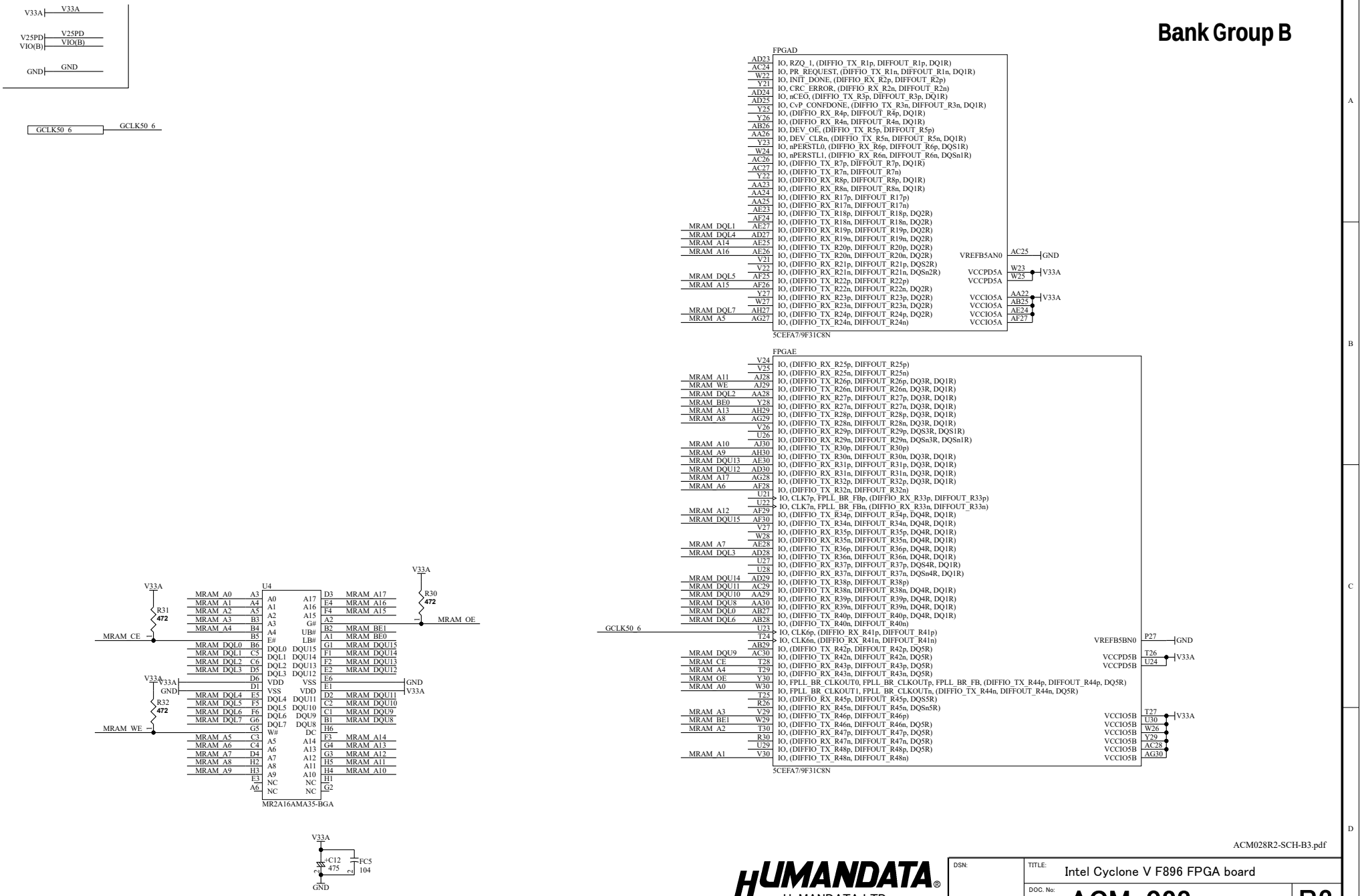
Bank Group B


V33A
V25PD
VIO(B)
GND
GCLK50_6
FPGAD
IO, RZQ_1, (DIFFIO_TX_R1p, DIFFOUT_R1p, DQ1R)
IO, PR_REQUEST, (DIFFIO_TX_R1n, DIFFOUT_R1n, DQ1R)
IO, INIT_DONE, (DIFFIO_RX_R2p, DIFFOUT_R2p)
IO, CRC_ERROR, (DIFFIO_RX_R2n, DIFFOUT_R2n)
IO, nCEO, (DIFFIO_TX_R3p, DIFFOUT_R3p, DQ1R)
IO, CvP_CONFDONE, (DIFFIO_TX_R3n, DIFFOUT_R3n, DQ1R)
IO, (DIFFIO_RX_R4p, DIFFOUT_R4p, DQ1R)
IO, (DIFFIO_RX_R4n, DIFFOUT_R4n, DQ1R)
IO, DEV_OE, (DIFFIO_TX_R5p, DIFFOUT_R5p)
IO, DEV_CLRn, (DIFFIO_TX_R5n, DIFFOUT_R5n, DQ1R)
IO, nPERSTL0, (DIFFIO_RX_R6p, DIFFOUT_R6p, DQS1R)
IO, nPERSTL1, (DIFFIO_RX_R6n, DIFFOUT_R6n, DQSn1R)
IO, (DIFFIO_TX_R7p, DIFFOUT_R7p, DQ1R)
IO, (DIFFIO_TX_R7n, DIFFOUT_R7n)
IO, (DIFFIO_RX_R8p, DIFFOUT_R8p, DQ1R)
IO, (DIFFIO_RX_R8n, DIFFOUT_R8n, DQ1R)
IO, (DIFFIO_RX_R17p, DIFFOUT_R17p)
IO, (DIFFIO_RX_R17n, DIFFOUT_R17n)
IO, (DIFFIO_TX_R18p, DIFFOUT_R18p, DQ2R)
IO, (DIFFIO_TX_R18n, DIFFOUT_R18n, DQ2R)
IO, (DIFFIO_RX_R19p, DIFFOUT_R19p, DQ2R)
IO, (DIFFIO_RX_R19n, DIFFOUT_R19n, DQ2R)
IO, (DIFFIO_TX_R20p, DIFFOUT_R20p, DQ2R)
IO, (DIFFIO_TX_R20n, DIFFOUT_R20n, DQ2R)
IO, (DIFFIO_RX_R21p, DIFFOUT_R21p, DQS2R)
IO, (DIFFIO_RX_R21n, DIFFOUT_R21n, DQSn2R)
IO, (DIFFIO_TX_R22p, DIFFOUT_R22p)
IO, (DIFFIO_TX_R22n, DIFFOUT_R22n, DQ2R)
IO, (DIFFIO_RX_R23p, DIFFOUT_R23p, DQ2R)
IO, (DIFFIO_RX_R23n, DIFFOUT_R23n, DQ2R)
IO, (DIFFIO_TX_R24p, DIFFOUT_R24p, DQ2R)
IO, (DIFFIO_TX_R24n, DIFFOUT_R24n)
VREFB5AN0
VCCPD5A
VCCIO5A
5CEFA7/9F31C8N
FPGAE
IO, (DIFFIO_RX_R25p, DIFFOUT_R25p)
IO, (DIFFIO_RX_R25n, DIFFOUT_R25n)
IO, (DIFFIO_TX_R26p, DIFFOUT_R26p, DQ3R, DQ1R)
IO, (DIFFIO_TX_R26n, DIFFOUT_R26n, DQ3R, DQ1R)
IO, (DIFFIO_RX_R27p, DIFFOUT_R27p, DQ3R, DQ1R)
IO, (DIFFIO_RX_R27n, DIFFOUT_R27n, DQ3R, DQ1R)
IO, (DIFFIO_RX_R28p, DIFFOUT_R28p, DQ3R, DQ1R)
IO, (DIFFIO_RX_R28n, DIFFOUT_R28n, DQ3R, DQ1R)
IO, (DIFFIO_RX_R29p, DIFFOUT_R29p, DQS3R, DQS1R)
IO, (DIFFIO_RX_R29n, DIFFOUT_R29n, DQSn3R, DQSn1R)
IO, (DIFFIO_TX_R30p, DIFFOUT_R30p)
IO, (DIFFIO_TX_R30n, DIFFOUT_R30n, DQ3R, DQ1R)
IO, (DIFFIO_RX_R31p, DIFFOUT_R31p, DQ3R, DQ1R)
IO, (DIFFIO_RX_R31n, DIFFOUT_R31n, DQ3R, DQ1R)
IO, (DIFFIO_TX_R32p, DIFFOUT_R32p, DQ3R, DQ1R)
IO, (DIFFIO_TX_R32n, DIFFOUT_R32n)
IO, CLK7p, FPLL_BR_FBp, (DIFFIO_RX_R33p, DIFFOUT_R33p)
IO, CLK7n, FPLL_BR_FBn, (DIFFIO_RX_R33n, DIFFOUT_R33n)
IO, (DIFFIO_TX_R34p, DIFFOUT_R34p, DQ4R, DQ1R)
IO, (DIFFIO_TX_R34n, DIFFOUT_R34n, DQ4R, DQ1R)
IO, (DIFFIO_RX_R35p, DIFFOUT_R35p, DQ4R, DQ1R)
IO, (DIFFIO_RX_R35n, DIFFOUT_R35n, DQ4R, DQ1R)
IO, (DIFFIO_TX_R36p, DIFFOUT_R36p, DQ4R, DQ1R)
IO, (DIFFIO_TX_R36n, DIFFOUT_R36n, DQ4R, DQ1R)
IO, (DIFFIO_RX_R37p, DIFFOUT_R37p, DQS4R, DQ1R)
IO, (DIFFIO_RX_R37n, DIFFOUT_R37n, DQSn4R, DQ1R)
IO, (DIFFIO_TX_R38p, DIFFOUT_R38p)
IO, (DIFFIO_TX_R38n, DIFFOUT_R38n, DQ4R, DQ1R)
IO, (DIFFIO_RX_R39p, DIFFOUT_R39p, DQ4R, DQ1R)
IO, (DIFFIO_RX_R39n, DIFFOUT_R39n, DQ4R, DQ1R)
IO, (DIFFIO_TX_R40p, DIFFOUT_R40p, DQ4R, DQ1R)
IO, (DIFFIO_TX_R40n, DIFFOUT_R40n)
IO, CLK6p, (DIFFIO_RX_R41p, DIFFOUT_R41p)
IO, CLK6n, (DIFFIO_RX_R41n, DIFFOUT_R41n)
IO, (DIFFIO_TX_R42p, DIFFOUT_R42p, DQ5R)
IO, (DIFFIO_TX_R42n, DIFFOUT_R42n, DQ5R)
IO, (DIFFIO_RX_R43p, DIFFOUT_R43p, DQ5R)
IO, (DIFFIO_RX_R43n, DIFFOUT_R43n, DQ5R)
IO, FPLL_BR_CLKOUT0, FPLL_BR_CLKOUTp, FPLL_BR_FB, (DIFFIO_TX_R44p, DIFFOUT_R44p, DQ5R)
IO, FPLL_BR_CLKOUT1, FPLL_BR_CLKOUTn, (DIFFIO_TX_R44n, DIFFOUT_R44n, DQ5R)
IO, (DIFFIO_RX_R45p, DIFFOUT_R45p, DQS5R)
IO, (DIFFIO_RX_R45n, DIFFOUT_R45n, DQSn5R)
IO, (DIFFIO_TX_R46p, DIFFOUT_R46p)
IO, (DIFFIO_TX_R46n, DIFFOUT_R46n, DQ5R)
IO, (DIFFIO_TX_R47p, DIFFOUT_R47p, DQ5R)
IO, (DIFFIO_RX_R47n, DIFFOUT_R47n, DQ5R)
IO, (DIFFIO_TX_R48p, DIFFOUT_R48p, DQ5R)
IO, (DIFFIO_TX_R48n, DIFFOUT_R48n)
VREFB5BN0
VCCPD5B
VCCIO5B
5CEFA7/9F31C8N
U4
MR2A16AMA35-BGA
R30
R31
R32
472
MRAM_OE
MRAM_CE
MRAM_WE
C12
475
FC5
104
ACM028R2-SCH-B3.pdf
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TITLE: Intel Cyclone V F896 FPGA board
DOC. No: ACM-028
B3
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DATE: 2022/04/13 11:52:49
Sheet: 5 / 8
V7_20200731

GCLK50_8 — GCLK50_8

FPGAH

	L15	IO, CLK9p, (DIFFIO_RX_T49p, DIFFOUT_T49p)
	K15	IO, CLK9n, (DIFFIO_RX_T49n, DIFFOUT_T49n)
DDR_A0	B11	IO, (DIFFIO_TX_T50p, DIFFOUT_T50p, DQ6T)
DDR_A1	A11	IO, (DIFFIO_TX_T50n, DIFFOUT_T50n, DQ6T)
DDR_A4	F16	IO, (DIFFIO_RX_T51p, DIFFOUT_T51p, DQ6T)
DDR_A5	E16	IO, (DIFFIO_RX_T51n, DIFFOUT_T51n, DQ6T)
DDR_A2	F9	IO, FPLL_TL_CLKOUT0, FPLL_TL_CLKOUTp, FPLL_TL_FB, (DIFFIO_TX_T52p, DIFFOUT_T52p, DQ6T)
DDR_A3	E10	IO, FPLL_TL_CLKOUT1, FPLL_TL_CLKOUTn, (DIFFIO_TX_T52n, DIFFOUT_T52n, DQ6T)
DDR_CK_P	M9	IO, (DIFFIO_RX_T53p, DIFFOUT_T53p, DQS6T)
DDR_CK_N	M8	IO, (DIFFIO_RX_T53n, DIFFOUT_T53n, DQSn6T)
DDR_A6	D9	IO, (DIFFIO_TX_T54p, DIFFOUT_T54p)
DDR_A7	C10	IO, (DIFFIO_TX_T54n, DIFFOUT_T54n, DQ6T)
DDR_BA1	F15	IO, (DIFFIO_RX_T55p, DIFFOUT_T55p, DQ6T)
DDR_BA2	E15	IO, (DIFFIO_RX_T55n, DIFFOUT_T55n, DQ6T)
DDR_BA0	A10	IO, (DIFFIO_TX_T56p, DIFFOUT_T56p, DQ6T)
	A9	IO, (DIFFIO_TX_T56n, DIFFOUT_T56n)
GCLK50_8	L14	IO, CLK8p, FPLL_TL_FBp, (DIFFIO_RX_T57p, DIFFOUT_T57p)
	L13	IO, CLK8n, FPLL_TL_FBn, (DIFFIO_RX_T57n, DIFFOUT_T57n)
DDR_CAS	C9	IO, (DIFFIO_TX_T58p, DIFFOUT_T58p, DQ7T)
DDR_RAS	B8	IO, (DIFFIO_TX_T58n, DIFFOUT_T58n, DQ7T)
DDR_A8	E12	IO, (DIFFIO_RX_T59p, DIFFOUT_T59p, DQ7T)
DDR_A9	D13	IO, (DIFFIO_RX_T59n, DIFFOUT_T59n, DQ7T)
DDR_A10	B7	IO, (DIFFIO_TX_T60p, DIFFOUT_T60p, DQ7T)
DDR_A11	A8	IO, (DIFFIO_TX_T60n, DIFFOUT_T60n, DQ7T)
DDR_CS	J15	IO, (DIFFIO_RX_T61p, DIFFOUT_T61p, DQS7T)
	H15	IO, (DIFFIO_RX_T61n, DIFFOUT_T61n, DQSn7T)
DDR_A12	B6	IO, (DIFFIO_TX_T62p, DIFFOUT_T62p)
DDR_A13	A6	IO, (DIFFIO_TX_T62n, DIFFOUT_T62n, DQ7T)
DDR_A14	E11	IO, (DIFFIO_RX_T63p, DIFFOUT_T63p, DQ7T)
	D10	IO, (DIFFIO_RX_T63n, DIFFOUT_T63n, DQ7T)
DDR_WE	C7	IO, (DIFFIO_TX_T64p, DIFFOUT_T64p, DQ7T)
	C6	IO, (DIFFIO_TX_T64n, DIFFOUT_T64n)
	L10	IO, (DIFFIO_RX_T65p, DIFFOUT_T65p)
	L9	IO, (DIFFIO_RX_T65n, DIFFOUT_T65n)
	F13	IO, (DIFFIO_TX_T66p, DIFFOUT_T66p, DQ8T)
	E13	IO, (DIFFIO_TX_T66n, DIFFOUT_T66n, DQ8T)
	G14	IO, (DIFFIO_RX_T67p, DIFFOUT_T67p, DQ8T)
	F14	IO, (DIFFIO_RX_T67n, DIFFOUT_T67n, DQ8T)
	A5	IO, (DIFFIO_TX_T68p, DIFFOUT_T68p, DQ8T)
	A4	IO, (DIFFIO_TX_T68n, DIFFOUT_T68n, DQ8T)
	J14	IO, (DIFFIO_RX_T69p, DIFFOUT_T69p, DQS8T)
	H14	IO, (DIFFIO_RX_T69n, DIFFOUT_T69n, DQSn8T)
	J7	IO, (DIFFIO_TX_T70p, DIFFOUT_T70p)
	H7	IO, (DIFFIO_TX_T70n, DIFFOUT_T70n, DQ8T)
	L11	IO, (DIFFIO_RX_T71p, DIFFOUT_T71p, DQ8T)
	K11	IO, (DIFFIO_RX_T71n, DIFFOUT_T71n, DQ8T)
	J9	IO, (DIFFIO_TX_T72p, DIFFOUT_T72p, DQ8T)
	H9	IO, (DIFFIO_TX_T72n, DIFFOUT_T72n)
	P12	IO, (DIFFIO_RX_T73p, DIFFOUT_T73p)
	N12	IO, (DIFFIO_RX_T73n, DIFFOUT_T73n)
	G9	IO, (DIFFIO_TX_T74p, DIFFOUT_T74p, DQ9T, DQ3T)
	F8	IO, (DIFFIO_TX_T74n, DIFFOUT_T74n, DQ9T, DQ3T)
	H12	IO, (DIFFIO_RX_T75p, DIFFOUT_T75p, DQ9T, DQ3T)
	G12	IO, (DIFFIO_RX_T75n, DIFFOUT_T75n, DQ9T, DQ3T)
	E8	IO, (DIFFIO_TX_T76p, DIFFOUT_T76p, DQ9T, DQ3T)
	D8	IO, (DIFFIO_TX_T76n, DIFFOUT_T76n, DQ9T, DQ3T)
	K13	IO, (DIFFIO_RX_T77p, DIFFOUT_T77p, DQS9T, DQS3T)
	J13	IO, (DIFFIO_RX_T77n, DIFFOUT_T77n, DQSn9T, DQSn3T)
	A3	IO, (DIFFIO_TX_T78p, DIFFOUT_T78p)
	A2	IO, (DIFFIO_TX_T78n, DIFFOUT_T78n, DQ9T, DQ3T)
	P10	IO, (DIFFIO_RX_T79p, DIFFOUT_T79p, DQ9T, DQ3T)
	N11	IO, (DIFFIO_RX_T79n, DIFFOUT_T79n, DQ9T, DQ3T)
	D7	IO, (DIFFIO_TX_T80p, DIFFOUT_T80p, DQ9T, DQ3T)
	D6	IO, (DIFFIO_TX_T80n, DIFFOUT_T80n)
	R12	IO, (DIFFIO_RX_T81p, DIFFOUT_T81p)
	R11	IO, (DIFFIO_RX_T81n, DIFFOUT_T81n)
	E7	IO, (DIFFIO_TX_T82p, DIFFOUT_T82p, DQ10T, DQ3T)
	E6	IO, (DIFFIO_TX_T82n, DIFFOUT_T82n, DQ10T, DQ3T)
	K12	IO, (DIFFIO_RX_T83p, DIFFOUT_T83p, DQ10T, DQ3T)
	J12	IO, (DIFFIO_RX_T83n, DIFFOUT_T83n, DQ10T, DQ3T)
	K10	IO, (DIFFIO_TX_T84p, DIFFOUT_T84p, DQ10T, DQ3T)
	J10	IO, (DIFFIO_TX_T84n, DIFFOUT_T84n, DQ10T, DQ3T)
	N10	IO, (DIFFIO_RX_T85p, DIFFOUT_T85p, DQS10T, DQ3T)
	N9	IO, (DIFFIO_RX_T85n, DIFFOUT_T85n, DQSn10T, DQ3T)
	G6	IO, (DIFFIO_TX_T86p, DIFFOUT_T86p)
	F6	IO, (DIFFIO_TX_T86n, DIFFOUT_T86n, DQ10T, DQ3T)
	M12	IO, (DIFFIO_RX_T87p, DIFFOUT_T87p, DQ10T, DQ3T)
	M11	IO, (DIFFIO_RX_T87n, DIFFOUT_T87n, DQ10T, DQ3T)
	G8	IO, (DIFFIO_TX_T88p, DIFFOUT_T88p, DQ10T, DQ3T)
	G7	IO, (DIFFIO_TX_T88n, DIFFOUT_T88n)
VREFB8AN0	B9	VDDRREF
VCCIO8A	A7	V15
VCCIO8A	B5	
VCCIO8A	C8	
VCCIO8A	D11	
VCCIO8A	E14	
VCCIO8A	F7	
VCCIO8A	G10	
VCCIO8A	H13	
VCCIO8A	J16	
VCCIO8A	L12	

5CEFA7/9F31C8N

FPGAG

	H21	IO, (DIFFIO_RX_T9p, DIFFOUT_T9p)
	G21	IO, (DIFFIO_RX_T9n, DIFFOUT_T9n)
	E26	IO, (DIFFIO_TX_T10p, DIFFOUT_T10p, DQ1T, DQ1T)
	E25	IO, (DIFFIO_TX_T10n, DIFFOUT_T10n, DQ1T, DQ1T)
	G22	IO, (DIFFIO_RX_T11p, DIFFOUT_T11p, DQ1T, DQ1T)
	G23	IO, (DIFFIO_RX_T11n, DIFFOUT_T11n, DQ1T, DQ1T)
	C27	IO, (DIFFIO_TX_T12p, DIFFOUT_T12p, DQ1T, DQ1T)
	C26	IO, (DIFFIO_TX_T12n, DIFFOUT_T12n, DQ1T, DQ1T)
	L20	IO, (DIFFIO_RX_T13p, DIFFOUT_T13p, DQS1T, DQS1T)
	L19	IO, (DIFFIO_RX_T13n, DIFFOUT_T13n, DQSn1T, DQSn1T)
	B27	IO, (DIFFIO_TX_T14p, DIFFOUT_T14p)
	A28	IO, (DIFFIO_TX_T14n, DIFFOUT_T14n, DQ1T, DQ1T)
	E22	IO, (DIFFIO_RX_T15p, DIFFOUT_T15p, DQ1T, DQ1T)
	E21	IO, (DIFFIO_RX_T15n, DIFFOUT_T15n, DQ1T, DQ1T)
	B26	IO, (DIFFIO_TX_T16p, DIFFOUT_T16p, DQ1T, DQ1T)
	A26	IO, (DIFFIO_TX_T16n, DIFFOUT_T16n)
	J20	IO, (DIFFIO_RX_T17p, DIFFOUT_T17p)
	H20	IO, (DIFFIO_RX_T17n, DIFFOUT_T17n)
DDR_DM3	D25	IO, (DIFFIO_TX_T18p, DIFFOUT_T18p, DQ2T, DQ1T)
DDR_DQ31	C25	IO, (DIFFIO_TX_T18n, DIFFOUT_T18n, DQ2T, DQ1T)
DDR_DQ29	C21	IO, (DIFFIO_RX_T19p, DIFFOUT_T19p, DQ2T, DQ1T)
DDR_DQ28	C20	IO, (DIFFIO_RX_T19n, DIFFOUT_T19n, DQ2T, DQ1T)
DDR_DQ30	D23	IO, (DIFFIO_TX_T20p, DIFFOUT_T20p, DQ2T, DQ1T)
	C22	IO, (DIFFIO_TX_T20n, DIFFOUT_T20n, DQ2T, DQ1T)
DDR_DQS3_P	K20	IO, (DIFFIO_RX_T21p, DIFFOUT_T21p, DQS2T, DQ1T)
DDR_DQS3_N	J19	IO, (DIFFIO_RX_T21n, DIFFOUT_T21n, DQSn2T, DQ1T)
	E23	IO, (DIFFIO_TX_T22p, DIFFOUT_T22p)
DDR_DQ27	D22	IO, (DIFFIO_TX_T22n, DIFFOUT_T22n, DQ2T, DQ1T)
DDR_DQ25	D20	IO, (DIFFIO_RX_T23p, DIFFOUT_T23p, DQ2T, DQ1T)
DDR_DQ24	C19	IO, (DIFFIO_RX_T23n, DIFFOUT_T23n, DQ2T, DQ1T)
DDR_DQ26	A25	IO, (DIFFIO_TX_T24p, DIFFOUT_T24p, DQ2T, DQ1T)
	A24	IO, (DIFFIO_TX_T24n, DIFFOUT_T24n)
	F20	IO, (DIFFIO_RX_T25p, DIFFOUT_T25p)
	E20	IO, (DIFFIO_RX_T25n, DIFFOUT_T25n)
DDR_DM2	C24	IO, (DIFFIO_TX_T26p, DIFFOUT_T26p, DQ3T, DQ2T)
DDR_DQ23	B24	IO, (DIFFIO_TX_T26n, DIFFOUT_T26n, DQ3T, DQ2T)
DDR_DQ21	F19	IO, (DIFFIO_RX_T27p, DIFFOUT_T27p, DQ3T, DQ2T)
DDR_DQ20	E18	IO, (DIFFIO_RX_T27n, DIFFOUT_T27n, DQ3T, DQ2T)
DDR_DQ22	B23	IO, (DIFFIO_TX_T28p, DIFFOUT_T28p, DQ3T, DQ2T)
	A23	IO, (DIFFIO_TX_T28n, DIFFOUT_T28n, DQ3T, DQ2T)
DDR_DQS2_P	L18	IO, (DIFFIO_RX_T29p, DIFFOUT_T29p, DQS3T, DQS2T)
DDR_DQS2_N	K18	IO, (DIFFIO_RX_T29n, DIFFOUT_T29n, DQSn3T, DQSn2T)
DDR_RESET	B22	IO, (DIFFIO_TX_T30p, DIFFOUT_T30p)
DDR_DQ19	B21	IO, (DIFFIO_TX_T30n, DIFFOUT_T30n, DQ3T, DQ2T)
DDR_DQ17	D19	IO, (DIFFIO_RX_T31p, DIFFOUT_T31p, DQ3T, DQ2T)
DDR_DQ16	D18	IO, (DIFFIO_RX_T31n, DIFFOUT_T31n, DQ3T, DQ2T)
DDR_DQ18	A21	IO, (DIFFIO_TX_T32p, DIFFOUT_T32p, DQ3T, DQ2T)
	A20	IO, (DIFFIO_TX_T32n, DIFFOUT_T32n)
	H19	IO, CLK11p, (DIFFIO_RX_T33p, DIFFOUT_T33p)
	J18	IO, CLK11n, (DIFFIO_RX_T33n, DIFFOUT_T33n)
DDR_DM1	B19	IO, (DIFFIO_TX_T34p, DIFFOUT_T34p, DQ4T, DQ2T)
DDR_DQ15	A19	IO, (DIFFIO_TX_T34n, DIFFOUT_T34n, DQ4T, DQ2T)
DDR_DQ13	G18	IO, (DIFFIO_RX_T35p, DIFFOUT_T35p, DQ4T, DQ2T)
DDR_DQ12	F18	IO, (DIFFIO_RX_T35n, DIFFOUT_T35n, DQ4T, DQ2T)
DDR_DQ14	B18	IO, (DIFFIO_TX_T36p, DIFFOUT_T36p, DQ4T, DQ2T)
DDR_CKE	A18	IO, (DIFFIO_TX_T36n, DIFFOUT_T36n, DQ4T, DQ2T)
DDR_DQS1_P	K16	IO, (DIFFIO_RX_T37p, DIFFOUT_T37p, DQS4T, DQ2T)
DDR_DQS1_N	L16	IO, (DIFFIO_RX_T37n, DIFFOUT_T37n, DQSn4T, DQ2T)
	D14	IO, (DIFFIO_TX_T38p, DIFFOUT_T38p)
DDR_DQ11	C14	IO, (DIFFIO_TX_T38n, DIFFOUT_T38n, DQ4T, DQ2T)
DDR_DQ9	C17	IO, (DIFFIO_TX_T39p, DIFFOUT_T39p, DQ4T, DQ2T)
DDR_DQ8	B17	IO, (DIFFIO_RX_T39n, DIFFOUT_T39n, DQ4T, DQ2T)
DDR_DQ10	A16	IO, (DIFFIO_TX_T40p, DIFFOUT_T40p, DQ4T, DQ2T)
	A15	IO, (DIFFIO_TX_T40n, DIFFOUT_T40n)
	H17	IO, CLK10p, (DIFFIO_RX_T41p, DIFFOUT_T41p)
	G17	IO, CLK10n, (DIFFIO_RX_T41n, DIFFOUT_T41n)
DDR_DM0	B14	IO, (DIFFIO_TX_T42p, DIFFOUT_T42p, DQ5T)
DDR_DQ7	A14	IO, (DIFFIO_TX_T42n, DIFFOUT_T42n, DQ5T)
DDR_DQ5	E17	IO, (DIFFIO_RX_T43p, DIFFOUT_T43p, DQ5T)
DDR_DQ4	D17	IO, (DIFFIO_RX_T43n, DIFFOUT_T43n, DQ5T)
DDR_DQ6	D12	IO, (DIFFIO_TX_T44p, DIFFOUT_T44p, DQ5T)
	C12	IO, (DIFFIO_TX_T44n, DIFFOUT_T44n, DQ5T)
DDR_DQS0_P	K17	IO, (DIFFIO_RX_T45p, DIFFOUT_T45p, DQS5T)
DDR_DQS0_N	J17	IO, (DIFFIO_RX_T45n, DIFFOUT_T45n, DQSn5T)
DDR_ODT	B13	IO, (DIFFIO_TX_T46p, DIFFOUT_T46p)
DDR_DQ3	A13	IO, (DIFFIO_TX_T46n, DIFFOUT_T46n, DQ5T)
DDR_DQ1	C16	IO, (DIFFIO_RX_T47p, DIFFOUT_T47p, DQ5T)
DDR_DQ0	C15	IO, (DIFFIO_RX_T47n, DIFFOUT_T47n, DQ5T)
DDR_DQ2	C11	IO, (DIFFIO_TX_T48p, DIFFOUT_T48p, DQ5T)
GND	B12	IO, RZQ_2, (DIFFIO_TX_T48n, DIFFOUT_T48n)
VREFB7AN0	B16	VDDRREF
	D24	V25PD
VCCPD7A8A	F11	
VCCPD7A8A	F21	
VCCPD7A8A	F23	
VCCPD7A8A	G13	
VCCPD7A8A	G16	
VCCPD7A8A	G19	
	A22	V15
VCCIO7A	B25	
VCCIO7A	B15	
VCCIO7A	C18	
VCCIO7A	D21	
VCCIO7A	E24	
VCCIO7A	F17	
VCCIO7A	G20	
VCCIO7A	H23	
VCCIO7A	K19	

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U_DDR_MEM
DDR_MEM.SchDoc

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DDR_DQ[0..31]	DDR_DQ[0..31]
DDR_BA[0..2]	DDR_BA[0..2]
DDR_RAS	DDR_RAS
DDR_CAS	DDR_CAS
DDR_WE	DDR_WE
DDR_CK_P	DDR_CK_P
DDR_CK_N	DDR_CK_N
DDR_CKE	DDR_CKE
DDR_RESET	DDR_RESET
DDR_ODT	DDR_ODT
DDR_CS	DDR_CS
DDR_DM[0..3]	DDR_DM[0..3]
DDR_DQS0_P	DDR_DQS0_P
DDR_DQS0_N	DDR_DQS0_N
DDR_DQS1_P	DDR_DQS1_P
DDR_DQS1_N	DDR_DQS1_N
DDR_DQS2_P	DDR_DQS2_P
DDR_DQS2_N	DDR_DQS2_N
DDR_DQS3_P	DDR_DQS3_P
DDR_DQS3_N	DDR_DQS3_N

V25PD — +C18 475 — +C19 475 — GND

V25PD — FC53 104 — FC54 104 — FC55 104 — GND

V15 — FC9 104 — FC10 104 — FC11 104 — FC12 104 — GND

V15 — +C15 475 — +C16 475 — +C17 475 — GND

VDDRREF — +C45 475 — GND

VDDRREF — FC73 104 — FC74 104 — GND

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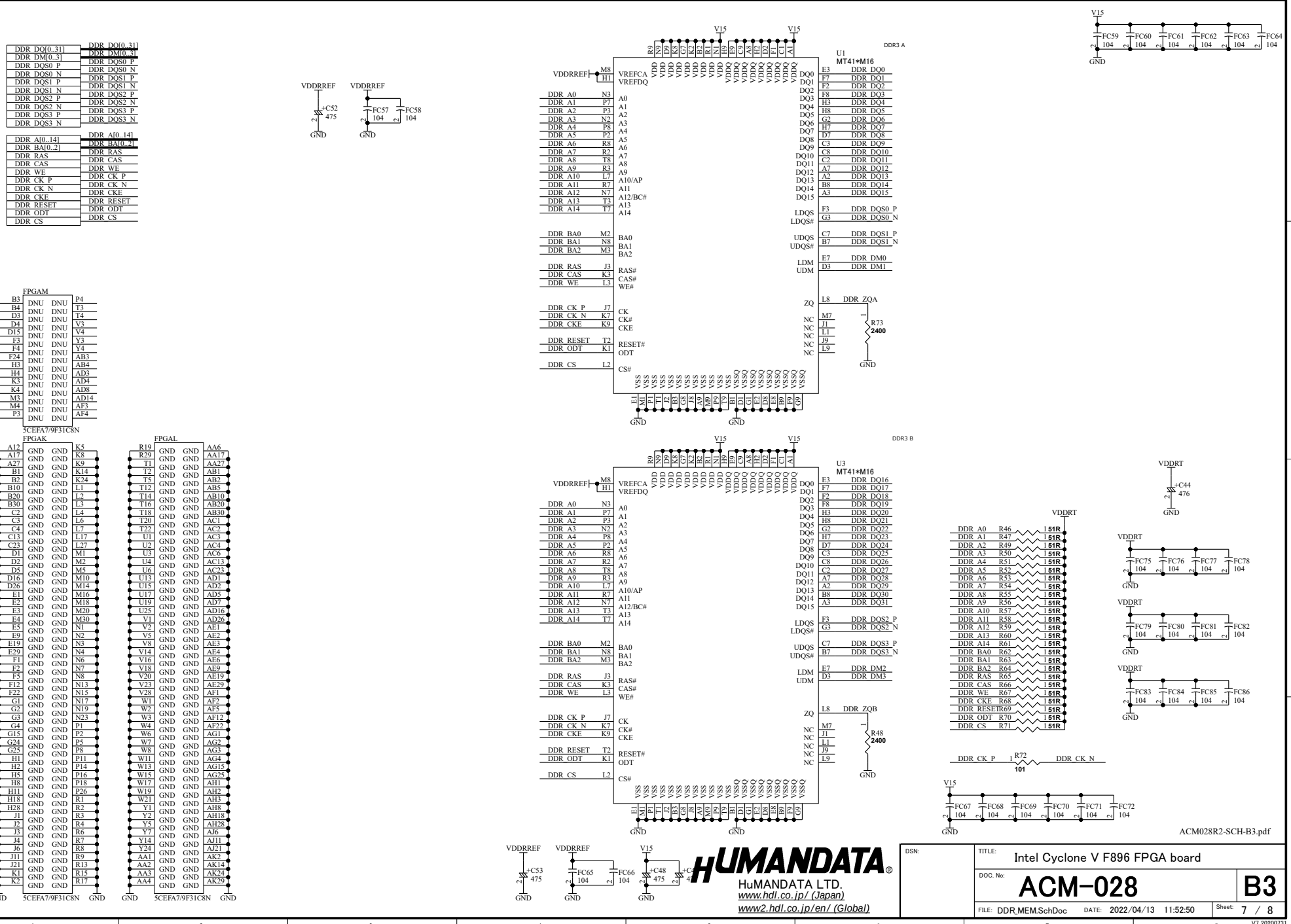
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DDR_DQS3_P
DDR_DQS3_N
DDR_A[0..14]
DDR_BA[0..2]
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DDR_CAS
DDR_WE
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DDR_CK_N
DDR_CKE
DDR_RESET
DDR_ODT
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+C52 475
FC57 104
FC58 104
GND
FPGAM
FPGAK
FPGAL
5CEFA7/9F31C8N
U1
MT41*M16
DDR3 A
U3
DDR3 B
V15
R9
VREFCA
VREFDQ
DQ0
DQ1
DQ2
DQ3
DQ4
DQ5
DQ6
DQ7
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CS#
DDR_ZQA
DDR_ZQB
R73 2400
R48 2400
VDDRT
+C44 476
R46
R47
R49
R50
R51
R52
R53
R54
R55
R56
R57
R58
R59
R60
R61
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R72 101
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FC83 104
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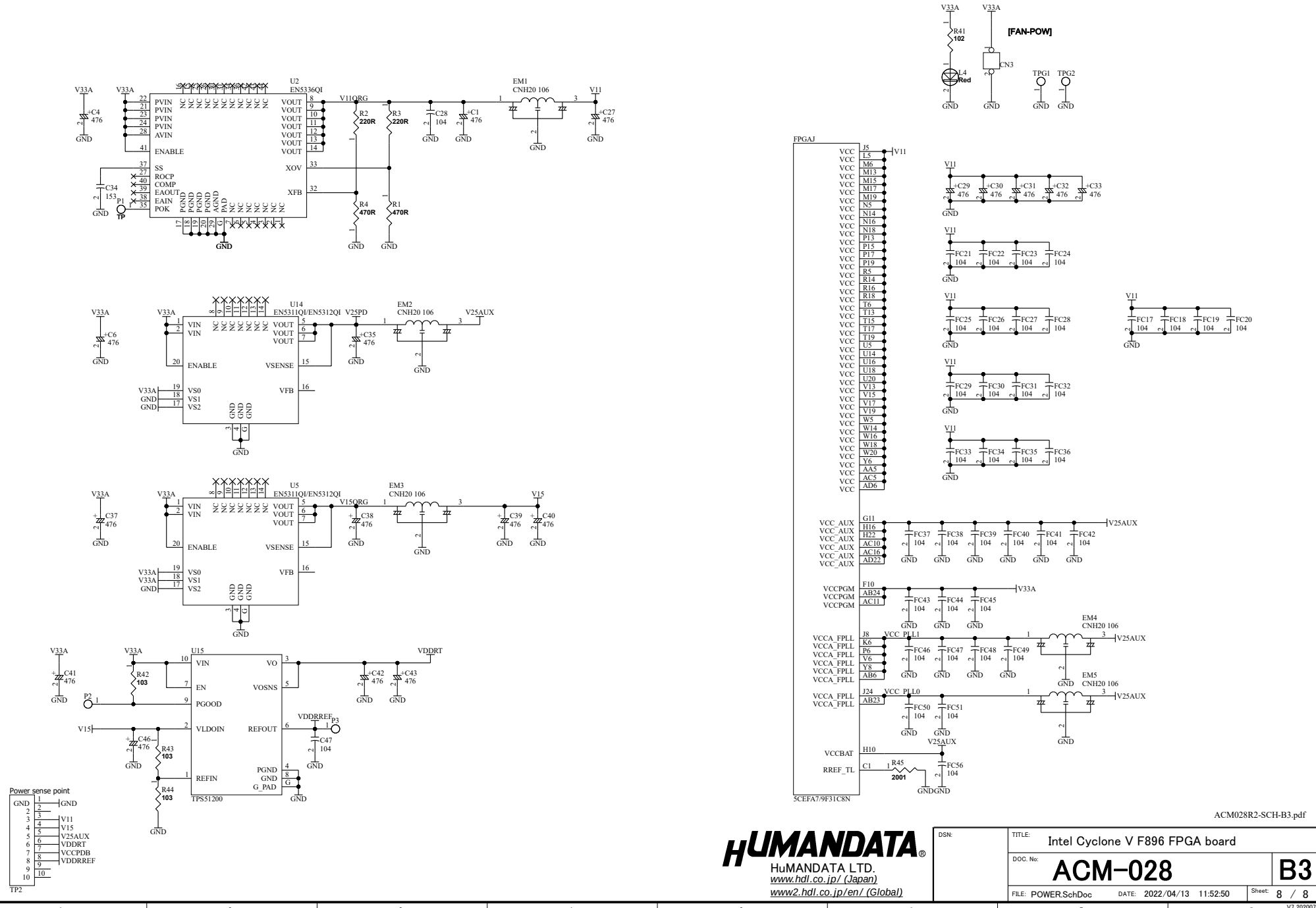
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CNH20 106
V11
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V25PD
EM2
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V25AUX
U5
EN5311QI/EN5312QI
V15ORG
EM3
CNH20 106
V15
U15
TPS51200
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Power sense point
TP2
V33A
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CN3
L4
Red
TPG1
TPG2
FPGAJ
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VCC_PLL0
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