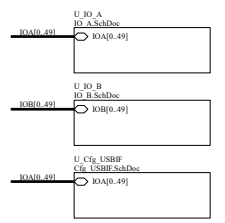
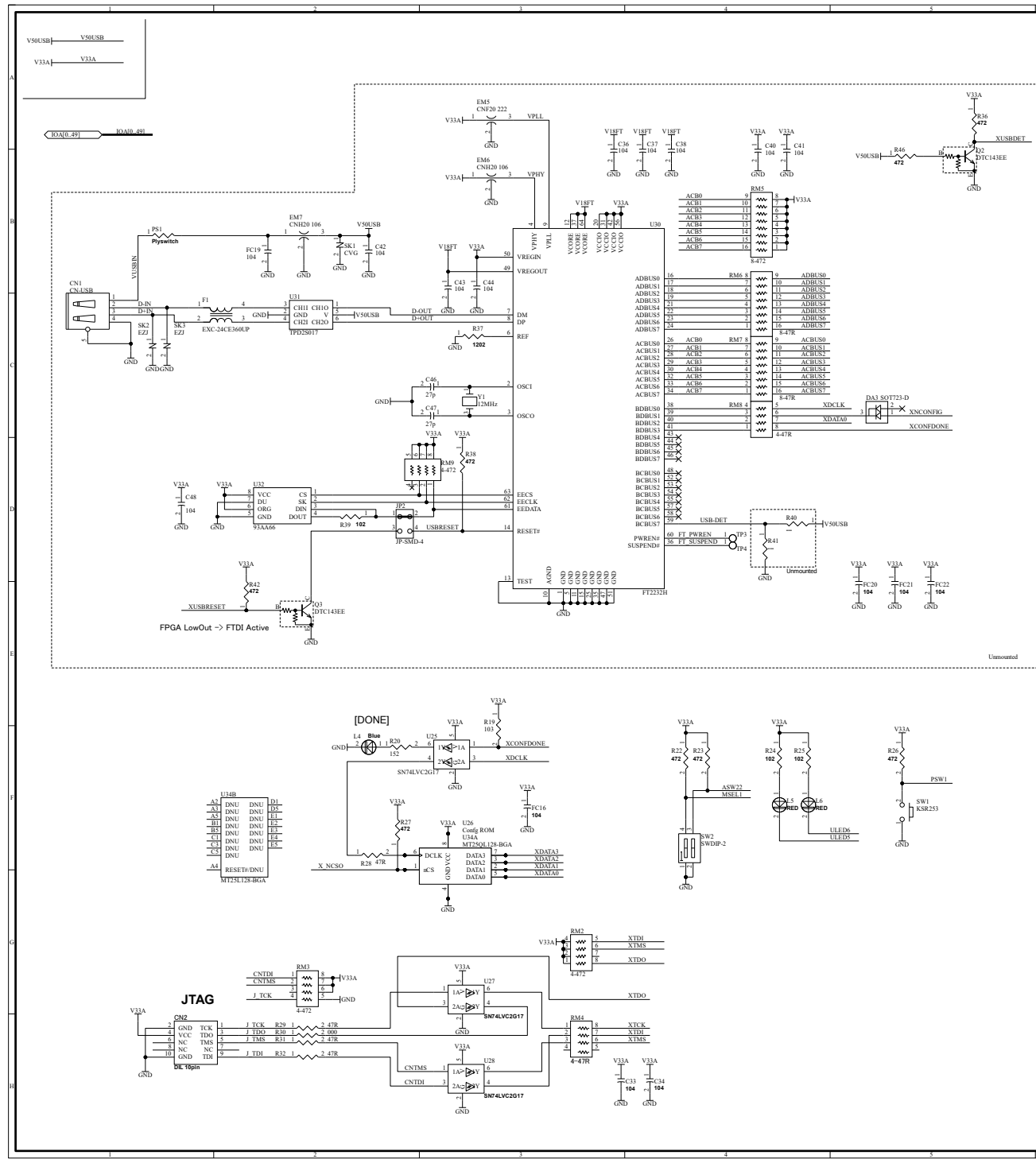


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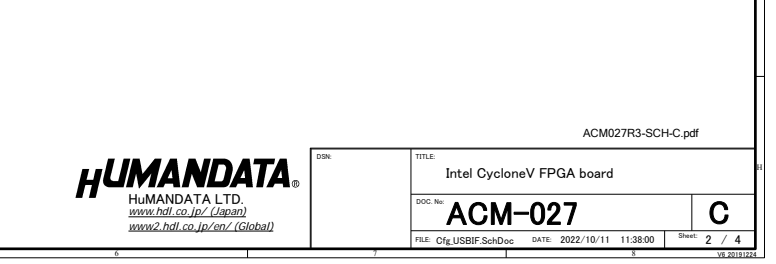
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XDATA0	R11	47R	AB6	DIFF0_TX_B1bGND_3B
XDATA1	R12	47R	AB5	DIFF0_TX_B3bP_WB_3B
			AB4	DIFF0_RX_B4bA_A_15_3B
			AB3	DIFF0_RX_B4bB_A_14_3B
			ACB3S1	PN
			ACB3	DIFF0_RX_B3bC_Cs1_3B
			AB3	DIFF0_RX_B3bP_Cs1_0_3B
			AB1	DIFF0_TX_B3bB_A_11_3B
			AB2	DIFF0_TX_B3bB_A_12_3B
			AA5	DIFF0_TX_B1bB_A_11_3B
			AA6	DIFF0_TX_B3bB_A_10_3B
			AB33	DIFF0_RX_B3bB_A_9_3B
			AB32	DIFF0_RX_B3bB_A_8_3B
			XUSBRES1	MS
			XCBRES	MS
			IA08	AA10
			IA09	AA5
			IA17	V10
			IA16	Y3
			ADBUS5	T10
			ADBUS6	R5
			IA24	U11
			IA25	U12
			ADBUS4	R12
			ULED5	P12
			IA07	AB10
			IA06	AB11
			ADBUS1	R10
			ADBUS2	R9
			ACBUS0	PS
			ACBUS7	NS
			IA28	Y11
			IA29	AA12

ACBUS1	R6	DIFF0_RX_B1bDATA6_3A
ACBUS2	R3	DIFF0_RX_B1bDATA5_3A
IA05	U7	DIFF0_TX_B2bDATA5_3A
IA04	U8	DIFF0_TX_B2bDATA4_3A
ADBUS1	P6	DIFF0_RX_B1bDATA10_3A
ADBUS4	NS	DIFF0_RX_B3bDATA12_3A
IA11	W3	DIFF0_TX_B4bDATA9_3A
IA10	W2	DIFF0_TX_B4bDATA11_3A
ADBUS7	T7	DIFF0_RX_B5bDATA4_3A
ADBUS8	T8	DIFF0_RX_B5bCLKUSE_3A
XDCLK	U6	DIFF0_TX_B6bDATA13_3A
	V6	DIFF0_TX_B6bDATA15_3A
	M6	DIFF0_RX_B7bP_DONE_3A
	M7	DIFF0_RX_B7bP_ERROR_3A
X_NCSO	R15	DIFF0_TX_B8bVP_READY_3A
	P2	DIFF0_TX_B9P_3A

XTDO	M5	TDO_3A
XTMS	P1	TMS_3A
XTCK	V2	TCK_3A
XTDI	W3	TDI_3A
X_NCSO	V2	CSO_3A
	V3	CLK_3A
	AB7	AS_DATAASDO_3A
	AB8	AS_DATA1_3A
	AB5	AS_DATA2_3A
	AB2	AS_DATA3_3A

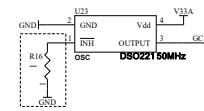


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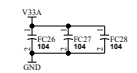
V33A V33A

IOA10_491



PGAD		
IOA14	AB13	DIFFIO_TX_R49wRZQ_0_4A
IOA15	AB12	DIFFIO_TX_R49wB_DQ_2_4A
IOA40	V13	DIFFIO_RX_R50wB_DQ_0_4A
IOA41	U13	DIFFIO_RX_R50wB_DQ_1_4A
T13		DIFFIO_RX_R51wB_DQS8_0_4A
T14		DIFFIO_RX_R51wB_DQS_5_4A
IOA12	AA14	DIFFIO_TX_R52wB_DQ_3_4A
IOA13	AA13	DIFFIO_TX_R52wB_ODT_0_4A
IOA21	AB15	DIFFIO_TX_R53wB_ODT_1_4A
IOA20	AA15	DIFFIO_TX_R53wB_DQ_6_4A
IOA30	V14	DIFFIO_RX_R54wB_DQ_4_4A
IOA31	V15	DIFFIO_RX_R54wB_DQ_5_4A
V14		DIFFIO_RX_R55wCLK2_4A
V15		DIFFIO_RX_R55wCLK3_4A
IOA19	AB17	DIFFIO_TX_R56wB_DQ_7_4A
IOA18	AB18	DIFFIO_TX_R56wB_DM_0_4A
IOA23	AB20	DIFFIO_TX_R57wGND_4A
IOA24	AB21	DIFFIO_TX_R57wB_DQ_10_4A
IOA39	V16	DIFFIO_RX_R58wB_DQ_8_4A
IOA38	V17	DIFFIO_RX_R58wB_DQ_9_4A
IOA37	T14	DIFFIO_RX_R59wB_DQS8_1_4A
IOA36	U15	DIFFIO_RX_R59wB_DQS_1_4A
IOA42	AA17	DIFFIO_TX_R60wB_DQ_11_4A
IOA43	AA18	DIFFIO_TX_R60wB_CKE_1_4A
IOA47	AA19	DIFFIO_TX_R61wB_CKE_0_4A
IOA26	AA20	DIFFIO_TX_R61wB_DQ_14_4A
IOA47	V20	DIFFIO_RX_R62wB_DQ_12_4A
IOA46	W19	DIFFIO_RX_R62wB_DQ_13_4A
IOA48	V16	DIFFIO_RX_R63wCLK3_4A
IOA49	W18	DIFFIO_RX_R63wCLK3_4A
MRAM_HB0	AB22	DIFFIO_TX_R64wB_DQ_15_4A
MRAM_CE	AA22	DIFFIO_TX_R64wB_DM_1_4A
MRAM_A1	V23	DIFFIO_TX_R65wGND_4A
MRAM_CE	W23	DIFFIO_TX_R65wB_DQ_18_4A
IOA45	V20	DIFFIO_RX_R66wB_DQ_16_4A
IOA44	V19	DIFFIO_RX_R66wB_DQ_17_4A
P14		DIFFIO_RX_R67wB_DQS8_2_4A
R14		DIFFIO_RX_R67wB_DQS_2_4A
MRAM_A0	V24	DIFFIO_TX_R68wB_DQ_19_4A
MRAM_A2	W24	DIFFIO_TX_R68wB_RBSSET_4A
MRAM_DQ12	U22	DIFFIO_TX_R69wGND_4A
MRAM_DQ10	V21	DIFFIO_TX_R69wB_DQ_22_4A
V19		DIFFIO_RX_R70wB_DQ_20_4A
V18		DIFFIO_RX_R70wB_DQ_21_4A
VCCIOA4	U16	DIFFIO_RX_R71wGND_4A
VCCIOA4	U17	DIFFIO_RX_R71wGND_4A
VCCIOA4	U19	DIFFIO_RX_R71wGND_4A
VCCIOA4	W15	DIFFIO_TX_R72wB_DQ_23_4A
VCCIOA4	W20	DIFFIO_TX_R72wB_DQ_2_4A

VREFBAAN0_4A



PGAE		
MRAM_DQ09	T20	DIFFIO_TX_R16wR REQUEST_5A
MRAM_DQ10	T19	DIFFIO_TX_R16wRZQ_1_5A
MRAM_BE1	T17	DIFFIO_RX_R2wCRC_ERROR_5A
MRAM_A3	T18	DIFFIO_RX_R2wINIT_DONE_5A
MRAM_DQ15	R22	DIFFIO_TX_R3wCwP_CONFIDENCE_5A
MRAM_DQ17	R22	DIFFIO_TX_R3wCwP_CONFIDENCE_5A
R15		DIFFIO_RX_R4w_5A
R16		DIFFIO_RX_R4w_5A
MRAM_DQ16	R22	DIFFIO_TX_R5wDEV_CLR_5A
MRAM_DQ14	R21	DIFFIO_TX_R5wDEV_OE_5A
MRAM_A6	R17	DIFFIO_RX_R6wPERSTL1_5A
MRAM_A5	R16	DIFFIO_RX_R6wPERSTL0_5A
MRAM_A7	P18	DIFFIO_TX_R7w_5A
MRAM_A16	P19	DIFFIO_TX_R7w_5A
MRAM_A17	R17	DIFFIO_RX_R8w_5A
MRAM_DQ11	P16	DIFFIO_RX_R8w_5A

VCCPD5A

VREFBSAN0_5A

VCCIO5A

VCCIO5A

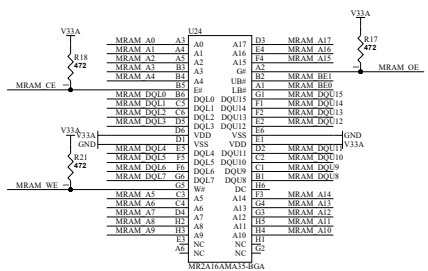
PGAE		
MRAM_DQ12	M16	DIFFIO_RX_R41wCLK6w_5B
MRAM_WE	N16	DIFFIO_RX_R41wCLK6w_5B
MRAM_A11	N21	DIFFIO_TX_R42w_5B
MRAM_A14	N20	DIFFIO_TX_R42w_5B
MRAM_DQ13	M18	DIFFIO_RX_R43w_5B
MRAM_A11	L22	DIFFIO_TX_R44wPPLL_BR_CLKOUT0_5B
MRAM_A15	M22	DIFFIO_TX_R44wPPLL_BR_CLKOUT0_5B
MRAM_DQ14	L17	DIFFIO_RX_R45w_5B
MRAM_A12	S17	DIFFIO_RX_R45w_5B
MRAM_DQ17	M21	DIFFIO_TX_R46w_5B
MRAM_DQ14	M20	DIFFIO_TX_R46w_5B
MRAM_A9	L18	DIFFIO_RX_R47w_5B
MRAM_A8	L19	DIFFIO_RX_R47w_5B
MRAM_A10	K22	DIFFIO_TX_R48w_5B
MRAM_A13	K21	DIFFIO_TX_R48w_5B

VCCPD5B

VREFBSB0_5B

VCCIO5B

VCCIO5B



MR26T6AM35-BGA

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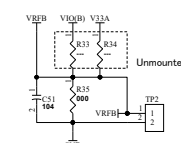
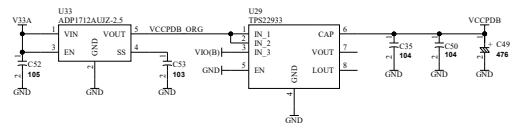
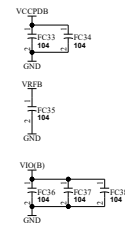
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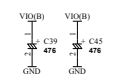
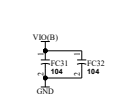
V33A
VIO(B)

VIO(B) VIO(B)

PGAG		FPGA	
K19	DIFFO_RX_T25nGND_7A	H21	DIFFO_RX_T49nCLK9_8A
R20	DIFFO_RX_T25pGND_7A	E21	DIFFO_RX_T49pCLK9_8A
K0B21	C16	K7	DIFFO_TX_T50nT_A_1_8A
K0B20	DIFFO_TX_T26nT_DQ_23_7A	L7	DIFFO_TX_T50nT_A_0_8A
E0A	DIFFO_TX_T26nT_DM_2_7A	J7	DIFFO_RX_T51nT_A_5_8A
K0B42	E16	J7	DIFFO_RX_T51pT_A_4_8A
K0B43	E16	G8	DIFFO_TX_T52nT_A_3FPPLL_TL_CLKOUT1_FPPLL_TL_CLKOUT1_6_8A
G16	DIFFO_TX_T28nGND_7A	H8	DIFFO_TX_T52pT_A_2FPPLL_TL_CLKOUT1_FPPLL_TL_CLKOUT1_5FPPLL_TL_FB_8A
G17	DIFFO_TX_T28nT_DQ_22_7A	H8	DIFFO_RX_T53nT_CK_8A
H18	DIFFO_RX_T28nT_DQS2_2_7A	J8	DIFFO_TX_T54nT_A_7_8A
G18	DIFFO_RX_T29nT_DQS2_2_7A	J8	DIFFO_TX_T54nT_A_6_8A
J18	DIFFO_TX_T30nT_DQ_19_7A	K9	DIFFO_RX_T55nT_BA_2_8A
J19	DIFFO_TX_T30nT_RESETP_7A	K9	DIFFO_RX_T55pT_BA_1_8A
K0B12	E15	A9	DIFFO_TX_T56nGND_8A
K0B13	E15	A10	DIFFO_TX_T56nT_BA_0_8A
K0B18	A14	B9	DIFFO_RX_T57nCLKnFPPLL_TL_FB_8A
K0B19	A15	B9	DIFFO_RX_T57pCLKnFPPLL_TL_FB_8A
K0B46	H15	A9	DIFFO_TX_T58nT_RAS_8A
K0B47	H16	A10	DIFFO_TX_T58nT_CAS_8A
E16	DIFFO_TX_T34nT_DQ_15_7A	B9	DIFFO_RX_T59nT_A_9_8A
E17	DIFFO_TX_T34nT_DM_1_7A	B9	DIFFO_RX_T59nT_A_8_8A
K0B30	B15	A9	DIFFO_TX_T60nT_A_11_8A
K0B31	C15	A9	DIFFO_TX_T60nT_A_10_8A
K0B44	F14	D9	DIFFO_RX_T61nT_CSE_1_8A
K0B45	G15	D9	DIFFO_RX_T61pT_CSE_0_8A
J11	DIFFO_RX_T37nT_DQS1_7A	E9	DIFFO_TX_T62nT_A_13_8A
H14	DIFFO_RX_T37pT_DQS_1_7A	E9	DIFFO_TX_T62pT_A_12_8A
K0B14	A11	D9	DIFFO_RX_T63nT_A_15_8A
K0B15	B11	D9	DIFFO_RX_T63pT_A_14_8A
K0B41	F13	H9	DIFFO_TX_T64nGND_8A
K0B49	H14	H9	DIFFO_TX_T64nT_WEB_8A
H10	DIFFO_TX_T40nGND_7A	E7	
H11	DIFFO_TX_T40nT_DQ_10_7A	F7	
K0B48	G13		
K0B49	H13		
K0B37	F12		
K0B36	G11		
K0B38	C13		
K0B39	D13		
K0B12	A12		
K0B13	B12		
G12	DIFFO_RX_T44nT_DQ_6_7A		
H11	DIFFO_RX_T45nT_DQS2_0_7A		
K9	DIFFO_TX_T46nT_DQ_3_7A		
L3	DIFFO_TX_T46nT_ODT_0_7A		
K0B26	E12		
K0B27	D12		
K0B28	H11		
K0B29	H12		
H11	DIFFO_TX_T48nRZQ_2_7A		
K0B24	H12		



FPGA	
E10	DIFFO_RX_T49nCLK9_8A
G10	DIFFO_RX_T49pCLK9_8A
K7	DIFFO_TX_T50nT_A_1_8A
L7	DIFFO_TX_T50nT_A_0_8A
J7	DIFFO_RX_T51nT_A_5_8A
J7	DIFFO_RX_T51pT_A_4_8A
G8	DIFFO_TX_T52nT_A_3FPPLL_TL_CLKOUT1_FPPLL_TL_CLKOUT1_6_8A
H8	DIFFO_TX_T52pT_A_2FPPLL_TL_CLKOUT1_FPPLL_TL_CLKOUT1_5FPPLL_TL_FB_8A
H8	DIFFO_RX_T53nT_CK_8A
J8	DIFFO_TX_T54nT_A_7_8A
J8	DIFFO_TX_T54nT_A_6_8A
K9	DIFFO_RX_T55nT_BA_2_8A
K9	DIFFO_RX_T55pT_BA_1_8A
A9	DIFFO_TX_T56nGND_8A
A10	DIFFO_TX_T56nT_BA_0_8A
B9	DIFFO_RX_T57nCLKnFPPLL_TL_FB_8A
B9	DIFFO_RX_T57pCLKnFPPLL_TL_FB_8A
A9	DIFFO_TX_T58nT_RAS_8A
A10	DIFFO_TX_T58nT_CAS_8A
B9	DIFFO_RX_T59nT_A_9_8A
B9	DIFFO_RX_T59nT_A_8_8A
A9	DIFFO_TX_T60nT_A_11_8A
A9	DIFFO_TX_T60nT_A_10_8A
D9	DIFFO_RX_T61nT_CSE_1_8A
D9	DIFFO_RX_T61pT_CSE_0_8A
E9	DIFFO_TX_T62nT_A_13_8A
E9	DIFFO_TX_T62pT_A_12_8A
D9	DIFFO_RX_T63nT_A_15_8A
D9	DIFFO_RX_T63pT_A_14_8A
H9	DIFFO_TX_T64nGND_8A
H9	DIFFO_TX_T64nT_WEB_8A



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