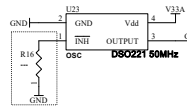
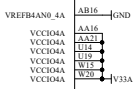


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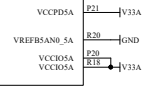
IOA10\_491 IOA10\_491



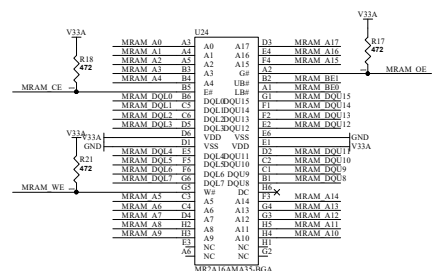
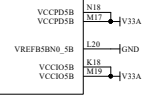
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IOA21	U13
	X T12
	X T13
IOA12	AA14
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IOA21	AB15
IOA20	AB15
IOA30	V14
IOA31	V15
	X V14
	X V15
IOA19	AB17
IOA18	AB18
IOA25	AB20
IOA24	AB21
IOA39	V16
IOA38	V17
IOA37	T14
IOA36	U15
IOA42	AA17
IOA43	AA38
IOA27	AA19
IOA26	AA20
IOA47	V20
IOA46	W19
IOA48	V16
IOA49	W16
MRAM BE0	AB22
MRAM OE	AB22
MRAM A1	Y22
MRAM CE	W22
IOA45	Y20
IOA44	Y19
	X P14
	X P14
MRAM A0	W21
MRAM A1	W21
MRAM DQ12	U22
MRAM DQ10	V21
	X V19
	X V18
MRAM DQ18	U16
MRAM DQ18	U16
MRAM DQ1	U21
MRAM A4	U20
	X V19
	X V18
MRAM DQ15	U16
MRAM DQ15	U16
MRAM DQ1	U21
MRAM A4	U20



FPGA	
MRAM DQ19	T20
MRAM DQ10	T19
MRAM BE1	T17
MRAM A3	T18
MRAM DQ15	R22
MRAM DQ13	T22
	X R11
	X T12
MRAM DQ16	P22
MRAM DQ14	R21
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MRAM A17	P17
MRAM DQ11	P16

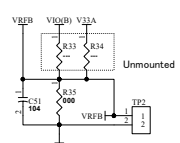
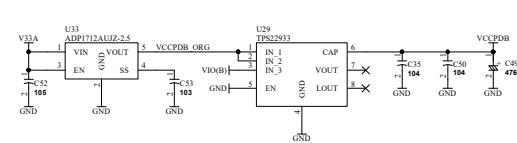
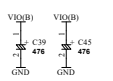
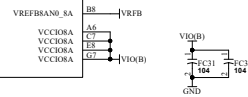
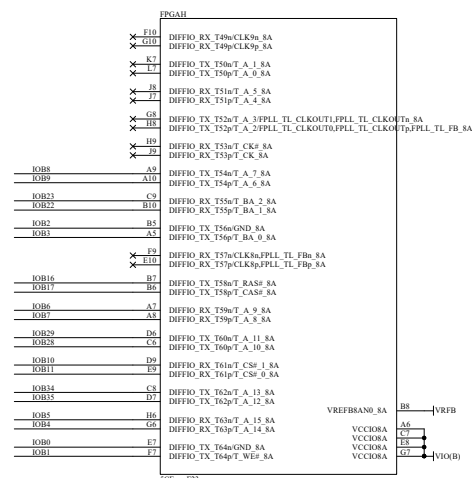
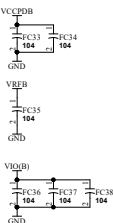
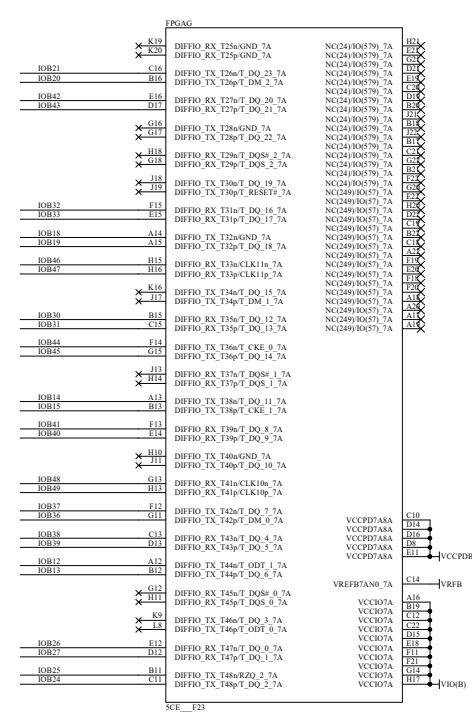
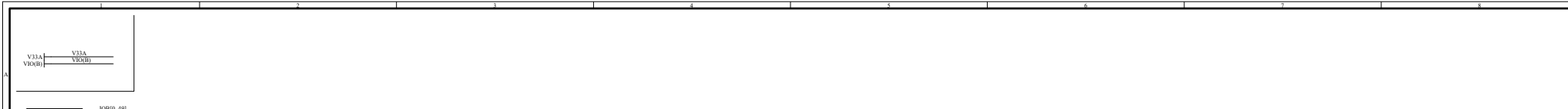


FPGA	
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	X N16
MRAM WE	N21
MRAM A14	N20
MRAM DQ13	M18
	X N19
MRAM A11	L22
MRAM A15	M22
MRAM DQ15	L17
MRAM A15	K17
MRAM DQ17	M21
MRAM DQ14	M20
MRAM A9	L18
MRAM A8	L19
MRAM A10	K22
MRAM A13	K21



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**A2**



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