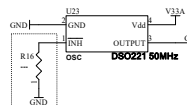


DSN	TITLE	ACM027R1-SCH-A1.pdf
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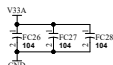
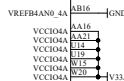
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V33A

IOA10_491 IOA10_491



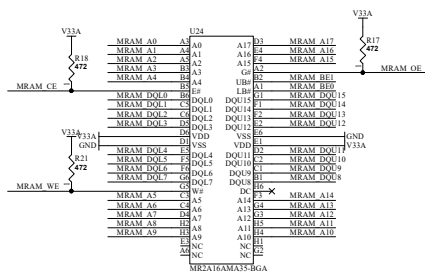
FPGA	
IOA14	AB13 DIFFIO_TX_B49wRZQ_0_4A
IOA15	AB15 DIFFIO_TX_B49wB_DQ_2_4A
IOA40	V13 DIFFIO_RX_B56wB_DQ_0_4A
IOA21	U13 DIFFIO_RX_B56wB_DQ_1_4A
	X T12 DIFFIO_RX_B51wB_DQ5_0_4A
	X T13 DIFFIO_RX_B51wB_DQ5_0_4A
IOA12	AA14 DIFFIO_TX_B52wB_DQ_3_4A
IOA13	AA13 DIFFIO_TX_B52wB_ODT_0_4A
IOA21	AB15 DIFFIO_TX_B53wB_ODT_1_4A
IOA20	AA15 DIFFIO_TX_B53wB_DQ_6_4A
IOA30	V14 DIFFIO_RX_B54wB_DQ_4_4A
IOA31	V15 DIFFIO_RX_B54wB_DQ_5_4A
	X V14 DIFFIO_RX_B55wCLK2w_4A
	X V15 DIFFIO_RX_B55wCLK2w_4A
IOA19	AB17 DIFFIO_TX_B56wB_DQ_7_4A
IOA18	AB18 DIFFIO_TX_B56wB_DM_0_4A
IOA25	AB20 DIFFIO_TX_B57wGND_4A
IOA24	AA20 DIFFIO_TX_B57wB_DQ_10_4A
IOA39	V16 DIFFIO_RX_B58wB_DQ_8_4A
IOA18	V17 DIFFIO_RX_B58wB_DQ_9_4A
	T14 DIFFIO_RX_B59wB_DQ5_1_4A
IOA17	T14 DIFFIO_RX_B59wB_DQ5_1_4A
IOA16	U15 DIFFIO_TX_B60wB_DQ_11_4A
IOA43	AA17 DIFFIO_TX_B60wB_CKE_1_4A
IOA42	AA17 DIFFIO_TX_B60wB_DQ_11_4A
IOA43	AA18 DIFFIO_TX_B60wB_DQ_12_4A
IOA27	AA19 DIFFIO_TX_B61wB_CKE_0_4A
IOA26	AA20 DIFFIO_TX_B61wB_DQ_13_4A
IOA47	V20 DIFFIO_RX_B62wB_DQ_12_4A
IOA46	W19 DIFFIO_RX_B62wB_DQ_13_4A
IOA48	V16 DIFFIO_RX_B63wCLK3w_4A
IOA49	W16 DIFFIO_RX_B63wCLK3w_4A
MRAM_BEO	AB22 DIFFIO_TX_B64wB_DQ_15_4A
MRAM_OE	AA22 DIFFIO_TX_B64wB_DM_1_4A
MRAM_A1	V22 DIFFIO_TX_B65wGND_4A
MRAM_CE	W22 DIFFIO_TX_B65wB_DQ_16_4A
IOA45	V20 DIFFIO_RX_B66wB_DQ_16_4A
IOA44	V19 DIFFIO_RX_B66wB_DQ_17_4A
	X P14 DIFFIO_RX_B67wB_DQ5_2_4A
	X R14 DIFFIO_RX_B67wB_DQ5_2_4A
MRAM_A0	V21 DIFFIO_TX_B68wB_DQ_19_4A
MRAM_A1	W21 DIFFIO_TX_B68wB_DQ_19_4A
MRAM_DQ1	U22 DIFFIO_TX_B69wGND_4A
MRAM_DQ2	U22 DIFFIO_TX_B69wGND_4A
MRAM_DQ0	V21 DIFFIO_TX_B69wB_DQ_22_4A
	X V19 DIFFIO_RX_B70wB_DQ_20_4A
	X V18 DIFFIO_RX_B70wB_DQ_21_4A
MRAM_DQ8	U16 DIFFIO_RX_B71wGND_4A
MRAM_DQ18	U16 DIFFIO_RX_B71wGND_4A
MRAM_DQ1	U21 DIFFIO_TX_B72wB_DQ_23_4A
MRAM_A4	U20 DIFFIO_TX_B72wB_DM_2_4A



FPGA	
MRAM_DQ9	T20 DIFFIO_TX_R10wPR_REQUEST_5A
MRAM_DQ10	U19 DIFFIO_TX_R10wRZQ_1_5A
MRAM_BE1	T17 DIFFIO_RX_R2wCRC_ERROR_5A
MRAM_A3	T18 DIFFIO_RX_R2wINIT_DONE_5A
MRAM_DQ5	R22 DIFFIO_TX_R3wCvP_CONF_DONE_5A
MRAM_DQ3	T22 DIFFIO_TX_R3wCvP_CONF_DONE_5A
	X R14 DIFFIO_RX_R4w_5A
	X T19 DIFFIO_RX_R4w_5A
MRAM_DQ6	P22 DIFFIO_TX_R5wDEV_CLKw_5A
MRAM_DQ4	R21 DIFFIO_TX_R5wDEV_OE_5A
MRAM_A6	R17 DIFFIO_RX_R6wSPERSTL1_5A
MRAM_A5	R16 DIFFIO_RX_R6wSPERSTL0_5A
MRAM_A7	P19 DIFFIO_TX_R7w_5A
MRAM_A16	P19 DIFFIO_TX_R7w_5A
MRAM_A17	P17 DIFFIO_RX_R8w_5A
MRAM_DQ11	P18 DIFFIO_RX_R8w_5A



FPGA	
MRAM_DQ12	X M16 DIFFIO_RX_R41wCLK6w_5B
	X M17 DIFFIO_RX_R41wCLK6w_5B
MRAM_WE	N21 DIFFIO_TX_R42w_5B
MRAM_A14	N20 DIFFIO_TX_R42w_5B
MRAM_DQ13	M18 DIFFIO_RX_R43w_5B
	X N19 DIFFIO_RX_R43w_5B
MRAM_A11	L22 DIFFIO_TX_R44wFPLL_BR_CLKOUT1_FPLL_BR_CLKOUTw_5B
MRAM_A15	M22 DIFFIO_TX_R44wFPLL_BR_CLKOUT0_FPLL_BR_CLKOUTw_FPLL_BR_FB_5B
MRAM_DQ15	L17 DIFFIO_RX_R45w_5B
MRAM_A15	K21 DIFFIO_RX_R45w_5B
MRAM_DQ7	M21 DIFFIO_TX_R46w_5B
MRAM_DQ14	M20 DIFFIO_TX_R46w_5B
MRAM_A9	L18 DIFFIO_RX_R47w_5B
MRAM_A8	L19 DIFFIO_RX_R47w_5B
MRAM_A10	K22 DIFFIO_TX_R48w_5B
MRAM_A13	K21 DIFFIO_TX_R48w_5B



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