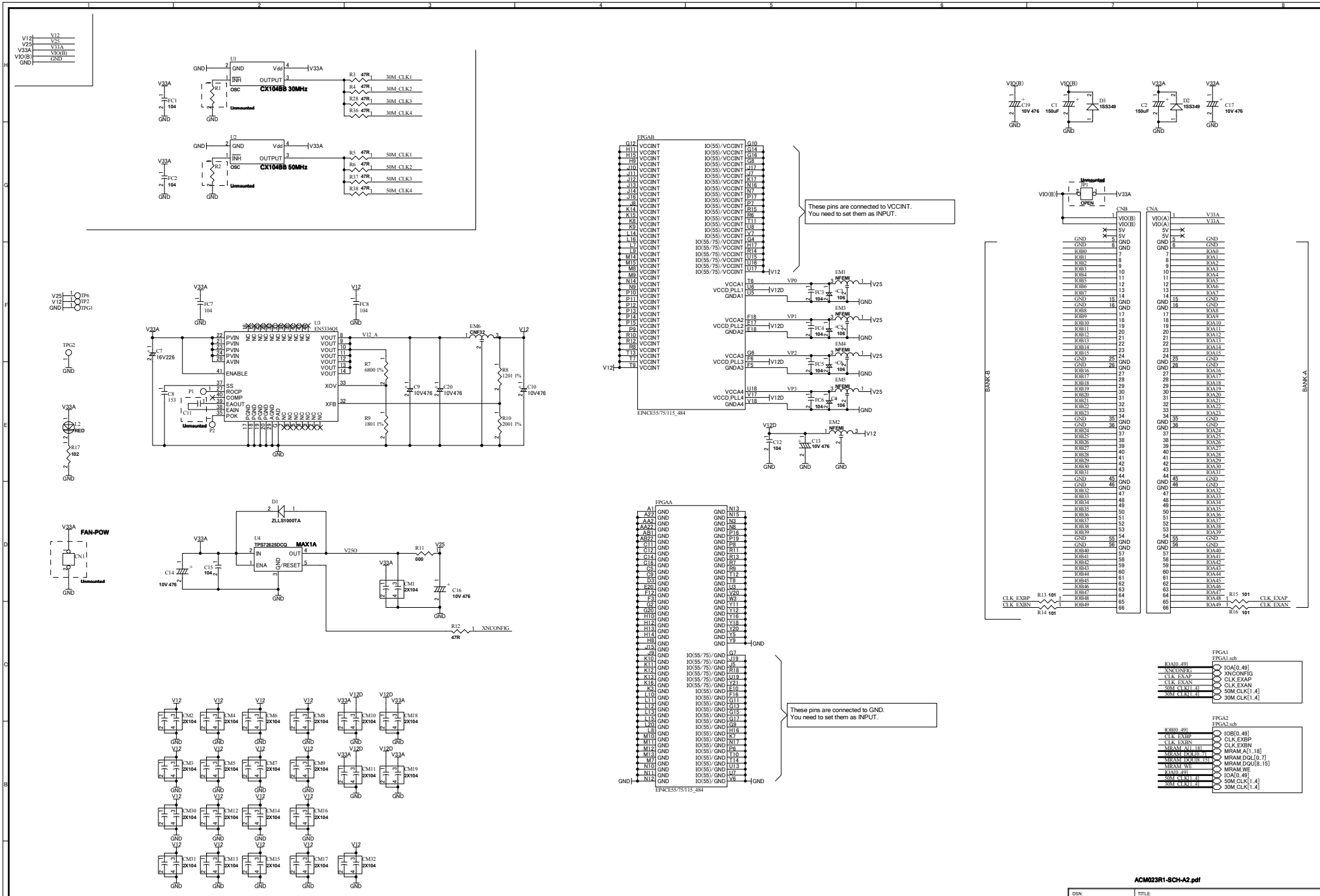

ACM023R1-SCH-A2.pdf

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TITLE:	Altera Cyclone IV E F484 FPGA board
DOC. No:	ACM-023
	A2
FILE: ACM023A.sch	DATE: 19-Jan-2018 09:56:23
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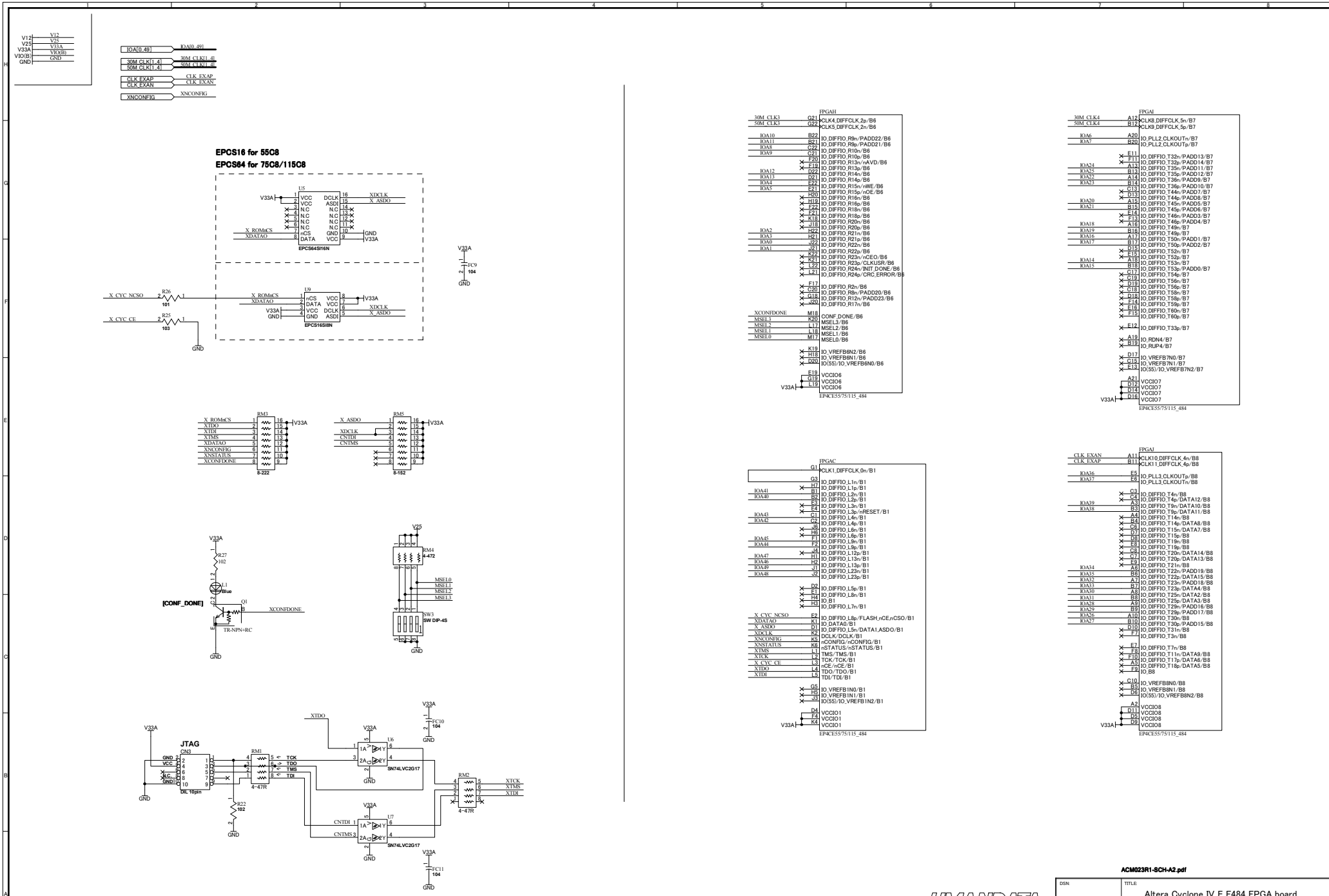
V5.20150107


V12
V25
V33A
VIO(B)
GND
IOA[0..49]
30M_CLK[1..4]
50M_CLK[1..4]
CLK_EXAP
CLK_EXAN
XNCONFIG
EPCS16 for 55C8
EPCS64 for 75C8/115C8
U5
EPCS64SI16N
U9
EPCS16SI8N
X_CYC_NCSO
X_CYC_CE
R26
101
R25
103
FC9
104
RM3
8-222
RM5
8-152
R27
102
L1
Blue
[CONF_DONE]
Q1
TR-NPN+RC
XCONFDONE
RM4
4-472
SW3
SW DIP-4S
MSEL0
MSEL1
MSEL2
MSEL3
JTAG
CN3
DIL 10pin
RM1
4-47R
TCK
TDO
TMS
TDI
R22
102
XTDO
U6
U7
SN74LVC2G17
CNTDI
CNTMS
FC10
FC11
RM2
XTCK
XTMS
XTDI
FPGAH
EP4CE55/75/115_484
FPGAI
FPGAC
FPGAJ
30M_CLK3
50M_CLK3
30M_CLK4
50M_CLK4
CLK_EXAN
CLK_EXAP
XCONFDONE
X_CYC_NCSO
X_CYC_CE
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TITLE
Altera Cyclone IV E F484 FPGA board
DOC. No.
ACM-023
A2
FILE: FPGA1.sch
DATE: 19-Jan-2018 09:56:23
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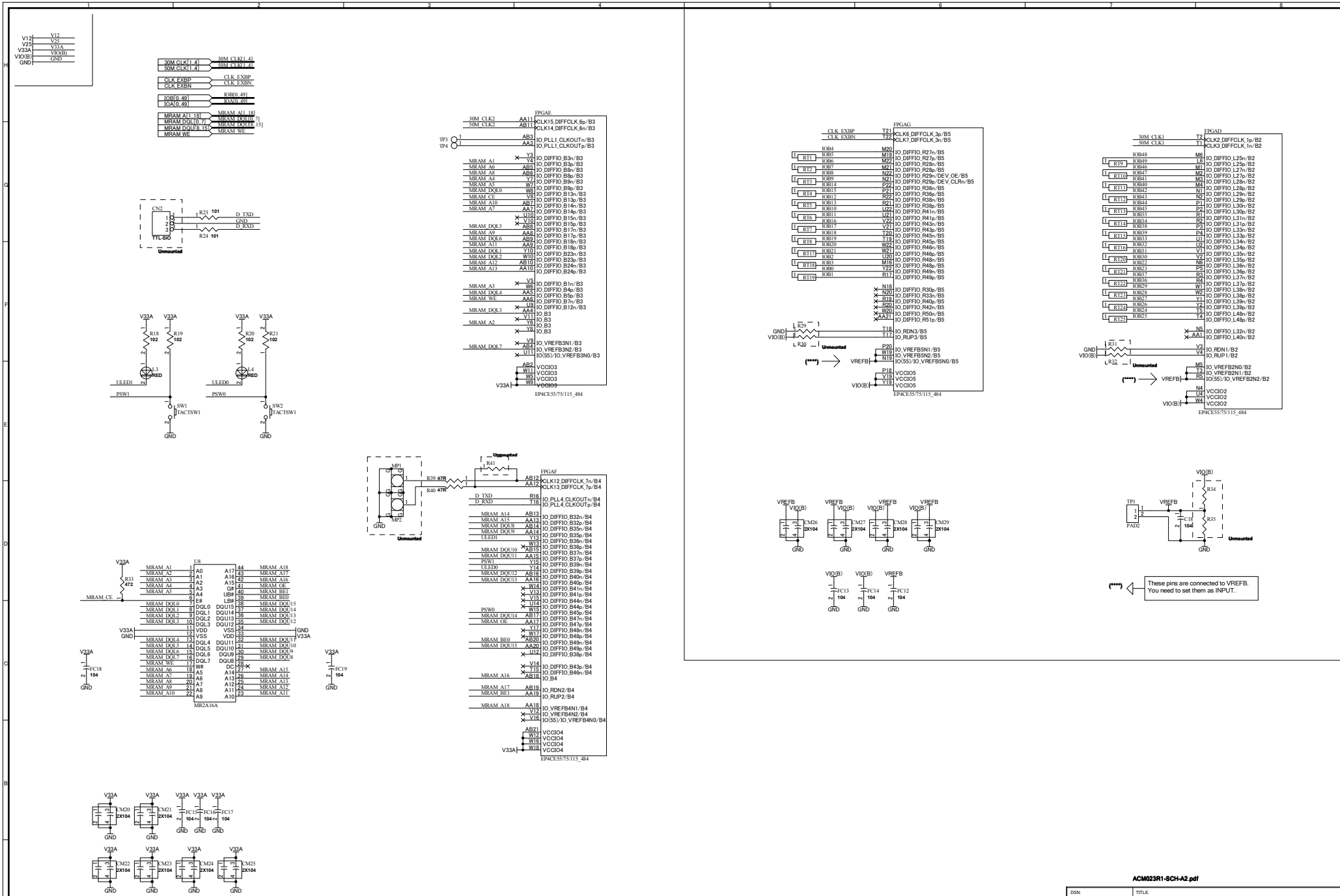
V5.20150107


V12
V25
V33A
VIO(B)
GND
30M_CLK[1..4]
50M_CLK[1..4]
CLK_EXBP
CLK_EXBN
IOB[0..49]
IOA[0..49]
MRAM_A[1..18]
MRAM_DQL[0..7]
MRAM_DQU[8..15]
MRAM_WE
FPGAE
EP4CE55/75/115_484
FPGAG
FPGAD
FPGAF
CN2
TTL-SIO
Unmounted
R23 101
R24 101
D_TXD
D_RXD
R18 102
R19 102
R20 102
R21 102
L3 RED
L4 RED
ULED1
ULED0
PSW1
PSW0
SW1 TACTSW1
SW2 TACTSW1
MP1
MP2
R39 47R
R40 47R
R41
U8
MR2A16A
R33 472
MRAM_CE
FC18 104
FC19 104
CM20 2X104
CM21 2X104
FC15 104
FC16 104
FC17 104
CM22 2X104
CM23 2X104
CM24 2X104
CM25 2X104
CM26 2X104
CM27 2X104
CM28 2X104
CM29 2X104
FC13 104
FC14 104
FC12 104
R29 R30
R31 R32
R34
R35
C18 104
TP1
PATS
(****)
These pins are connected to VREFB.
You need to set them as INPUT.
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DSN:
TITLE:
Altera Cyclone IV E F484 FPGA board
DOC. No:
ACM-023
A2
FILE: FPGA2.sch
DATE: 19-Jan-2018 09:56:24
Sheet: 3 / 3
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