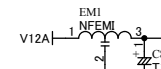
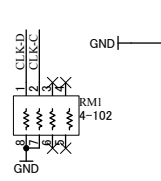
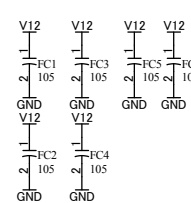
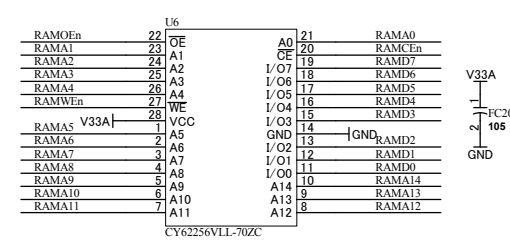
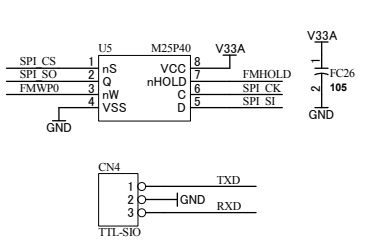
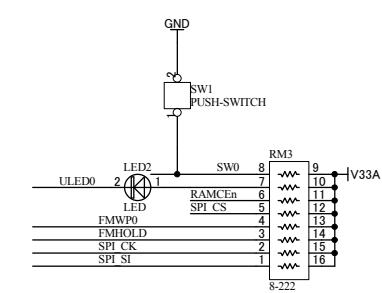
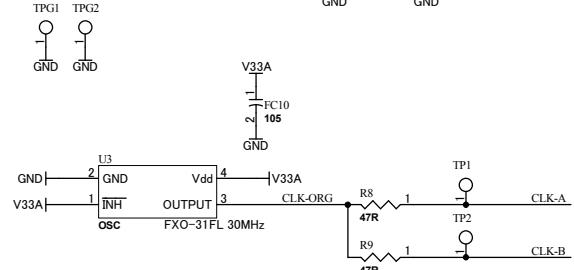
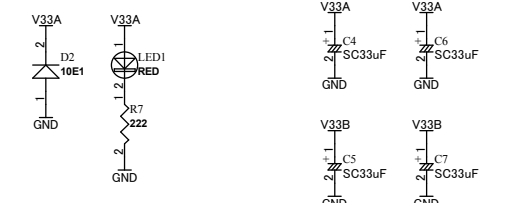
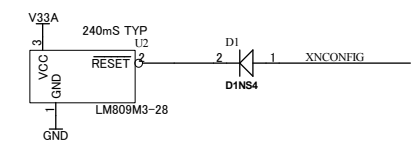
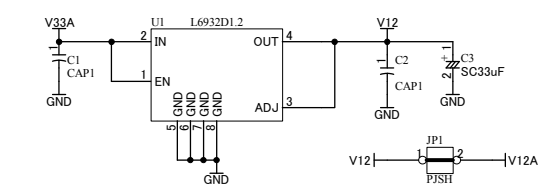
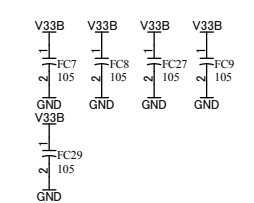


電源回路



FPGA		IOB	
X ASDO	1	B1/IO ASDO	208
X CYC NCSO	2	B1/IO nCSO	207
IOA0	3	B1/IO LVDS15p	206
IOA1	4	B1/IO LVDS15n	205
IOA2	5	B1/IO LVDS17p	204
IOA3	6	B1/IO LVDS14n	203
IOA4	7	B1/VCCIO1	202
IOA5	8	B1/IO LVDS13p	201
IOA6	9	B1/IO LVDS13n	200
IOA7	10	B1/IO LVDS12p	199
IOA8	11	B1/IO LVDS12n	198
IOA9	12	B1/IO VREFB1N0	197
IOA10	13	B1/IO LVDS8p	196
X TDO	14	B1/IO LVDS8n	195
X TMS	15	B1/TDO	194
X TCK	16	B1/TMS	193
X TDI	17	B1/TCK	192
XDATA0	18	B1/IO LVDS24p	191
X DCLK	19	B2/VCCINT	190
X CLK CE	20	B1/DATA0 DATA0	189
CLK-C	21	B1/DCLK DCLK	188
CLK-D	22	B1/IO LVDS25p	187
XNCONFNG	23	B1/IO LVDS25n	186
CLK-A	24	B2/IO LVDS26p	185
CLK-B	25	B2/IO LVDS26n	184
IOA11	26	B2/VCCIO2	183
IOA12	27	B1/nCONFIG	182
IOA13	28	B1/CLK2 LVDSCLK1p <input(1)< input=""></input(1)<>	181
IOA14	29	B1/CLK1 LVDSCLK1n <input(1)< input=""></input(1)<>	180
IOA15	30	B2/IO LVDS29p	179
IOA16	31	B2/IO LVDS29n	178
IOA17	32	B2/VCCINT	177
IOA18	33	B1/IO LVDS7p	176
IOA19	34	B1/IO LVDS7n	175
IOA20	35	B1/VCCINT/IO	174
IOA21	36	B1/IO LVDS5p	173
IOA22	37	B1/IO LVDS5n	172
IOA23	38	B2/VCCIO2	171
IOA24	39	B2/IO LVDS3p	170
IOA25	40	B2/VREFB2N0	169
IOA26	41	B2/IO LVDS34p	168
IOA27	42	B2/IO LVDS34n	167
IOA28	43	B2/VCCIO2	166
IOA29	44	B2/VCCIO2	165
IOA30	45	B2/IO LVDS35p	164
IOA31	46	B2/IO LVDS35n	163
IOA32	47	B2/IO LVDS36p	162
IOA33	48	B2/IO LVDS36n	161
IOA34	49	B2/IO LVDS37p	160
IOA35	50	B2/IO LVDS37n	159
IOA36	51	B2/GNDAPLL2	158
IOA37	52	B2/VCCAPLL2	157
IOA38	53	B3/GNDPLL2	156
IOA39	54	B3/VCCPLL2	155
IOA40	55	B3/GNDPLL2	154
IOA41	56	B3/VCCPLL2	153
IOA42	57	B3/IO LVDS77n	152
IOA43	58	B3/IO LVDS77p	151
IOA44	59	B3/IO LVDS78n	150
IOA45	60	B3/IO LVDS78p	149
IOA46	61	B3/IO LVDS79n	148
IOA47	62	B3/IO LVDS79p	147
IOA48	63	B3/IO LVDS39n	146
IOA49	64	B3/IO LVDS39p	145
IOA50	65	B3/VREFB3N0	144
IOA51	66	B3/IO LVDS42n	143
IOA52	67	B3/IO LVDS42p	142
IOA53	68	B3/IO LVDS44n	141
IOA54	69	B3/IO LVDS44p	140
IOA55	70	B3/VCCINT	139
IOA56	71	B4/VCCAPLL1	138
IOA57	72	B4/GNDAPLL1	137
IOA58	73	B4/VCCAPLL1	136
IOA59	74	B4/IO LVDS64p	135
IOA60	75	B4/IO LVDS64n	134
IOA61	76	B4/IO LVDS66p	133
IOA62	77	B4/IO LVDS66n	132
IOA63	78	B4/IO LVDS68p	131
IOA64	79	B4/IO LVDS68n	130
IOA65	80	B4/VCCINT	129
IOA66	81	B4/IO LVDS48p	128
IOA67	82	B4/IO LVDS48n	127
IOA68	83	B3/MSELO	126
IOA69	84	B3/MSELI	125
IOA70	85	B3/MSELI	124
IOA71	86	B3/VCCIO3	123
IOA72	87	B3/CONF DONE	122
IOA73	88	B3/VCCIO3	121
IOA74	89	B3/nSTATUS	120
IOA75	90	B3/VCCINT/IO	119
IOA76	91	B3/IO	118
IOA77	92	B3/IO	117
IOA78	93	B3/VREFB3N1	116
IOA79	94	B3/IO LVDS51p	115
IOA80	95	B3/IO LVDS51n	114
IOA81	96	B3/IO LVDS52p	113
IOA82	97	B3/IO LVDS52n	112
IOA83	98	B3/IO LVDS53p	111
IOA84	99	B3/IO LVDS53n	110
IOA85	100	B3/VCCIO3	109
IOA86	101	B3/IO LVDS54p	108
IOA87	102	B3/IO LVDS54n	107
IOA88	103	B3/IO LVDS55p	106
IOA89	104	B3/IO LVDS55n	105

【重要】  
EP2C5の時、これらのピンは、GNDまたはVCCINTに固定されています。  
設計データではダメージ入力としてください。



BANK-B

