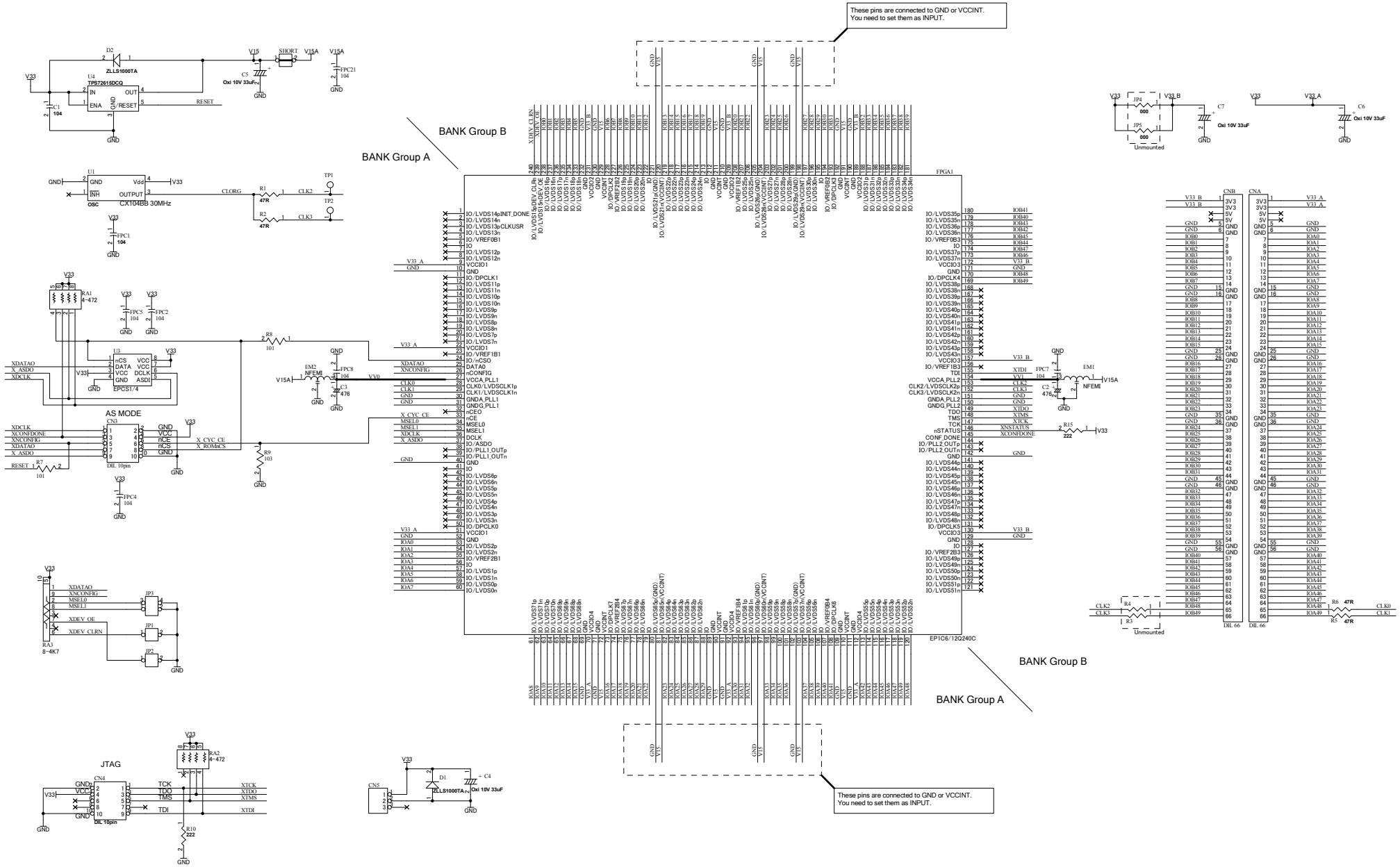
These pins are connected to GND or VCCINT.
You need to set them as INPUT.
BANK Group B
BANK Group A
FPGA1
EP1C6/12Q240C
CX104BB 30MHz
EPCS1/4
AS MODE
JTAG
ACM006R2-SCH-B4.sch.pdf
Altera Cyclone Q240 FPGA board (Rev 2)
HuMANDATA LTD.
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ACM-006
B4
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