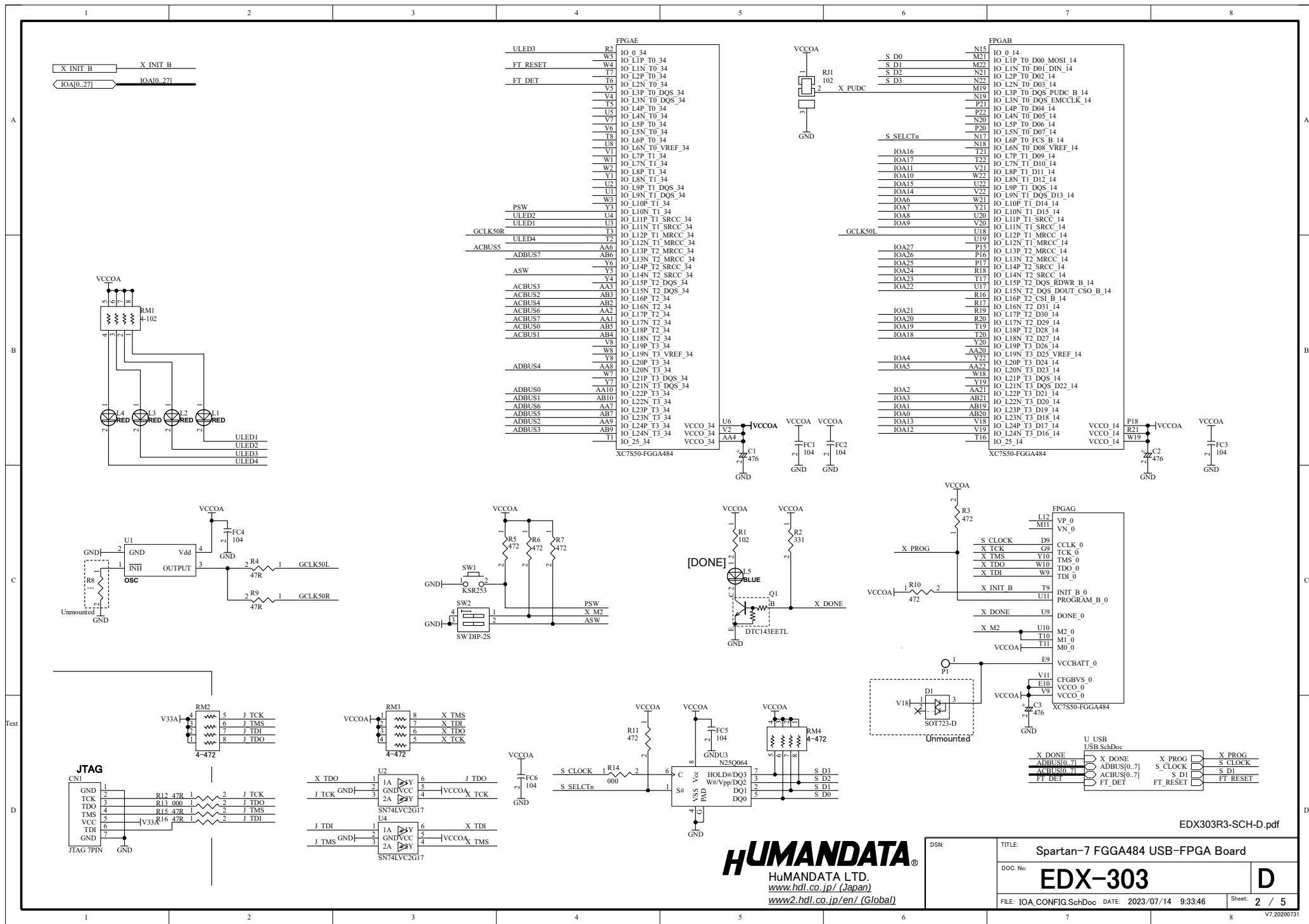
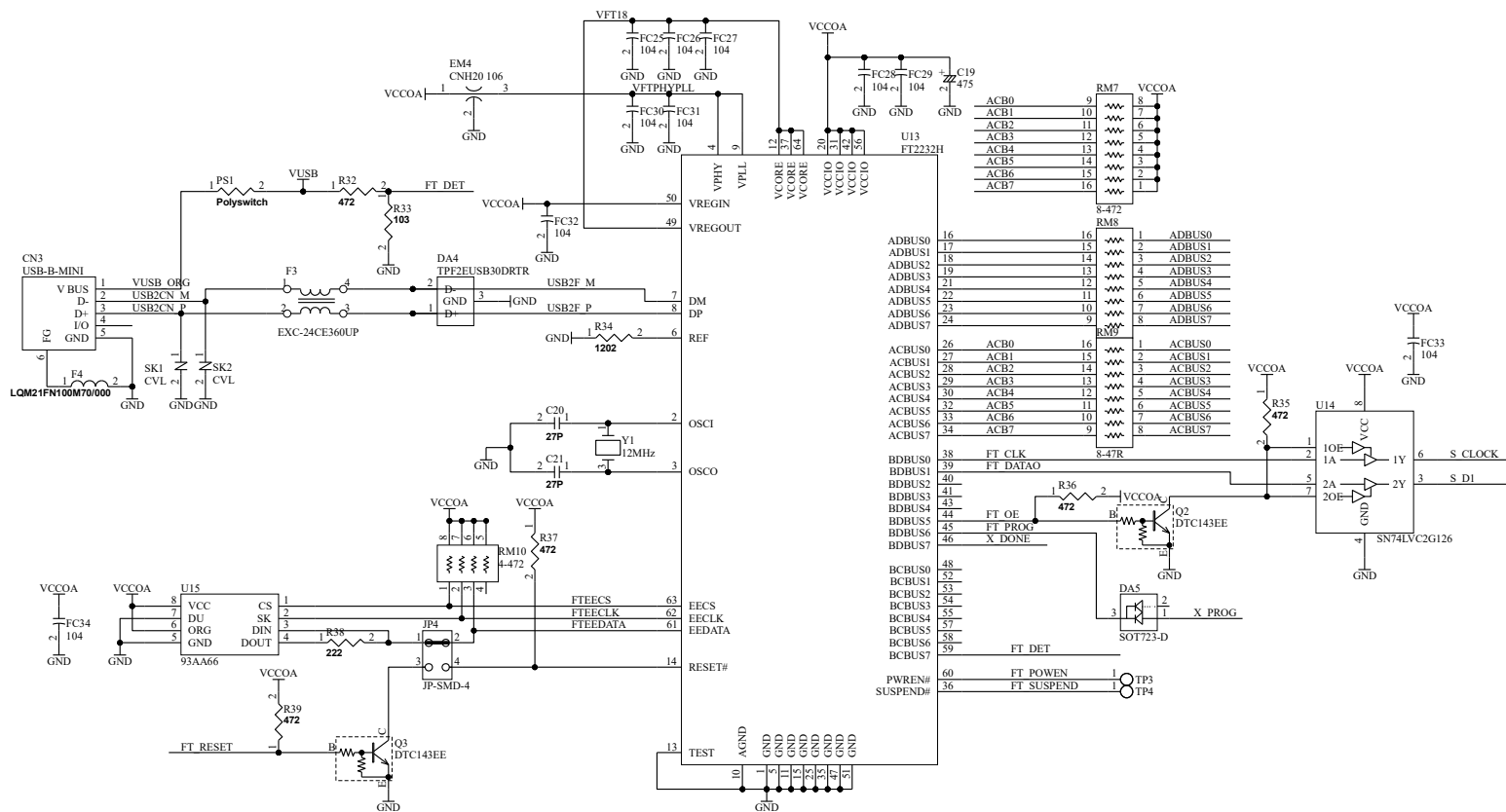






ADBUS[0..7]	ADBUS[0..7]
ACBUS[0..7]	ACBUS[0..7]
S CLOCK	S D1
S D1	
X PROG	X DONE
X DONE	
FT DET	FT DET
FT RESET	FT RESET



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