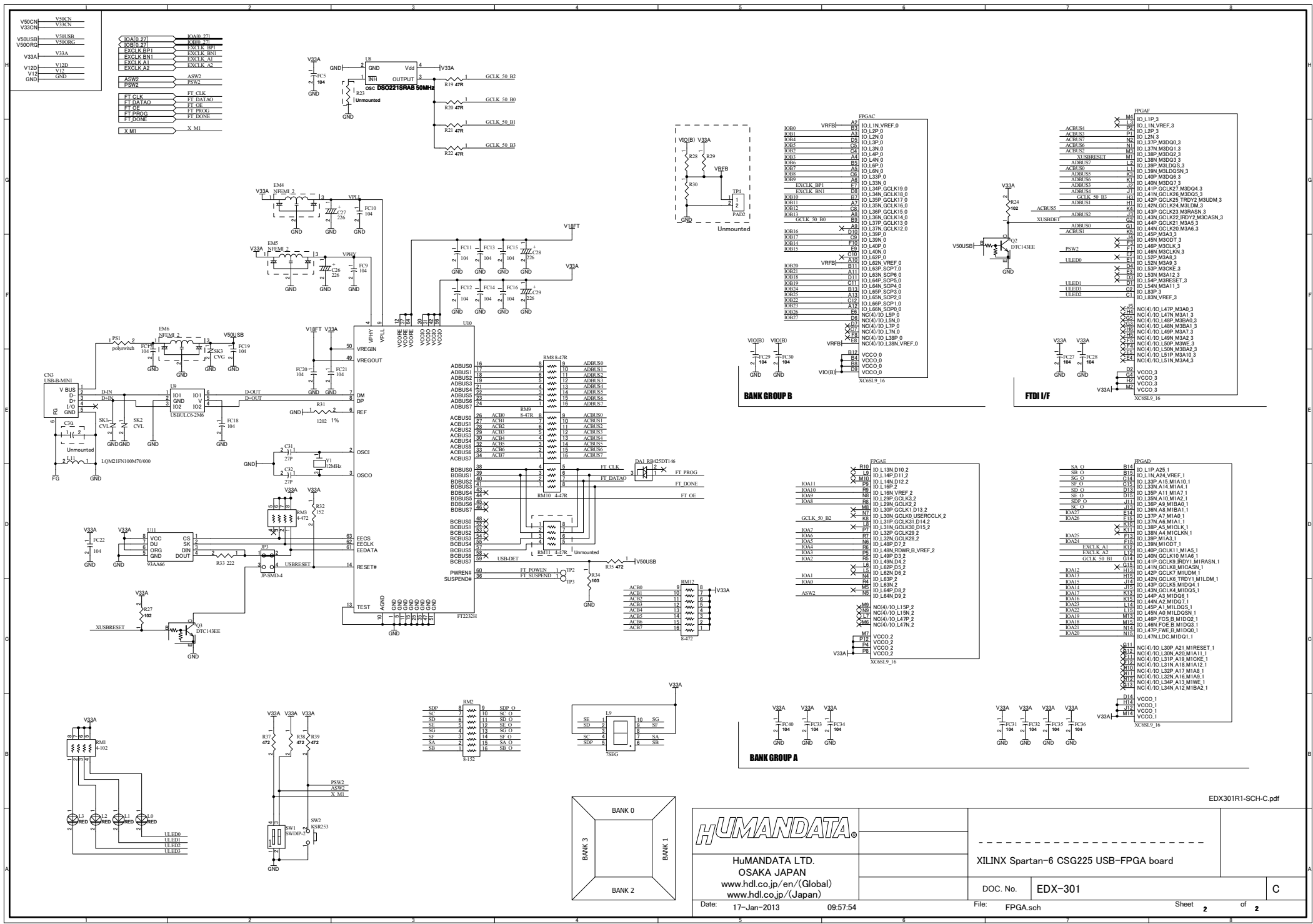



IPC#	FPGA_sch
R40(0.27)	IOA0(0.27)
EXCLK_BP1	IOB0(0.27)
EXCLK_BN1	EXCLK_BN1
EXCLK_A1	EXCLK_A1
EXCLK_A2	EXCLK_A2
ASW1	ASW1
PSW2	PSW2
FT_CLK	FT_CLK
FT_DATA0	FT_DATA0
FT_DATA1	FT_DATA1
FT_PROG	FT_PROG
FT_DONE	FT_DONE
FT_DONE	FT_DONE
X_M1	X_M1

HuMANDATA LTD.  
OSAKA JAPAN  
www.hdl.co.jp/en/(Global)  
www.hdl.co.jp/(Japan)

XILINX Spartan-6 CSG225 USB-FPGA board

Date:	17-Jan-2013	09:57:53	File:	EDX301C.sch	Sheet 1 of 2
DOC. No.	EDX-301				C





**HUMAN DATA**  
HuMANDATA LTD.  
OSAKA JAPAN  
www.hdl.co.jp/en/(Global)  
www.hdl.co.jp/(Japan)

XILINX Spartan-6 CSG225 USB-FPGA board

DOC. No. EDX-301

Date: 17-Jan-2013 09:57:54

File: FPGA.sch

Sheet 2 of 2

EDX301R1-SCH-C.pdf