



**HUMANDATA**

有限会社ヒューマンデータ

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EDX-001B FPGA Trainer

DOC. No.

1

B

File: EDX\_TOP.sch

Sheet 1 of 3

Connection with the FPGA

USB_DATA[7:0]	USB_DATA[7:0]
USB_RXF	USB_RXF
USB_RD	USB_RD
USB_TXE	USB_TXE
USB_WR	USB_WR
RESET	RESET



