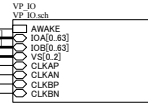
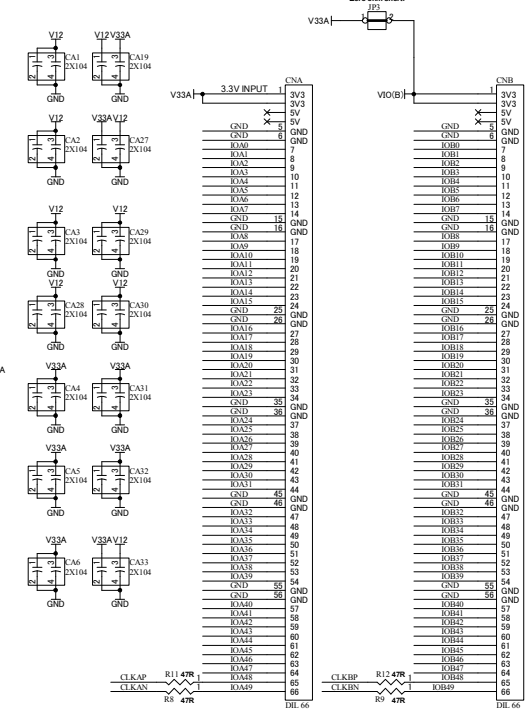
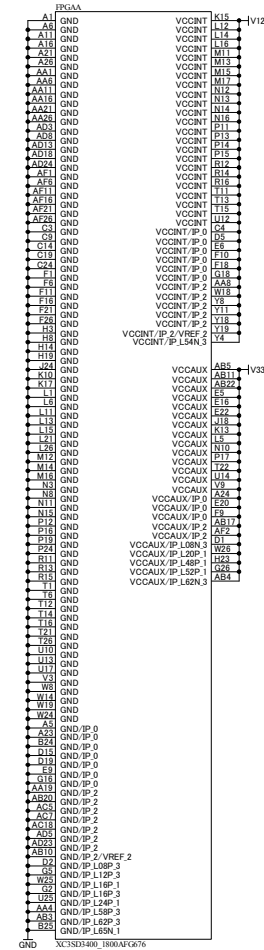
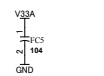
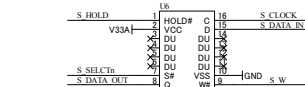
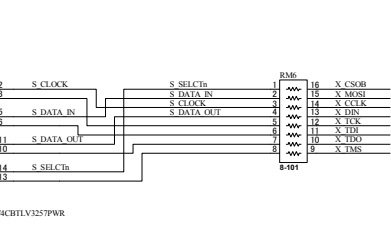
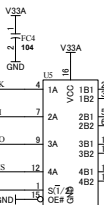
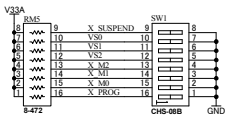
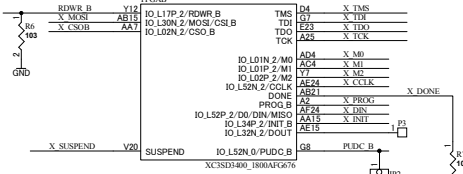
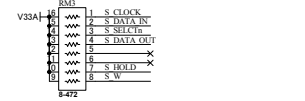
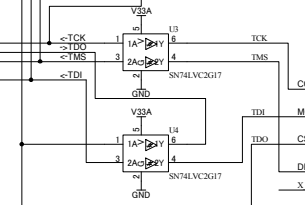
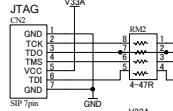
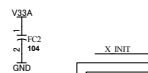
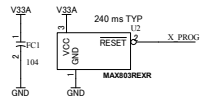
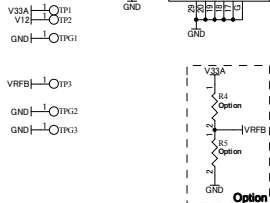
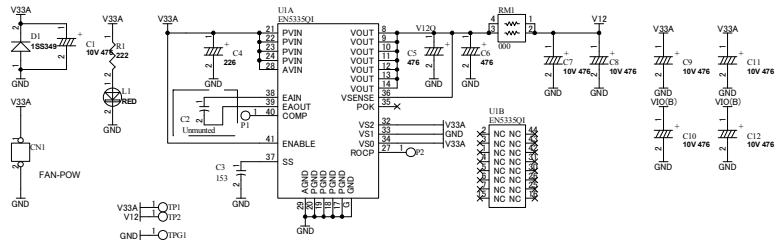
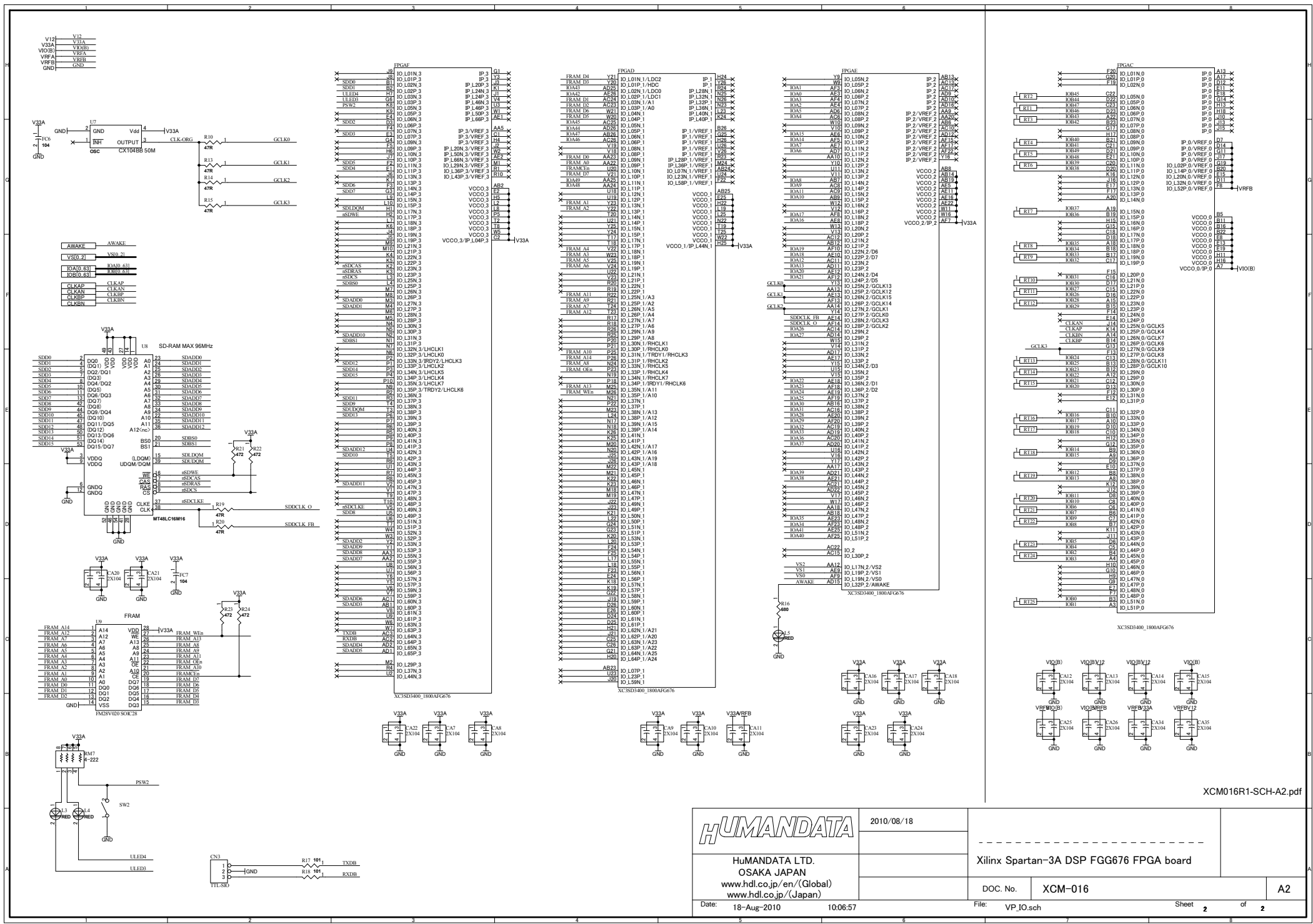


V12
V33A
V33B
VFB
VFB
VFB
GND



XCM016R1-SCH-A2.pdf



HUMAN DATA LTD.
OSAKA JAPAN
www.hdl.co.jp/en/(Global)
www.hdl.co.jp/(Japan)

2010/08/18

Xilinx Spartan-3A DSP FGG676 FPGA board

Date:	18-Aug-2010	Time:	10:06:57	File:	VP_IO.sch	Sheet:	2	of:	2
-------	-------------	-------	----------	-------	-----------	--------	---	-----	---

DOC. No. XCM-016

A2