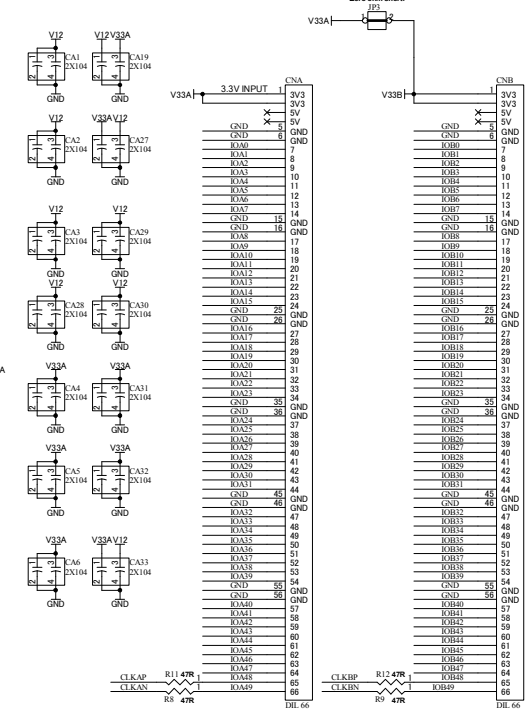
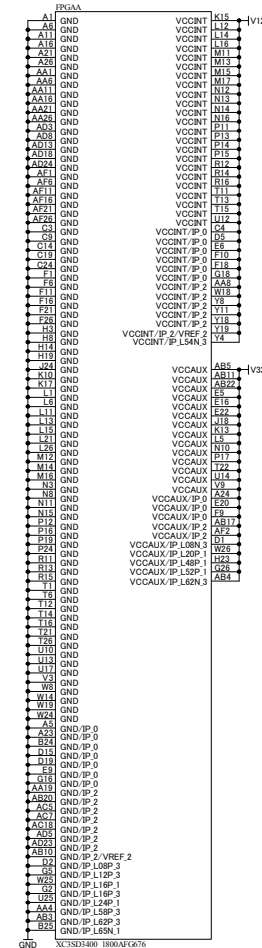
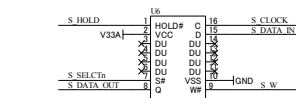
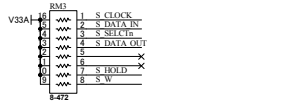
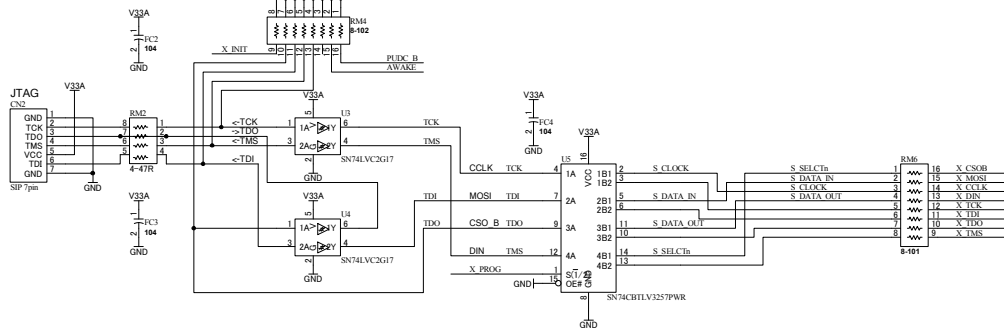
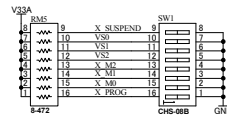
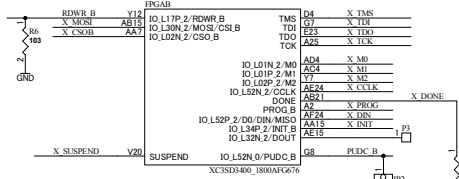
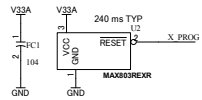
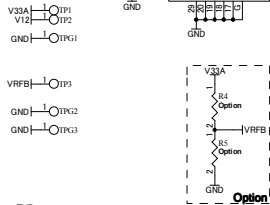
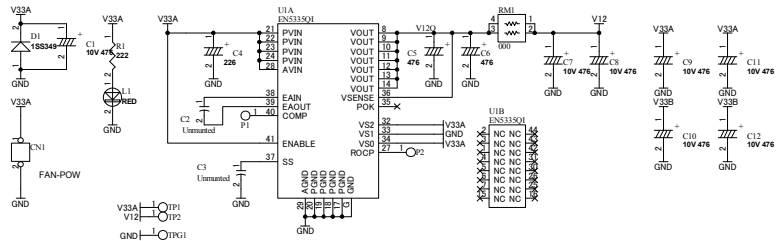


V12
V33A
V33B
V33S
V33S
VRF4
VRF4
VRF4
VRF4
GND



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	2010/03/15	Xilinx Spartan-3A DSP FGG676 FPGA board	
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Date: 15-Mar-2010	09:04:37	File: XCM016A.sch	Sheet 1 of 2

