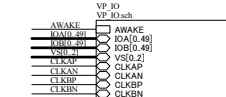
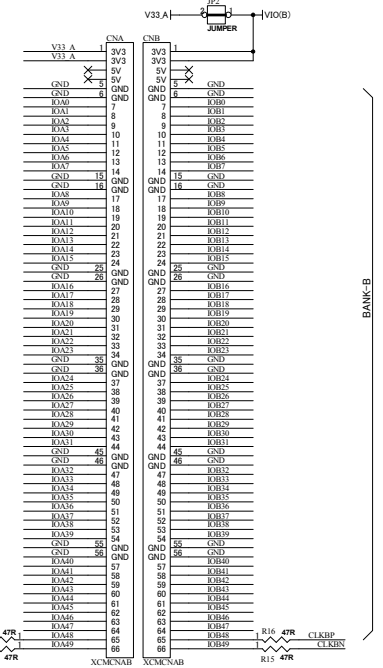
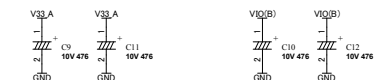
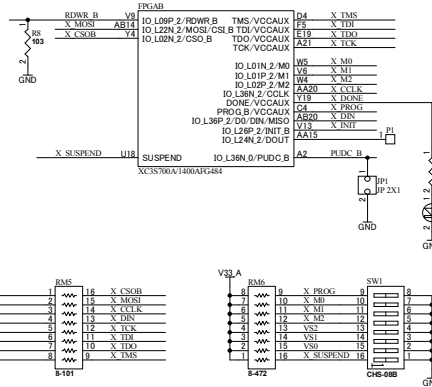
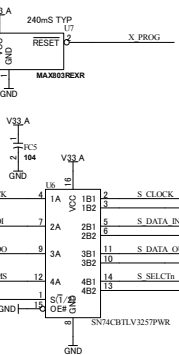
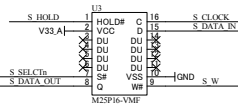
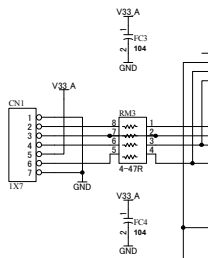
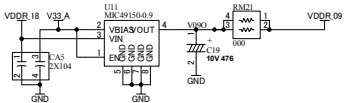
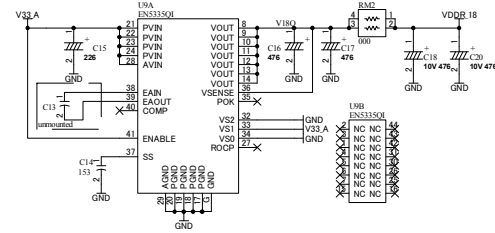
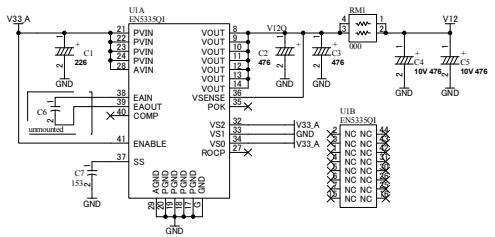
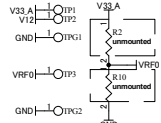
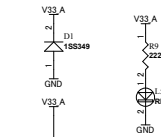


V12 V12_A
 V33_A V33_B
 VRF0 VRF0
 GND GND

VDDR_09 VDDR_09
 VDDR_18 VDDR_18



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