電源回路

2素子を1278に実装

JTAGコネクタ

2-7pinはショート

CONFIGURATION PINS
H : Hi-Z
L : Weak Pull-up

HUMANDATA	2007/10/12	
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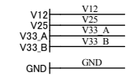

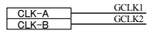

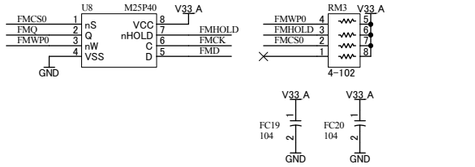

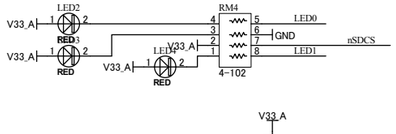

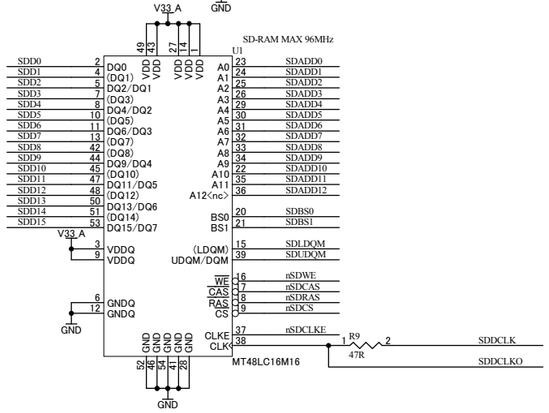

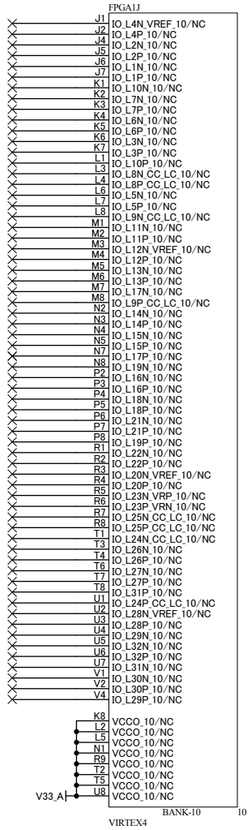

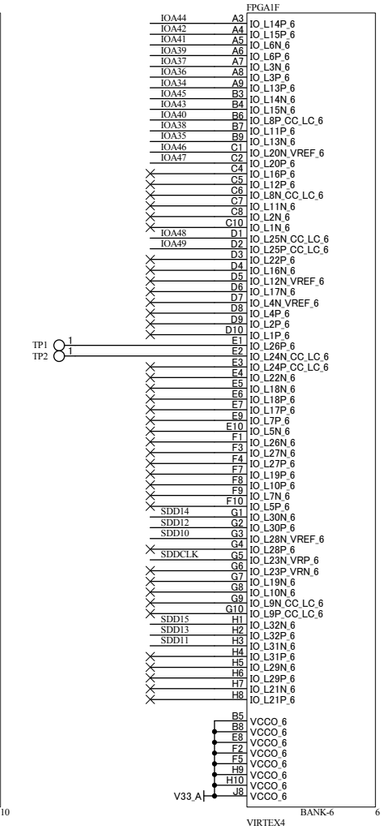

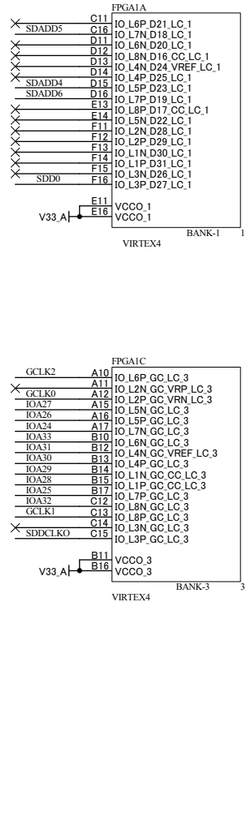

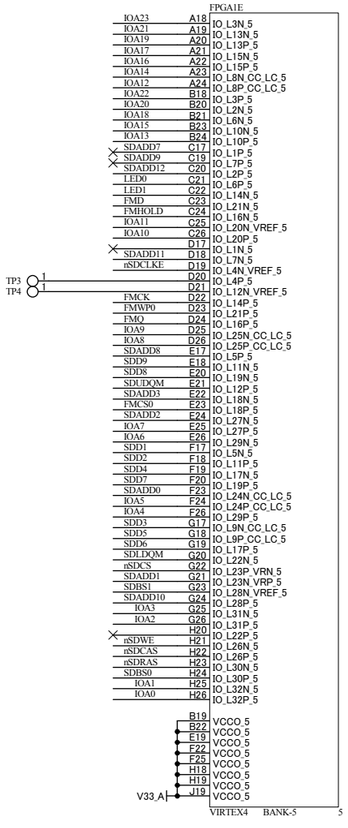

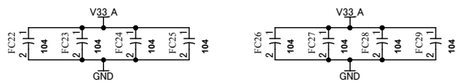

BANK-A

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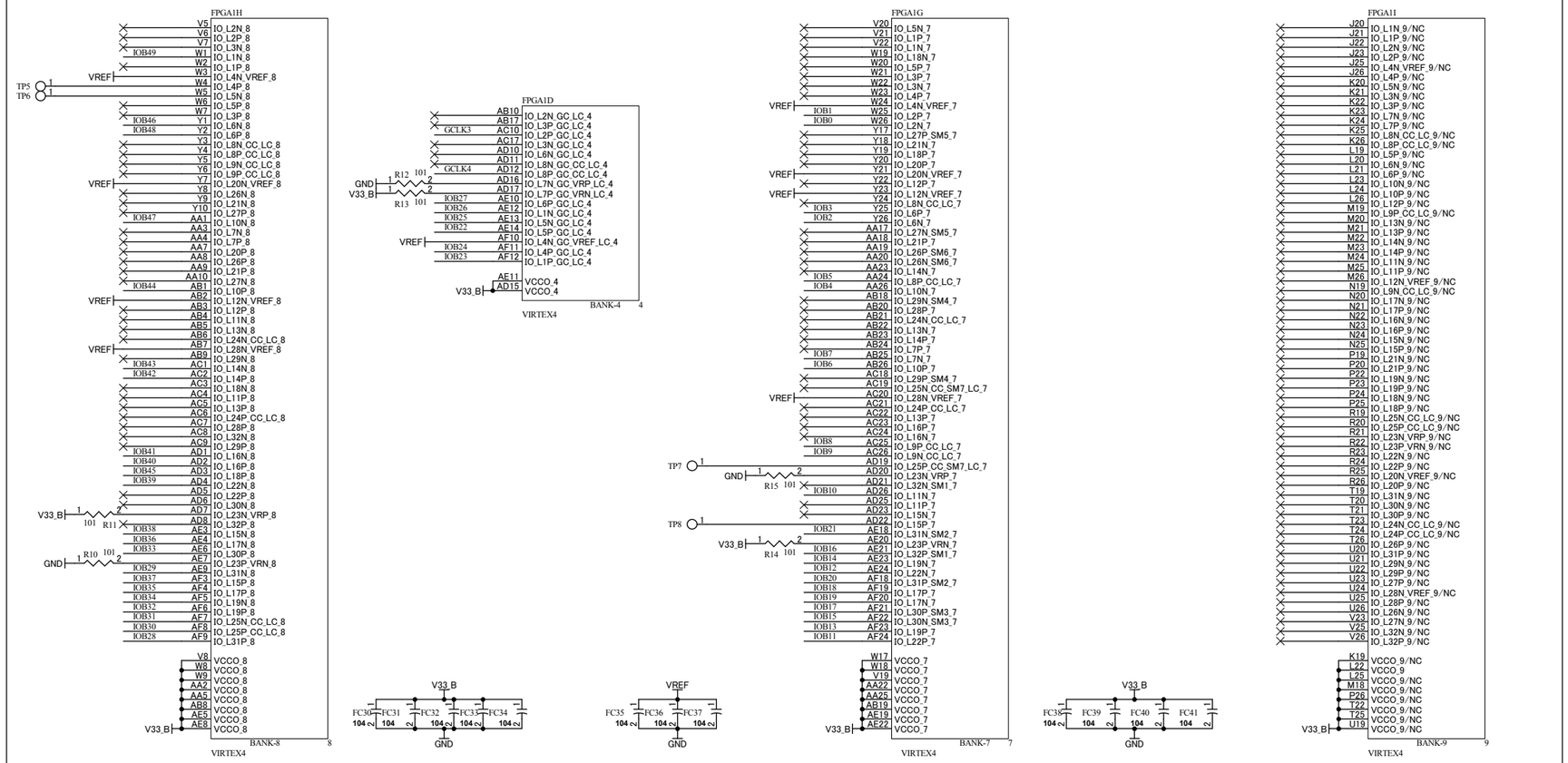
V12
V25
V33_A
V33_B
GND
VREF

IOB[0_49]

CLK-C
CLK-D
GCLK3
GCLK4



BANK-B

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