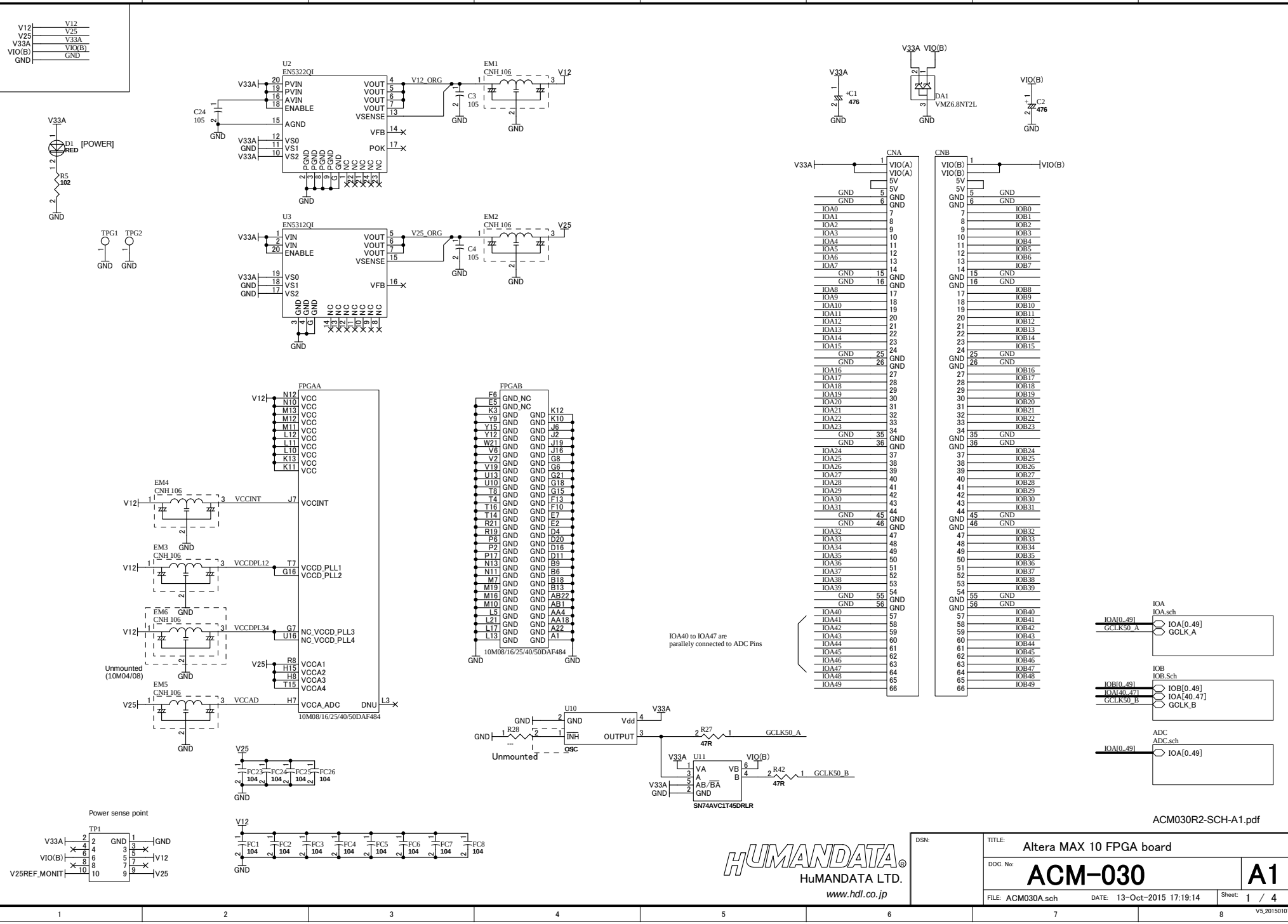
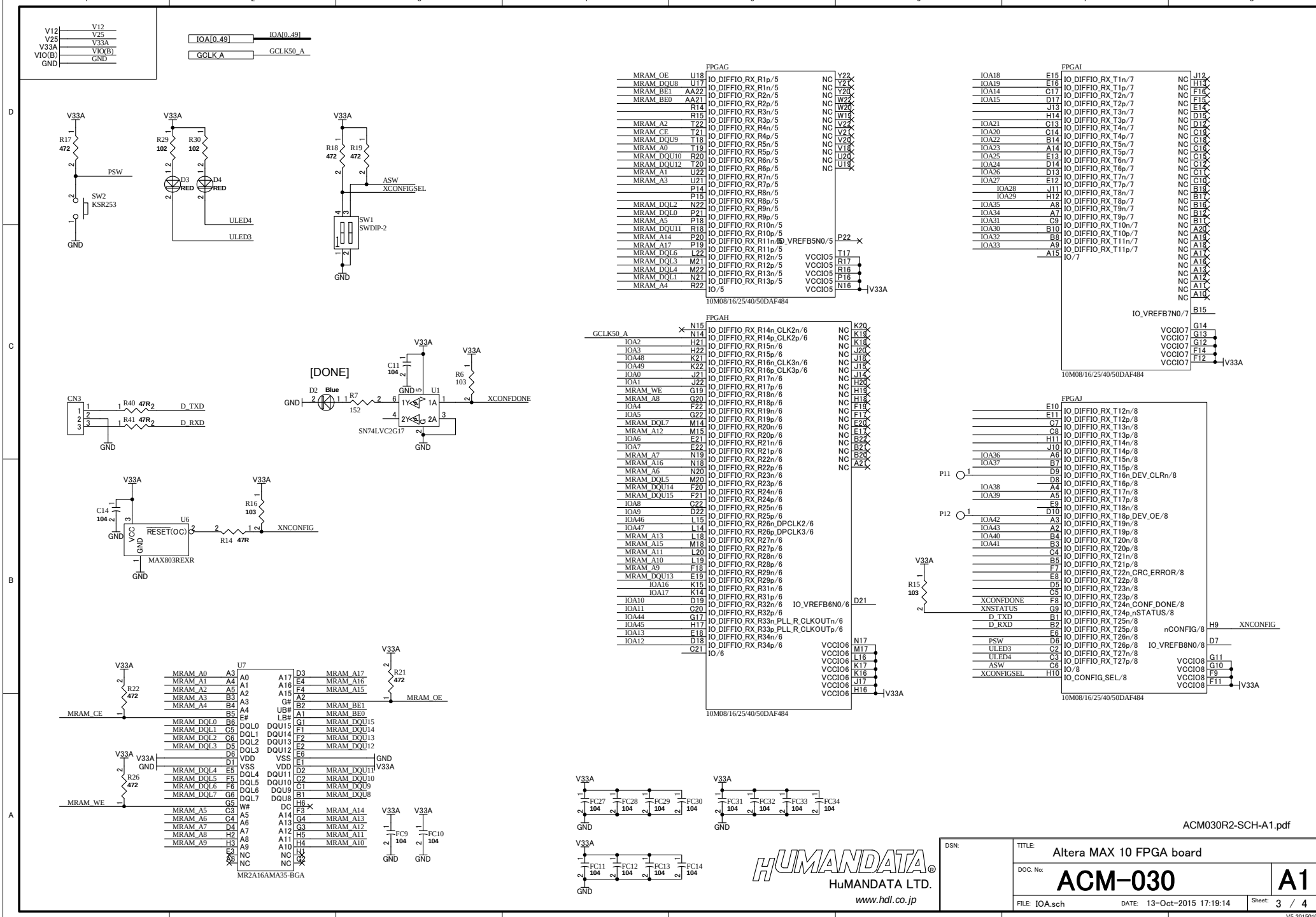
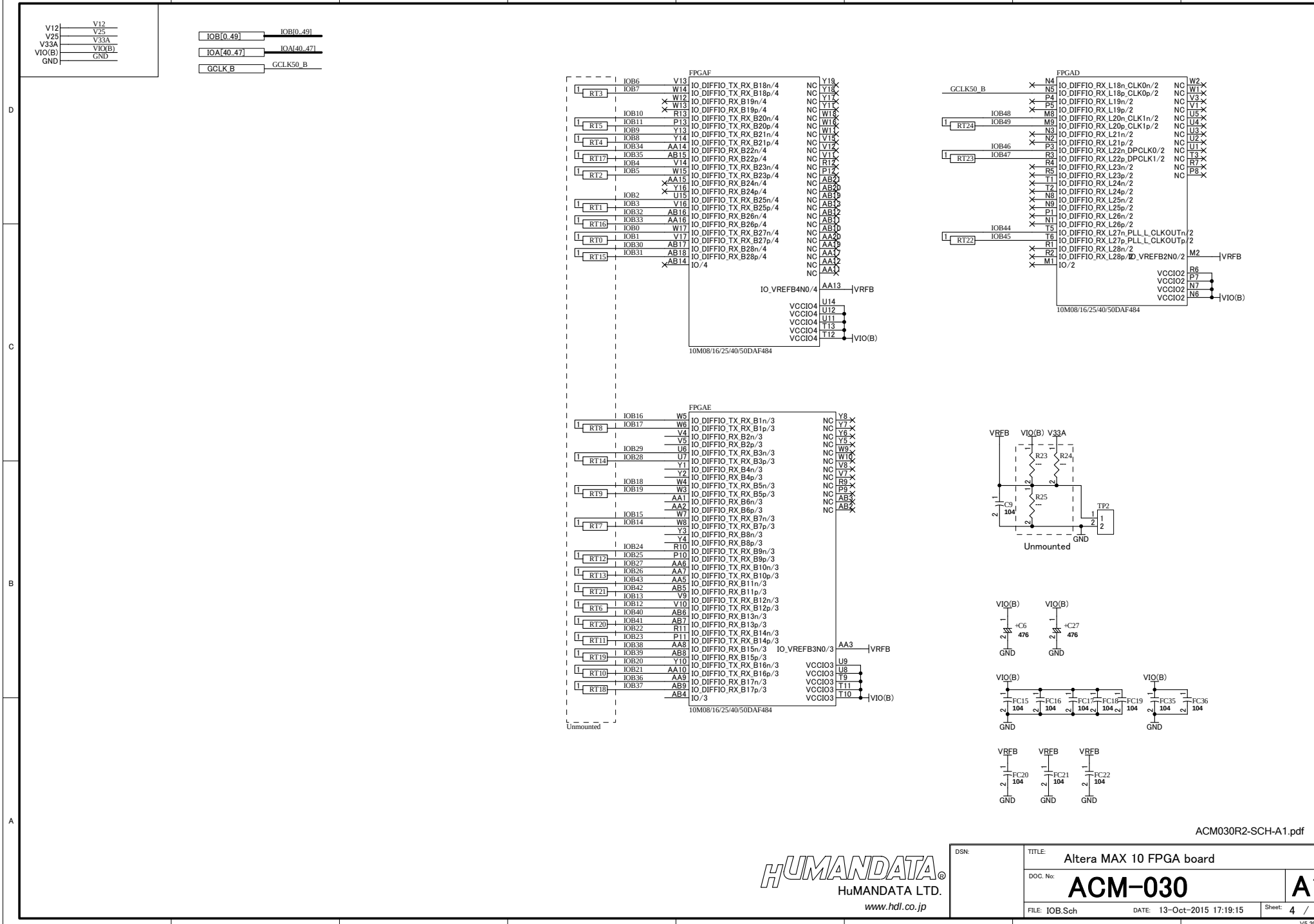


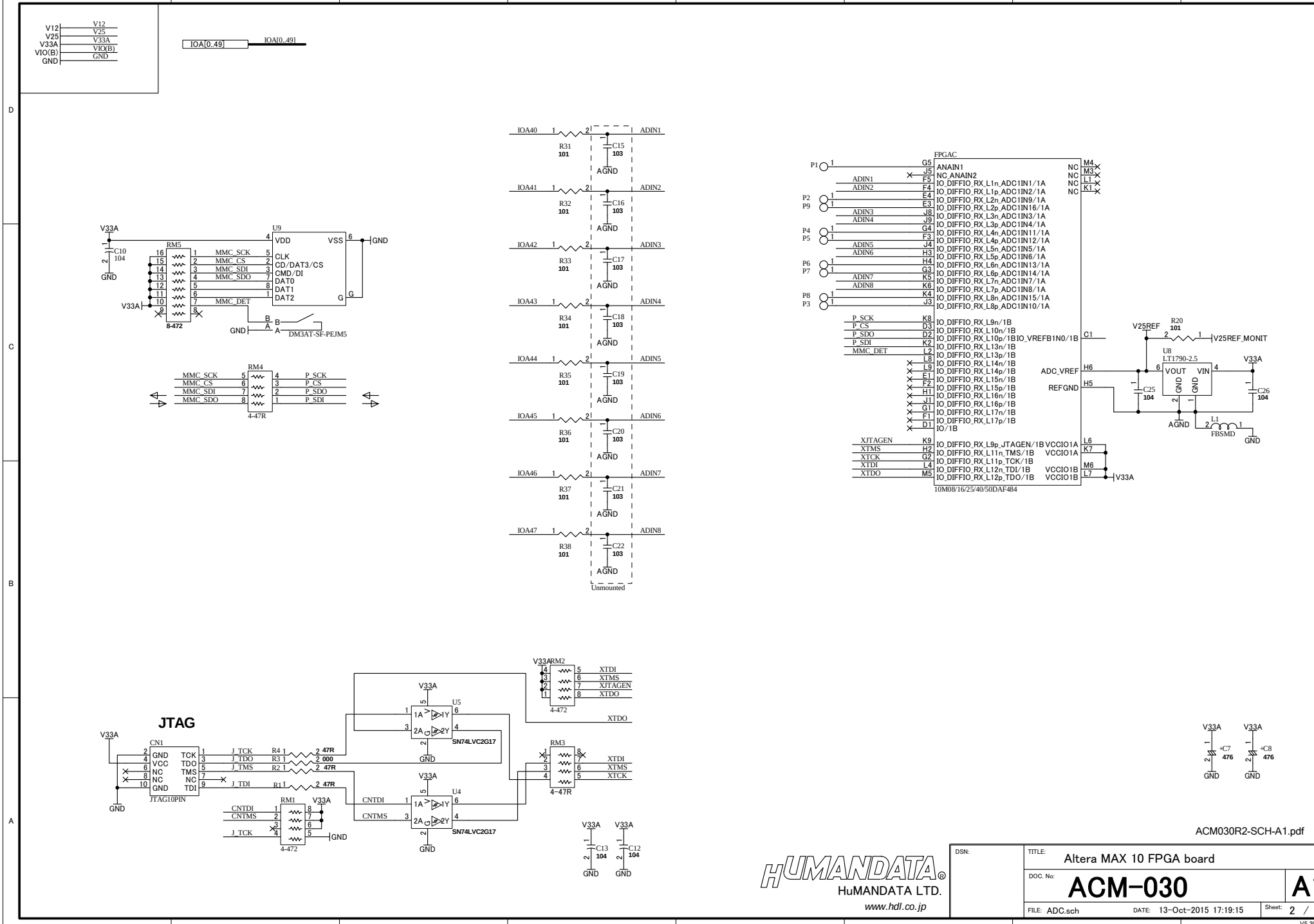
HuMANDATA LTD.
www.hdl.co.jp

DSN:	TITLE: Altera MAX 10 FPGA board	
	DOC. No: ACM-030	A1
	FILE: ACM030A.sch DATE: 13-Oct-2015 17:19:14	Sheet: 1 / 4

V5_20150107


V12
V25
V33A
VIO(B)
GND
IOA[0..49]
GCLK_A
FPGAG
FPGAH
FPGAI
FPGAJ
10M08/16/25/40/50DAF484
PSW
SW2
KSR253
D3
D4
RED
ULED4
ULED3
ASW
XCONFIGSEL
SW1
SWDIP-2
[DONE]
D2
Blue
R7
152
C11
104
U1
SN74LVC2G17
R6
103
XCONFDONE
CN3
R40
47R
D_TXD
R41
47R
D_RXD
C14
104
U6
RESET(OC)
MAX803REXR
R16
103
R14
47R
XNCONFIG
U7
MR2A16AMA35-BGA
R22
R21
R26
MRAM_CE
MRAM_OE
MRAM_WE
FC27
FC28
FC29
FC30
FC31
FC32
FC33
FC34
FC9
FC10
FC11
FC12
FC13
FC14
IO_VREFB5N0/5
IO_VREFB6N0/6
IO_VREFB7N0/7
IO_VREFB8N0/8
nCONFIG/8
IO_CONFIG_SEL/8
R15
103
P11
P12
HuMANDATA LTD.
www.hdl.co.jp
ACM030R2-SCH-A1.pdf
DSN:
TITLE:
Altera MAX 10 FPGA board
DOC. No:
ACM-030
A1
FILE: IOA.sch
DATE: 13-Oct-2015 17:19:14
Sheet: 3 / 4
V5_20150107


V12
V25
V33A
VIO(B)
GND
IOB[0..49]
IOA[40..47]
GCLK_B
GCLK50_B
FPGAF
FPGAD
FPGAE
10M08/16/25/40/50DAF484
IO_VREFB4N0/4
IO_VREFB3N0/3
IO_VREFB2N0/2
VRFB
VCCIO4
VCCIO3
VCCIO2
Unmounted
R23
R24
R25
C9
TP2
GND
+C6
+C27
FC15
FC16
FC17
FC18
FC19
FC35
FC36
FC20
FC21
FC22
ACM030R2-SCH-A1.pdf
HuMANDATA LTD.
www.hdl.co.jp
DSN:
TITLE: Altera MAX 10 FPGA board
DOC. No: ACM-030
A1
FILE: IOB.Sch
DATE: 13-Oct-2015 17:19:15
Sheet: 4 / 4
V5_20150107


V12
V25
V33A
VIO(B)
GND
IOA[0..49]
FPGAC
10M08/16/25/40/50DAF484
U9
RM5
8-472
DM3AT-SF-PEJM5
RM4
4-47R
Unmounted
U8
LT1790-2.5
V25REF_MONIT
FBSMD
JTAG
CN1
JTAG10PIN
U5
U4
SN74LVC2G17
RM1
RM2
RM3
4-472
4-47R
ACM030R2-SCH-A1.pdf
HuMANDATA LTD.
www.hdl.co.jp
Altera MAX 10 FPGA board
ACM-030
A1
ADC.sch
13-Oct-2015 17:19:15
2 / 4
V5_20150107