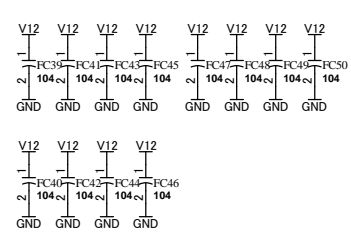
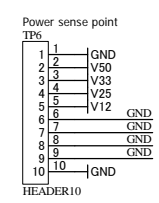
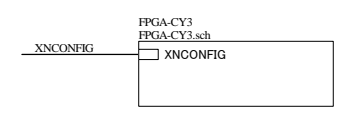
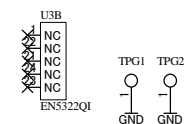
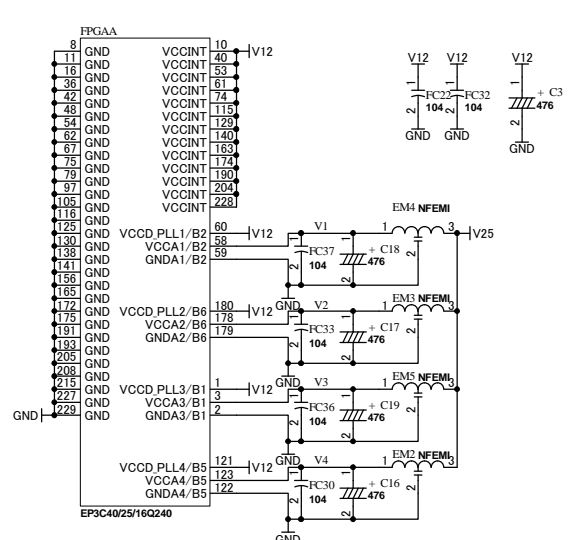
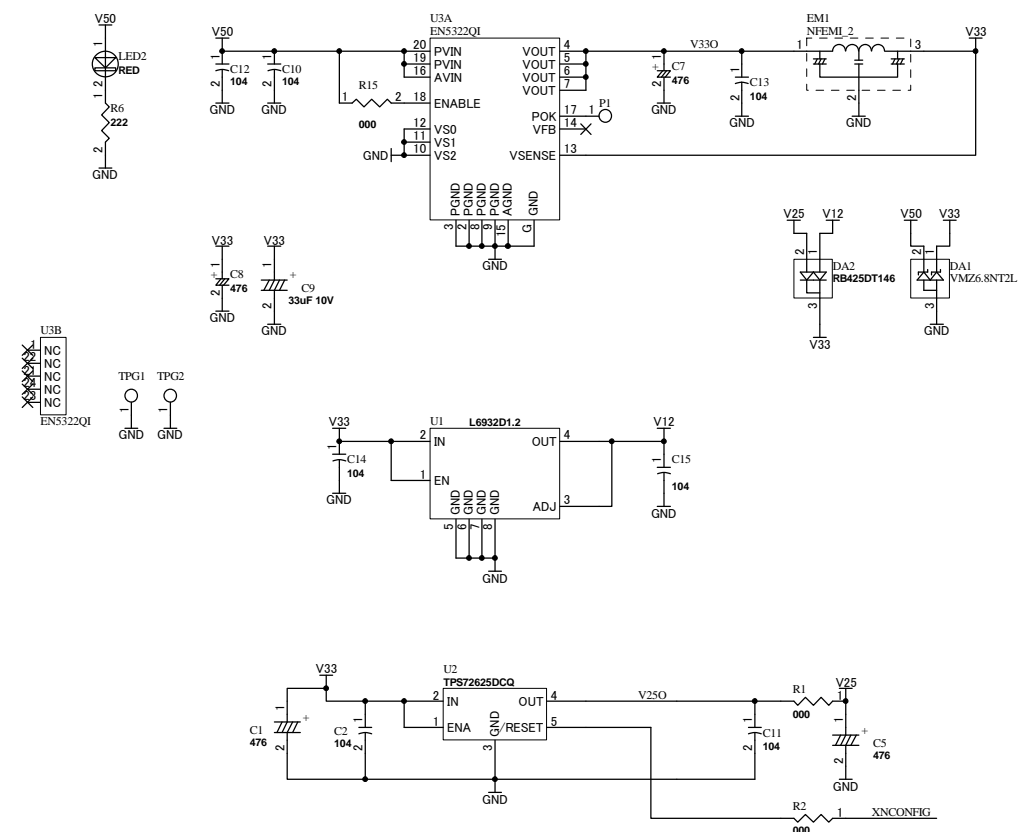


V50 V50  
 V33 V33  
 V12 V12  
 V25 V25  
 GND GND

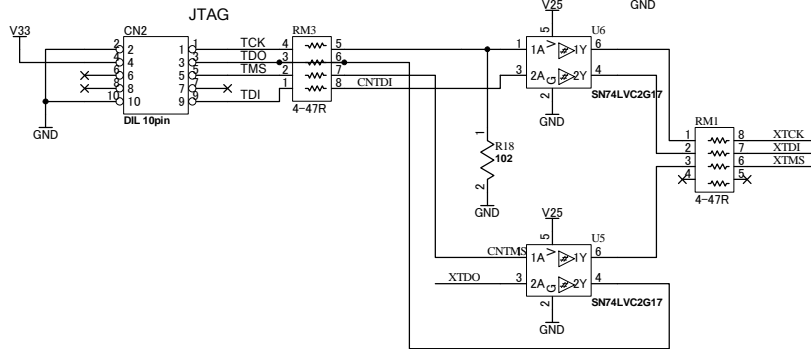
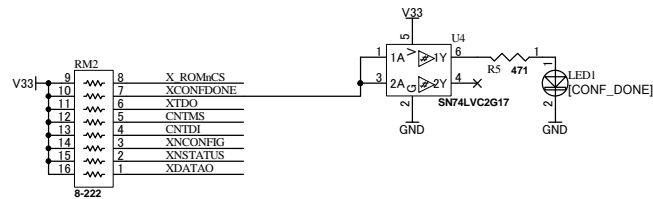
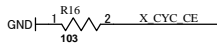
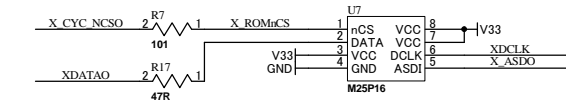
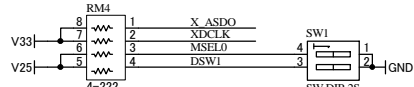
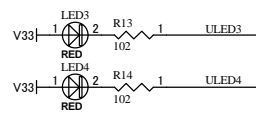
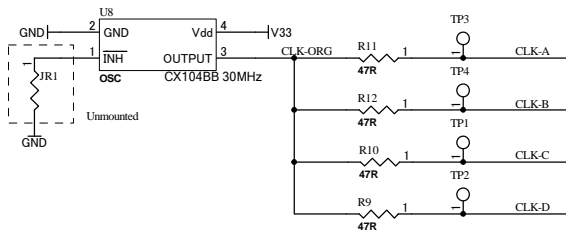
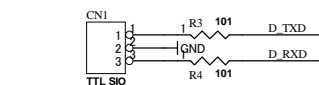


ACM029YR1-SCH-A.pdf

		Altera CycloneIII Q240 FPGA board - 5V I/O	
Date: 13-Mar-2014 16:46:35		File: ACM029A.sch	
Sheet 1 of 3		Page 1 of 3	

V33| V33  
 V25| V25  
 GND| GND

XNCONFIG XNCONFIG



FPGA H	
CLK-A	209
	210
	CLK8/DIFFCLK_5n/B7
	CLK9/DIFFCLK_5p/B7
	VCCINT/IO/DIFFIO_T24p/B7
	GND/IO/DIFFIO_T23n/B7
IOA15	183
IOA16	184
IOA17	185
IOA18	186
IOA19	187
IOA20	188
IOA21	189
IOA22	190
IOA23	191
IOA24	192
IOA25	193
IOA26	194
IOA27	195
IOA28	196
IOA29	197
IOA30	198

FPGA I	
	192
	193
	VCCIO7/B7
	VCCIO7/B7
	211
	212
	CLK10/DIFFCLK_4n/B8
	CLK11/DIFFCLK_4p/B8
IOA31	214
IOA32	215
IOA33	216
IOA34	217
IOA35	218
IOA36	219
IOA37	220
IOA38	221
IOA39	222
IOA40	223
IOA41	224
IOA42	225
	233
	234
	DIRA0
	DIRA1
IOA43	235
IOA44	236
	VCCINT/IO/DIFFIO_T1n/B8
	GND/IO/DIFFIO_T1p/B8
IOA45	239
IOA46	240
	213
	225
	VCCIO8/B8
	VCCIO8/B8

FPGA B	
CLK-C	31
	32
	CLK0/DIFFCLK_0p/B1
	CLK1/DIFFCLK_0n/B1
	VCCINT/IO/DIFFIO_L1p/B1
	GND/IO/DIFFIO_L1n/B1
IOA47	6
IOA48	7
IOA49	13
IOA50	18
IOA51	21
IOA52	22
IOA53	23
IOA54	24
IOA55	25
IOA56	26
IOA57	27
IOA58	28
IOA59	29
IOA60	30
	4
	5
	DIRA4
	DIRA5
	OEX
	20

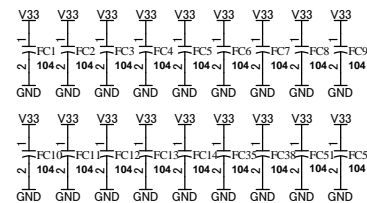
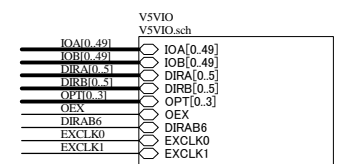
FPGA G	
	7
	15
	VCCIO1/B1
	VCCIO1/B1
	151
	152
	CLK5/DIFFCLK_2n/B6
	CLK4/DIFFCLK_2p/B6
IOB47	161
IOB48	162
IOB49	163
	166
	169
	IO/VREFB6N0/B6
	IO/RDY/B6
	173
	176
	IO/PADD23/B6
	IO/DIFFIO_R1p/PADD20/B6
	177
	IO/DIFFIO_R1p/B6
	183
	CONF_DONE/B6
	185
	MSEL0/B6
	187
	MSEL1/B6
	188
	MSEL2/B6
	189
	IO/DIFFIO_R5n/INIT_DONE/B6
	190
	IO/DIFFIO_R5p/CRC_ERROR/B6
	191
	IO/DIFFIO_R4n/nCE/B6
	192
	IO/DIFFIO_R4p/CLKUSR/B6
	193
	VCCINT/IO/DIFFIO_R3n/nWE/B6
	GND/IO/DIFFIO_R3p/nOE/B6
	VCCINT/IO
	GND/IO
	154
	170
	VCCIO6/B6
	VCCIO6/B6

FPGA F	
CLK-B	149
	150
	CLK7/DIFFCLK_3n/B5
	CLK6/DIFFCLK_3p/B5
IOA0	126
IOA1	127
IOA2	128
IOA3	131
IOA4	132
IOA5	133
IOA6	134
IOA7	135
IOA8	137
IOA9	139
IOA10	142
IOA11	143
IOA12	144
IOA13	145
IOA14	146
	124
	136
	VCCIO5/B5
	VCCIO5/B5
	147
	148
	VCCINT/IO/DIFFIO_R7n/B5
	GND/IO/DIFFIO_R7p/B5

FPGA E	
EXCLK0	1
	R19 47R
	91
	92
	CLK13/DIFFCLK_7p/B4
	CLK12/DIFFCLK_7n/B4
IOB15	93
IOB14	94
IOB13	95
IOB12	96
IOB11	98
IOB10	100
IOB9	103
IOB8	106
IOB7	107
	101
	102
	DIRB0
	DIRB1
	108
	109
	DIRB2
	DIRB3
	110
	111
	IO/DIFFIO_B21n/B4
	IO/RUP2/B4
	112
	IO/RDN2/B4
	113
	IO/DIFFIO_B23n/B4
	IO/DIFFIO_B24p/B4
	114
	IOB1
	117
	IO/PLL4_CLKOUTp/B4
	118
	IO/PLL4_CLKOUTn/B4
	VCCINT/IO/DIFFIO_B25p/B4
	GND/IO/DIFFIO_B25n/B4
	119
	120
	DIRB4
	DIRB5

FPGA D	
	89
	90
	CLK15/DIFFCLK_6p/B3
	CLK14/DIFFCLK_6n/B3
IOB29	63
	IO/DIFFIO_B2p/B3
	VCCINT/IO/DIFFIO_B2n/B3
	GND/IO/DIFFIO_B3p/B3
	64
	DIRAB6
IOB28	68
IOB27	69
IOB26	70
	IO/B3
	IO/PLL1_CLKOUTp/B3
	IO/PLL1_CLKOUTn/B3
	VCCINT/IO/DIFFIO_B4p/B3
	GND/IO/DIFFIO_B4n/B3
	71
	72
	VCCINT/IO
	GND/IO
	73
	IO/B3
	IO/VREFB3N0/B3
	VCCINT/IO
	GND/IO
	76
	IOB25
	IOB24
	IOB23
	IOB22
	IOB21
	IOB20
	IOB19
	IOB18
	IOB17
	IOB16
	IO/DIFFIO_B7n/B3
	IO/DIFFIO_B8n/B3
	IO/DIFFIO_B10n/B3
	IO/DIFFIO_B11p/B3
	66
	67
	VCCIO3/B3
	VCCIO3/B3

FPGA C	
EXCLK1	1
	R20 47R
	33
	34
	CLK2/DIFFCLK_1p/B2
	CLK3/DIFFCLK_1n/B2
IOB44	37
IOB43	38
IOB42	39
IOB41	41
IOB40	43
IOB39	44
IOB38	45
IOB37	46
IOB36	49
IOB35	50
IOB34	51
IOB33	52
IOB32	55
IOB31	56
IOB30	57
	IO/DIFFIO_L8p/B2
	IO/DIFFIO_L8n/B2
	IO/DIFFIO_L9n/B2
	IO/DIFFIO_L10p/B2
	IO/DIFFIO_L10n/B2
	IO/DIFFIO_L11p/B2
	IO/DIFFIO_L11n/B2
	IO/VREFB2N0/B2
	IO/DIFFIO_L13p/B2
	IO/DIFFIO_L13n/B2
	IOB34
	IOB33
	IOB32
	IOB31
	IOB30
	IO/RUP1/B2
	IO/RDN1/B2
	IO/B2
	IO/DIFFIO_L15p/B2
	IO/DIFFIO_L15n/B2
	35
	47
	VCCIO2/B2
	VCCIO2/B2



HUMAN DATA LTD.  
 OSAKA JAPAN  
[www.hdl.co.jp/en/](http://www.hdl.co.jp/en/) (Global)  
[www.hdl.co.jp/](http://www.hdl.co.jp/) (Japan)

Date: 13-Mar-2014 16:46:35

Altera CycloneIII Q240 FPGA board - 5V I/O

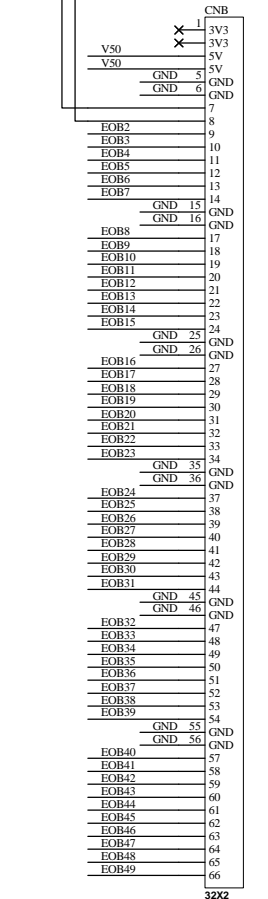
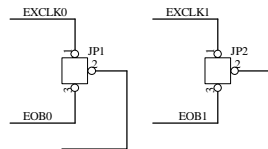
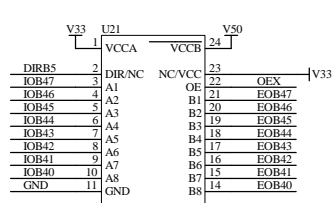
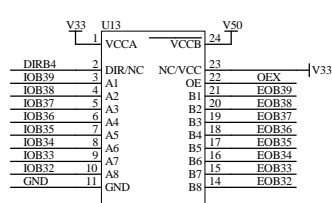
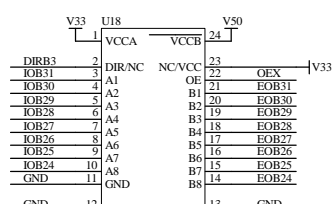
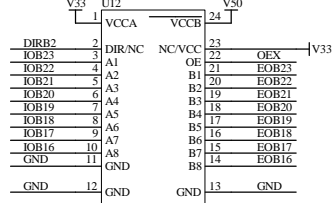
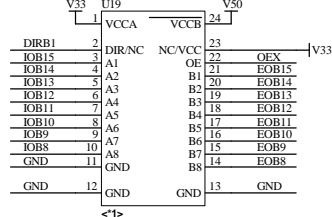
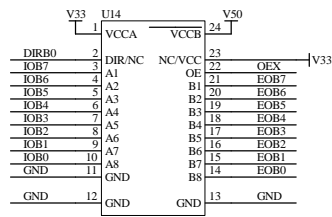
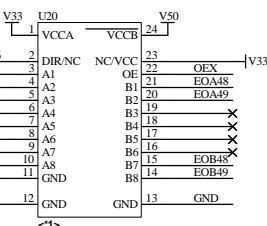
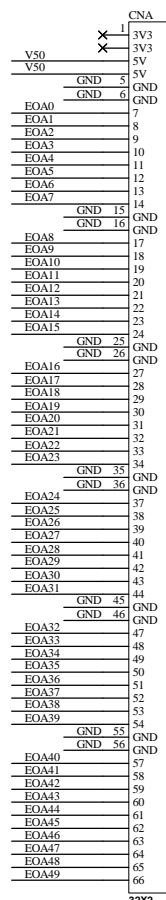
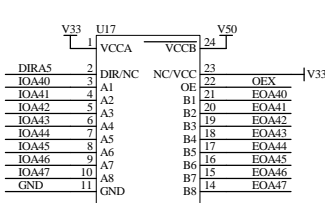
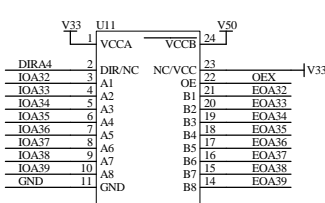
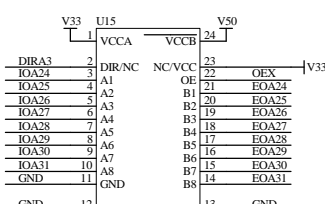
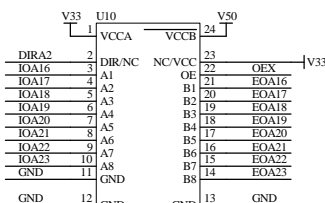
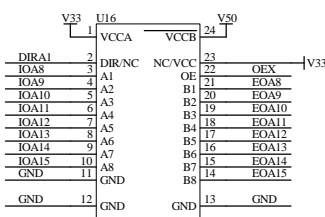
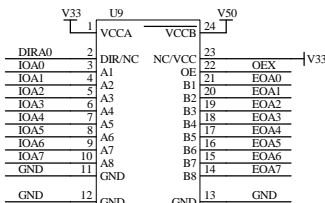
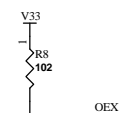
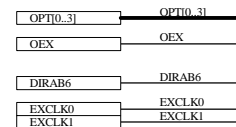
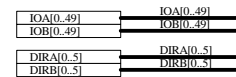
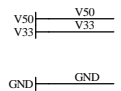
DOC. No. ACM-029Y

File: FPGA-CY3.sch

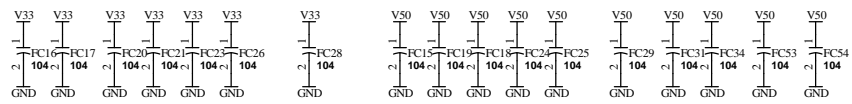
Sheet 2 of 3

A

ACM029Y1-SCH-A.pdf



<*1>	ACM-029	ACM-029Y
U4-16	74LVCC3245	74CB3T3245



HUMAN DATA LTD.  
OSAKA JAPAN  
www.hdl.co.jp/en/(Global)  
www.hdl.co.jp/(Japan)

Altera CycloneIII Q240 FPGA board - 5V I/O

Date:	13-Mar-2014	16:46:36	File:	V5VIO.sch	Sheet 3 of 3
DOC. No.	ACM-029Y				A