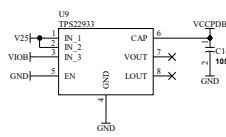
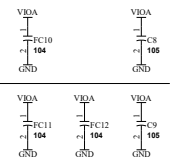


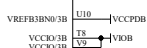
IOA16	F7	FPGA8
IOA17	D14	IO_CLK0, DIFFIO_RX_L19a/2A
	H4	IO_CLK1, DIFFIO_RX_L19b/2A
	J3	IO_CLK2, DIFFIO_RX_L19c/2A
	K2	IO_CLK3, DIFFIO_RX_L19d/2A
	K3	IO_CLK4, DIFFIO_RX_L19e/2A
	E3	IO_CLK5, DIFFIO_RX_L19f/2A
	F2	IO_CLK6, DIFFIO_RX_L19g/2A
IOA15	F1	IO_CLK7, DIFFIO_RX_L19h/2A
IOA14	F1	IO_CLK8, DIFFIO_RX_L19i/2A
	H4	IO_CLK9, DIFFIO_RX_L19j/2A
	G3	IO_CLK10, DIFFIO_RX_L19k/2A
IOA13	K1	IO_CLK11, DIFFIO_RX_L19l/2A
IOA12	L1	IO_CLK12, DIFFIO_RX_L19m/2A
	H2	IO_CLK13, DIFFIO_RX_L19n/2A
	G2	IO_CLK14, DIFFIO_RX_L19o/2A
	J1	IO_CLK15, DIFFIO_RX_L19p/2A
	H1	IO_CLK16, DIFFIO_RX_L19q/2A
	L2	IO_CLK17, DIFFIO_RX_L19r/2A
	M2	IO_CLK18, DIFFIO_RX_L19s/2A
	M1	IO_CLK19, DIFFIO_RX_L19t/2A
	M2	IO_CLK20, DIFFIO_RX_L19u/2A
IOA10	R1	IO_CLK21, DIFFIO_RX_L19v/2A
IOA11	P1	IO_CLK22, DIFFIO_RX_L19w/2A
IOA9	B2	IO_CLK23, DIFFIO_RX_L19x/2A
IOA8	T1	IO_CLK24, DIFFIO_RX_L19y/2A
IOA7	N2	IO_CLK25, DIFFIO_RX_L19z/2A
IOA6	N3	IO_CLK26, DIFFIO_RX_L19aa/2A
IOA4	M4	IO_CLK27, DIFFIO_RX_L19ab/2A
IOA5	M3	IO_CLK28, DIFFIO_RX_L19ac/2A
	T3	IO_CLK29, DIFFIO_RX_L19ad/2A
	R3	IO_CLK30, DIFFIO_RX_L19ae/2A



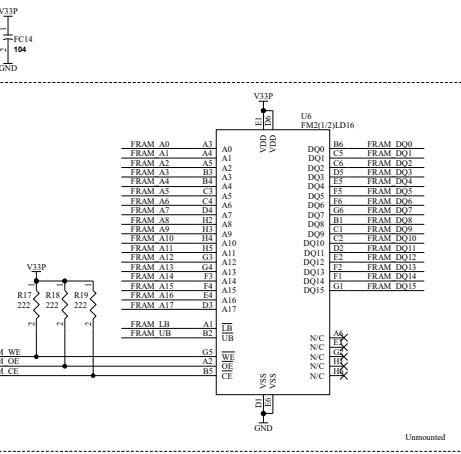
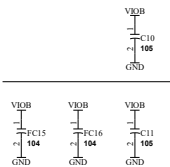
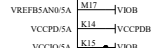
IOA1	E6	FPGA1
IOA0	G6	IO_CLK0, DIFFIO_RX_T25a/8A
	A5	IO_CLK9, DIFFIO_RX_T25b/8A
IOA2	A4	IO_FPLL_TL_CLKOUT1_FPLL_TL_CLKOUT6, DIFFIO_TX_B, DIFFIO_TX_T26, DQ3T/8A
IOA20	A7	IO_CLK0, DIFFIO_RX_T26, DQ3T/8A
IOA21	A7	IO_CLK1, DIFFIO_RX_T26, DQ3T/8A
	B8	IO_CLK2, DIFFIO_RX_T27, DQ3T/8A
IOA24	B7	IO_CLK3, DIFFIO_RX_T27, DQ3T/8A
	F7	IO_CLK4, DIFFIO_RX_T28, DQ3eT/8A
	F8	IO_CLK5, DIFFIO_RX_T29, DQ3T/8A
IOA19	C1	IO_CLK6, DIFFIO_RX_T30, DQ3T/8A
IOA18	C1	IO_CLK7, DIFFIO_RX_T31, DQ3T/8A
	B5	IO_CLK8, DIFFIO_RX_T32, DQ3T/8A
	B4	IO_CLK9, DIFFIO_RX_T33, DQ3T/8A
IOA27	C3	IO_CLK10, DIFFIO_RX_T34, DQ3T/8A
IOA23	B3	IO_CLK11, DIFFIO_RX_T35, DQ3T/8A



IOB1	T11	FPGA9
IOB0	R11	IO_CLK1, DIFFIO_RX_R23a/3B
	V6	IO_CLK2, DIFFIO_RX_R23b/3B
IOB2	V7	IO_CLK3, DIFFIO_RX_R23c/3B
IOB3	V7	IO_CLK4, DIFFIO_RX_R23d/3B
IOB5	U5	IO_CLK5, DIFFIO_RX_R23e/3B
IOB4	U4	IO_CLK6, DIFFIO_RX_R23f/3B
IOB12	P9	IO_CLK7, DIFFIO_RX_R23g/3B
IOB13	P9	IO_CLK8, DIFFIO_RX_R23h/3B
	T4	IO_CLK9, DIFFIO_RX_R23i/3B
	U5	IO_CLK10, DIFFIO_RX_R23j/3B
	V8	IO_FPLL_BL_CLKOUT1_FPLL_BL_CLKOUT6, DIFFIO_TX_B, DIFFIO_TX_R24, DQ2B/3B
IOB6	V8	IO_CLK11, DIFFIO_RX_R24a/3B
IOB7	U8	IO_CLK12, DIFFIO_RX_R24b/3B
IOB8	T9	IO_CLK13, DIFFIO_RX_R24c/3B
IOB9	T9	IO_CLK14, DIFFIO_RX_R24d/3B
IOB11	U9	IO_CLK15, DIFFIO_RX_R24e/3B
IOB10	U9	IO_CLK16, DIFFIO_RX_R24f/3B



IOB15	L13	FPGA9
IOB14	L16	IO_PR_REQUEST, DIFFIO_TX_R1a, DQ1R/5A
	L16	IO_RZQ_1, DIFFIO_TX_R1p, DQ1R/5A
	K13	IO_CRC_ERROR, DIFFIO_RX_R2b/5A
	K13	IO_CSP_CONFIG, DIFFIO_TX_R3a, DQ1R/5A
IOB23	J14	IO_CLK1, DIFFIO_RX_R4a, DQ1R/5A
IOB22	J13	IO_CLK2, DIFFIO_RX_R4b, DQ1R/5A
IOB19	K16	IO_DEV_CLK0, DIFFIO_TX_R5a, DQ1R/5A
IOB18	K17	IO_DEV_OE, DIFFIO_TX_R5p/5A
IOB24	H13	IO_CLK3, DIFFIO_RX_R6a, DQ3aR/5A
	H13	IO_CLK4, DIFFIO_RX_R6b, DQ3aR/5A
IOB16	K18	IO_CLK5, DIFFIO_RX_R7a/5A
IOB17	K18	IO_CLK6, DIFFIO_RX_R7b/5A
IOB21	J15	IO_CLK7, DIFFIO_RX_R8a, DQ1R/5A
IOB20	J16	IO_CLK8, DIFFIO_RX_R8b, DQ1R/5A
	L14	IO_INT_DONE, DIFFIO_RX_R2a/5A
	L13	IO_nCEO, DIFFIO_TX_R3p, DQ1R/5A



GCLK50_7A	F11	FPGA10
	F12	IO_CLK1, DIFFIO_RX_T19a/7A
	F12	IO_CLK2, DIFFIO_RX_T19b/7A
	F12	IO_CLK3, DIFFIO_RX_T19c/7A
	F12	IO_CLK4, DIFFIO_RX_T19d/7A
FRAM DQ13	C16	IO_CLK5, DIFFIO_RX_T19e/7A
FRAM DQ12	D16	IO_CLK6, DIFFIO_RX_T19f/7A
FRAM A14	C13	IO_CLK7, DIFFIO_RX_T19g/7A
	C13	IO_CLK8, DIFFIO_RX_T19h/7A
FRAM DQ15	B15	IO_CLK9, DIFFIO_RX_T19i/7A
FRAM DQ14	B15	IO_CLK10, DIFFIO_RX_T19j/7A
FRAM A1	E11	IO_CLK11, DIFFIO_RX_T19k/7A
FRAM A2	D12	IO_CLK12, DIFFIO_RX_T19l/7A
FRAM A3	D12	IO_CLK13, DIFFIO_RX_T19m/7A
FRAM A4	B4	IO_CLK14, DIFFIO_RX_T19n/7A
FRAM A5	C4	IO_CLK15, DIFFIO_RX_T19o/7A
FRAM A6	C4	IO_CLK16, DIFFIO_RX_T19p/7A
FRAM A7	D4	IO_CLK17, DIFFIO_RX_T19q/7A
FRAM A8	A6	IO_CLK18, DIFFIO_RX_T19r/7A
FRAM A9	B1	IO_CLK19, DIFFIO_RX_T19s/7A
FRAM A10	B1	IO_CLK20, DIFFIO_RX_T19t/7A
FRAM A11	H5	IO_CLK21, DIFFIO_RX_T19u/7A
FRAM A12	G3	IO_CLK22, DIFFIO_RX_T19v/7A
FRAM A13	G4	IO_CLK23, DIFFIO_RX_T19w/7A
FRAM A14	F3	IO_CLK24, DIFFIO_RX_T19x/7A
FRAM A15	F4	IO_CLK25, DIFFIO_RX_T19y/7A
FRAM A16	E4	IO_CLK26, DIFFIO_RX_T19z/7A
FRAM A17	D3	IO_CLK27, DIFFIO_RX_T19aa/7A
FRAM A18	A1	IO_CLK28, DIFFIO_RX_T19ab/7A
FRAM A19	B1	IO_CLK29, DIFFIO_RX_T19ac/7A
FRAM A20	A1	IO_CLK30, DIFFIO_RX_T19ad/7A
FRAM A21	B1	IO_CLK31, DIFFIO_RX_T19ae/7A
FRAM A22	C1	IO_CLK32, DIFFIO_RX_T19af/7A
FRAM A23	C1	IO_CLK33, DIFFIO_RX_T19ag/7A
FRAM A24	D1	IO_CLK34, DIFFIO_RX_T19ah/7A
FRAM A25	D1	IO_CLK35, DIFFIO_RX_T19ai/7A
FRAM A26	E1	IO_CLK36, DIFFIO_RX_T19aj/7A
FRAM A27	F1	IO_CLK37, DIFFIO_RX_T19ak/7A
FRAM A28	F1	IO_CLK38, DIFFIO_RX_T19al/7A
FRAM A29	G1	IO_CLK39, DIFFIO_RX_T19am/7A
FRAM A30	G1	IO_CLK40, DIFFIO_RX_T19an/7A
FRAM A31	H1	IO_CLK41, DIFFIO_RX_T19ao/7A
FRAM A32	H1	IO_CLK42, DIFFIO_RX_T19ap/7A
FRAM A33	I1	IO_CLK43, DIFFIO_RX_T19aq/7A
FRAM A34	I1	IO_CLK44, DIFFIO_RX_T19ar/7A
FRAM A35	J1	IO_CLK45, DIFFIO_RX_T19as/7A
FRAM A36	J1	IO_CLK46, DIFFIO_RX_T19at/7A
FRAM A37	K1	IO_CLK47, DIFFIO_RX_T19au/7A
FRAM A38	K1	IO_CLK48, DIFFIO_RX_T19av/7A
FRAM A39	L1	IO_CLK49, DIFFIO_RX_T19aw/7A
FRAM A40	L1	IO_CLK50, DIFFIO_RX_T19ax/7A
FRAM A41	M1	IO_CLK51, DIFFIO_RX_T19ay/7A
FRAM A42	M1	IO_CLK52, DIFFIO_RX_T19az/7A
FRAM A43	N1	IO_CLK53, DIFFIO_RX_T19ba/7A
FRAM A44	N1	IO_CLK54, DIFFIO_RX_T19bb/7A
FRAM A45	O1	IO_CLK55, DIFFIO_RX_T19bc/7A
FRAM A46	O1	IO_CLK56, DIFFIO_RX_T19bd/7A
FRAM A47	P1	IO_CLK57, DIFFIO_RX_T19be/7A
FRAM A48	P1	IO_CLK58, DIFFIO_RX_T19bf/7A
FRAM A49	Q1	IO_CLK59, DIFFIO_RX_T19bg/7A
FRAM A50	Q1	IO_CLK60, DIFFIO_RX_T19bh/7A
FRAM A51	R1	IO_CLK61, DIFFIO_RX_T19bi/7A
FRAM A52	R1	IO_CLK62, DIFFIO_RX_T19bj/7A
FRAM A53	S1	IO_CLK63, DIFFIO_RX_T19bk/7A
FRAM A54	S1	IO_CLK64, DIFFIO_RX_T19bl/7A
FRAM A55	T1	IO_CLK65, DIFFIO_RX_T19bm/7A
FRAM A56	T1	IO_CLK66, DIFFIO_RX_T19bn/7A
FRAM A57	U1	IO_CLK67, DIFFIO_RX_T19bo/7A
FRAM A58	U1	IO_CLK68, DIFFIO_RX_T19bp/7A
FRAM A59	V1	IO_CLK69, DIFFIO_RX_T19bq/7A
FRAM A60	V1	IO_CLK70, DIFFIO_RX_T19br/7A
FRAM A61	W1	IO_CLK71, DIFFIO_RX_T19bs/7A
FRAM A62	W1	IO_CLK72, DIFFIO_RX_T19bt/7A
FRAM A63	X1	IO_CLK73, DIFFIO_RX_T19bu/7A
FRAM A64	X1	IO_CLK74, DIFFIO_RX_T19bv/7A
FRAM A65	Y1	IO_CLK75, DIFFIO_RX_T19bw/7A
FRAM A66	Y1	IO_CLK76, DIFFIO_RX_T19bx/7A
FRAM A67	Z1	IO_CLK77, DIFFIO_RX_T19by/7A
FRAM A68	Z1	IO_CLK78, DIFFIO_RX_T19bz/7A
FRAM A69	AA1	IO_CLK79, DIFFIO_RX_T19ca/7A
FRAM A70	AA1	IO_CLK80, DIFFIO_RX_T19cb/7A
FRAM A71	AB1	IO_CLK81, DIFFIO_RX_T19cc/7A
FRAM A72	AB1	IO_CLK82, DIFFIO_RX_T19cd/7A
FRAM A73	AC1	IO_CLK83, DIFFIO_RX_T19ce/7A
FRAM A74	AC1	IO_CLK84, DIFFIO_RX_T19cf/7A
FRAM A75	AD1	IO_CLK85, DIFFIO_RX_T19cg/7A
FRAM A76	AD1	IO_CLK86, DIFFIO_RX_T19ch/7A
FRAM A77	AE1	IO_CLK87, DIFFIO_RX_T19ci/7A
FRAM A78	AE1	IO_CLK88, DIFFIO_RX_T19cj/7A
FRAM A79	AF1	IO_CLK89, DIFFIO_RX_T19ck/7A
FRAM A80	AF1	IO_CLK90, DIFFIO_RX_T19cl/7A
FRAM A81	AG1	IO_CLK91, DIFFIO_RX_T19cm/7A
FRAM A82	AG1	IO_CLK92, DIFFIO_RX_T19cn/7A
FRAM A83	AH1	IO_CLK93, DIFFIO_RX_T19co/7A
FRAM A84	AH1	IO_CLK94, DIFFIO_RX_T19cp/7A
FRAM A85	AI1	IO_CLK95, DIFFIO_RX_T19cq/7A
FRAM A86	AI1	IO_CLK96, DIFFIO_RX_T19cr/7A
FRAM A87	AJ1	IO_CLK97, DIFFIO_RX_T19cs/7A
FRAM A88	AJ1	IO_CLK98, DIFFIO_RX_T19ct/7A
FRAM A89	AK1	IO_CLK99, DIFFIO_RX_T19cu/7A
FRAM A90	AK1	IO_CLK100, DIFFIO_RX_T19cv/7A



FRAM DQ13	C16	FPGA11
FRAM DQ12	D16	IO_CLK1, DIFFIO_RX_R19a/8B
	D16	IO_CLK2, DIFFIO_RX_R19b/8B
FRAM A5	U16	IO_CLK3, DIFFIO_RX_R19c/8B
FRAM A6	U16	IO_CLK4, DIFFIO_RX_R19d/8B
FRAM A7	V16	IO_CLK5, DIFFIO_RX_R19e/8B
FRAM A8	V16	IO_CLK6, DIFFIO_RX_R19f/8B
FRAM A9	W16	IO_CLK7, DIFFIO_RX_R19g/8B
FRAM A10	W16	IO_CLK8, DIFFIO_RX_R19h/8B
FRAM A11	X16	IO_CLK9, DIFFIO_RX_R19i/8B
FRAM A12	X16	IO_CLK10, DIFFIO_RX_R19j/8B
FRAM A13	Y16	IO_CLK11, DIFFIO_RX_R19k/8B
FRAM A14	Y16	IO_CLK12, DIFFIO_RX_R19l/8B
FRAM A15	Z16	IO_CLK13, DIFFIO_RX_R19m/8B
FRAM A16	Z16	IO_CLK14, DIFFIO_RX_R19n/8B
FRAM A17	AA16	IO_CLK15, DIFFIO_RX_R19o/8B
FRAM A18	AA16	IO_CLK16, DIFFIO_RX_R19p/8B
FRAM A19	AB16	IO_CLK17, DIFFIO_RX_R19q/8B
FRAM A20	AB16	IO_CLK18, DIFFIO_RX_R19r/8B
FRAM A21	AC16	IO_CLK19, DIFFIO_RX_R19s/8B
FRAM A22	AC16	IO_CLK20, DIFFIO_RX_R19t/8B
FRAM A23	AD16	IO_CLK21, DIFFIO_RX_R19u/8B
FRAM A24	AD16	IO_CLK22, DIFFIO_RX_R19v/8B
FRAM A25	AE16	IO_CLK23, DIFFIO_RX_R19w/8B
FRAM A26	AE16	IO_CLK24, DIFFIO_RX_R19x/8B
FRAM A27	AF16	IO_CLK25, DIFFIO_RX_R19y/8B
FRAM A28	AF16	IO_CLK26, DIFFIO_RX_R19z/8B
FRAM A29	AG16	IO_CLK27, DIFFIO_RX_R19aa/8B
FRAM A30	AG16	IO_CLK28, DIFFIO_RX_R19ab/8B
FRAM A31	AH16	IO_CLK29, DIFFIO_RX_R19ac/8B
FRAM A32	AH16	IO_CLK30, DIFFIO_RX_R19ad/8B
FRAM A33	AI16	IO_CLK31, DIFFIO_RX_R19ae/8B
FRAM A34	AI16	IO_CLK32, DIFFIO_RX_R19af/8B
FRAM A35	AJ16	IO_CLK33, DIFFIO_RX_R19ag/8B
FRAM A36	AJ16	IO_CLK34, DIFFIO_RX_R19ah/8B
FRAM A37	AK16	IO_CLK35, DIFFIO_RX_R19ai/8B
FRAM A38	AK16	IO_CLK36, DIFFIO_RX_R19aj/8B
FRAM A39	AL16	IO_CLK37, DIFFIO_RX_R19ak/8B
FRAM A40	AL16	IO_CLK38, DIFFIO_RX_R19al/8B
FRAM A41	AM16	IO_CLK39, DIFFIO_RX_R19am/8B
FRAM A42	AM16	IO_CLK40, DIFFIO_RX_R19an/8B
FRAM A43	AN16	IO_CLK41, DIFFIO_RX_R19ao/8B
FRAM A44	AN16	IO_CLK42, DIFFIO_RX_R19ap/8B
FRAM A45	AO16	IO_CLK43, DIFFIO_RX_R19aq/8B
FRAM A46	AO16	IO_CLK44, DIFFIO_RX_R19ar/8B
FRAM A47	AP16	IO_CLK45, DIFFIO_RX_R19as/8B
FRAM A48	AP16	IO_CLK46, DIFFIO_RX_R19at/8B
FRAM A49	AQ16	IO_CLK47, DIFFIO_RX_R19au/8B
FRAM A50	AQ16	IO_CLK48, DIFFIO_RX_R19av/8B
FRAM A51	AR16	IO_CLK49, DIFFIO_RX_R19aw/8B
FRAM A52	AR16	IO_CLK50, DIFFIO_RX_R19ax/8B
FRAM A53	AS16	IO_CLK51, DIFFIO_RX_R19ay/8B
FRAM A54	AS16	IO_CLK52, DIFFIO_RX_R19az/8B
FRAM A55	AT16	IO_CLK53, DIFFIO_RX_R19ba/8B
FRAM A56	AT16	IO_CLK54, DIFFIO_RX_R19bb/8B
FRAM A57	AU16	IO_CLK55, DIFFIO_RX_R19bc/8B
FRAM A58	AU16	IO_CLK56, DIFFIO_RX_R19bd/8B
FRAM A59	AV16	IO_CLK57, DIFFIO_RX_R19be/8B
FRAM A60	AV16	IO_CLK58, DIFFIO_RX_R19bf/8B
FRAM A61	AW16	IO_CLK59, DIFFIO_RX_R19bg/8B
FRAM A62	AW16	IO_CLK60, DIFFIO_RX_R19bh/8B
FRAM A		