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Spartan-3AN FPGA Training Board

DOC. No. EDX-005

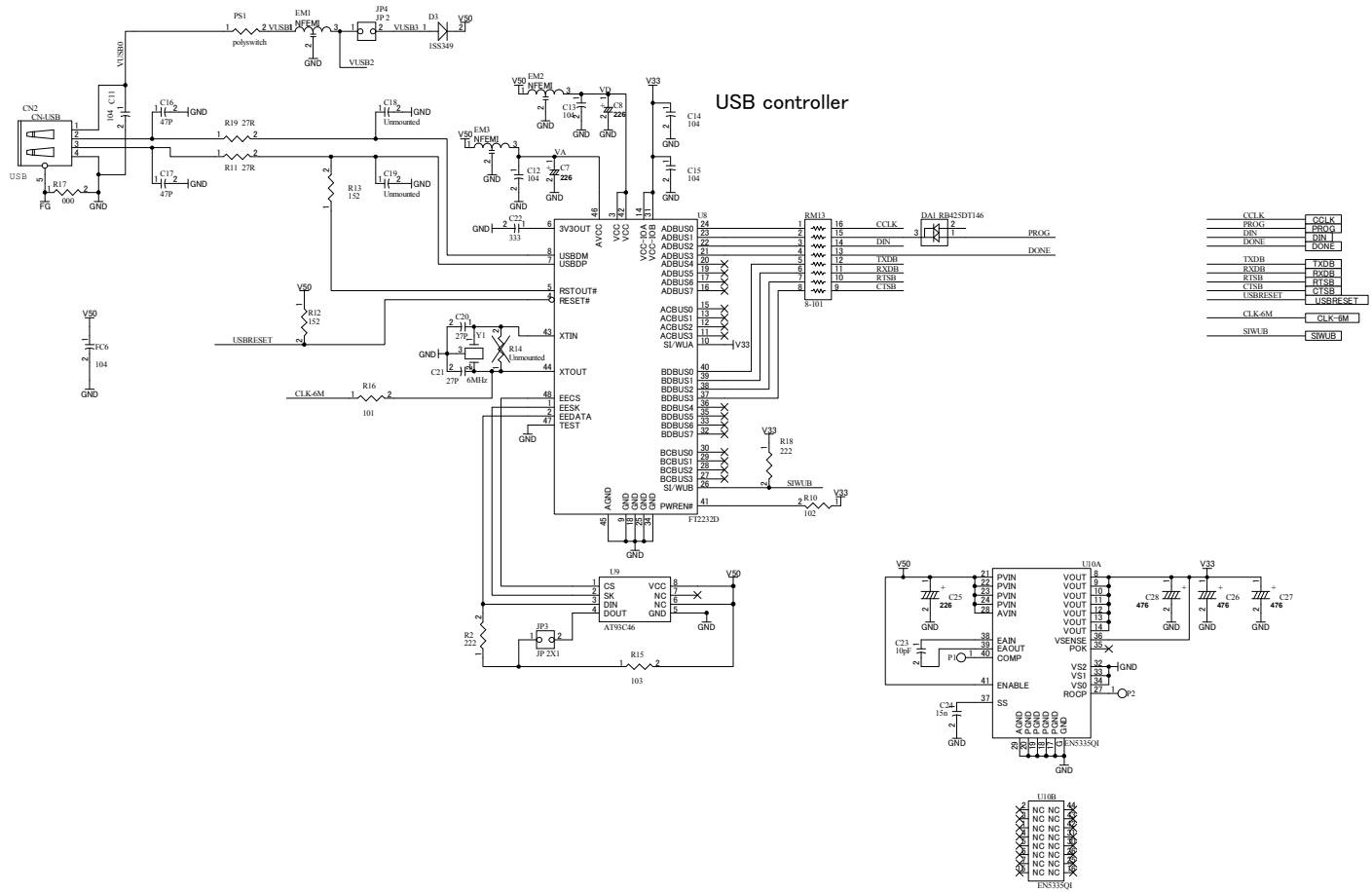
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C1

V50 V50
 V33 V33
 V12 V12
 GND GND



CCLK	CCLK
PROG	PROG
DIN	DIN
DONE	DONE
TXDB	TXDB
RXDB	RXDB
RTSB	RTSB
CTSB	CTSB
USBRISH	USBRISH
USBRESET	USBRESET
CLK-6M	CLK-6M
SW1B	SW1B

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