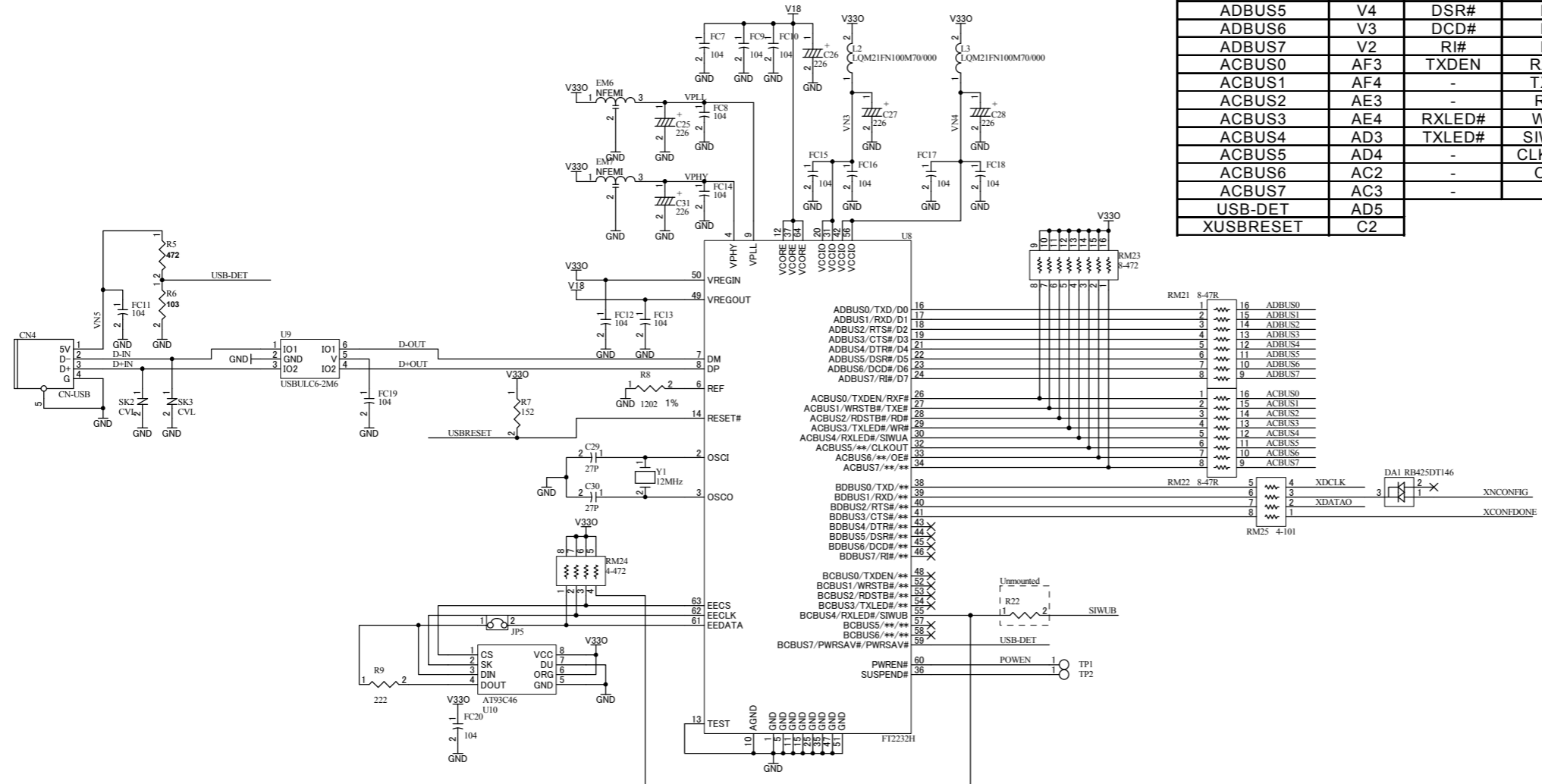


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V18 V18
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 GND GND

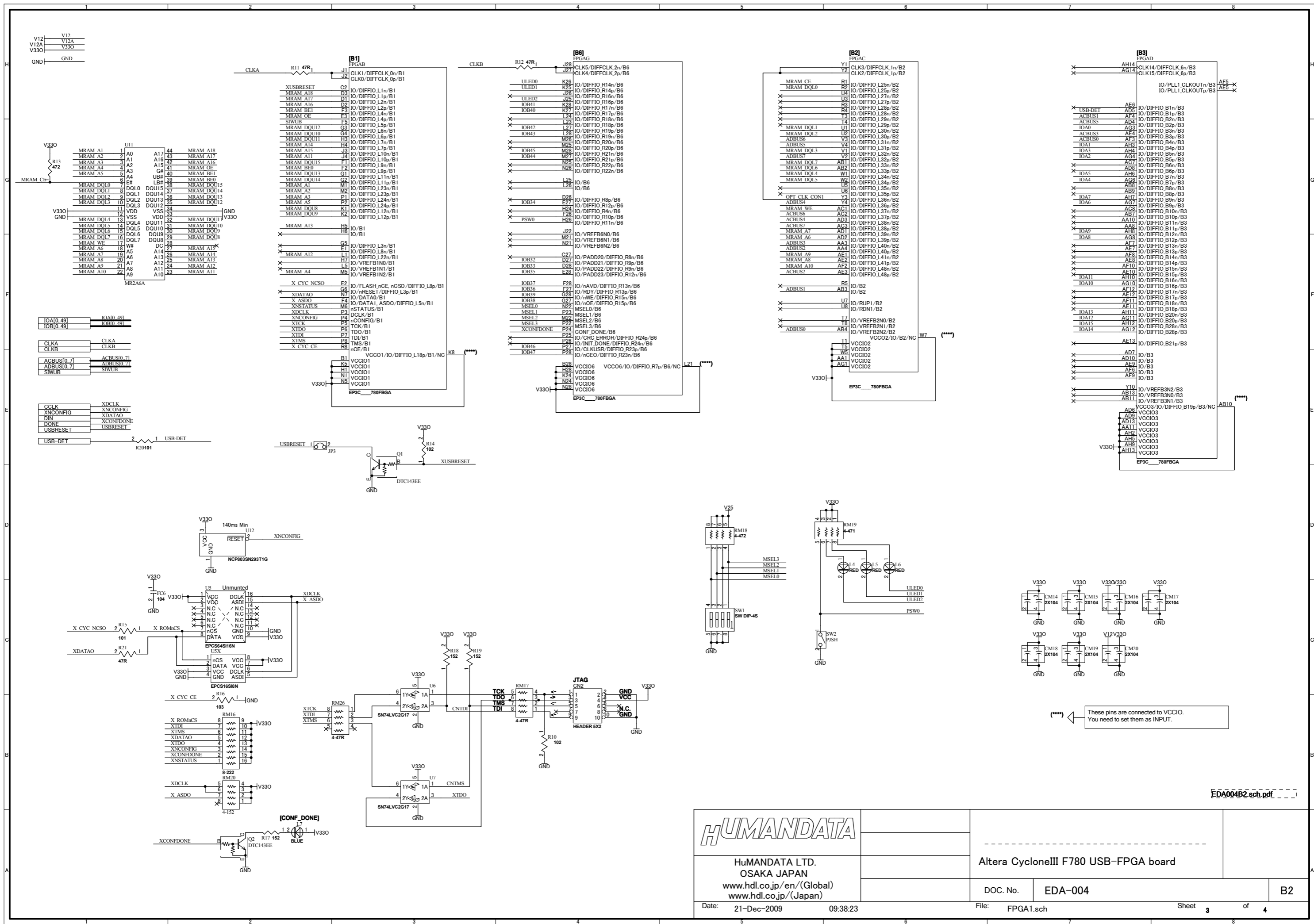
NET LABEL	FPGA Pin	Operation Mode						
		RS232	245 FIFO Sync	245 FIFO	ASync Bit-bang	Sync Bit-bang	MPSSE	CPU Style FIFO
ADBUS0	AB4	TDX	D0	D0	D0	D0	TCK/SK	D0
ADBUS1	AB3	RXD	D1	D1	D1	D1	TDI/DO	D1
ADBUS2	AA4	RTS#	D2	D2	D2	D2	TDO/DI	D2
ADBUS3	AA3	CTS#	D3	D3	D3	D3	TMS/CS	D3
ADBUS4	Y4	DTR#	D4	D4	D4	D4	GPIOL0	D4
ADBUS5	V4	DSR#	D5	D5	D5	D5	GPIOL1	D5
ADBUS6	V3	DCD#	D6	D6	D6	D6	GPIOL2	D6
ADBUS7	V2	RI#	D7	D7	D7	D7	GPIOL3	D7
ACBUS0	AF3	TXDEN	RXF#	RXF#	-	-	GPIOH0	CS#
ACBUS1	AF4	-	TXE#	TXE#	WRSTB#	WRSTB#	GPIOH1	A0
ACBUS2	AE3	-	RD#	RD#	RDSTB#	RDSTB#	GPIOH2	RD#
ACBUS3	AE4	RXLED#	WR#	WR#	-	-	GPIOH3	WR#
ACBUS4	AD3	TXLED#	SIWUA	SIWUA	SIWUA	SIWUA	GPIOH4	SIWUA
ACBUS5	AD4	-	CLKOUT	-	-	-	GPIOH5	-
ACBUS6	AC2	-	OE#	-	-	-	GPIOH6	-
ACBUS7	AC3	-	-	-	-	-	GPIOH7	-
USB-DET	AD5	-	-	-	-	-	-	-
XUSBRESET	C2	-	-	-	-	-	-	-



ADBUS0_71	ADBUS0_71
ACBUS0_71	ACBUS0_71
XDCLK	CLK
XNCONFIG	XNCONFIG
XDATA0	DIN
XCONFDONE	DONE
USBRESET	USBRESET
USB-DET	USB-DET
SIWUB	SIWUB

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HUMAN DATA		-----	
HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/(Global) www.hdl.co.jp/(Japan)		Altera CycloneIII F780 USB-FPGA board	
Date:	21-Dec-2009	DOC. No.	EDA-004
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			B2



		HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/ (Global) www.hdl.co.jp/ (Japan)		Altera CycloneIII F780 USB-FPGA board	
Date:	21-Dec-2009	09:38:23	File:	FPGA1.sch	Sheet 3 of 4

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