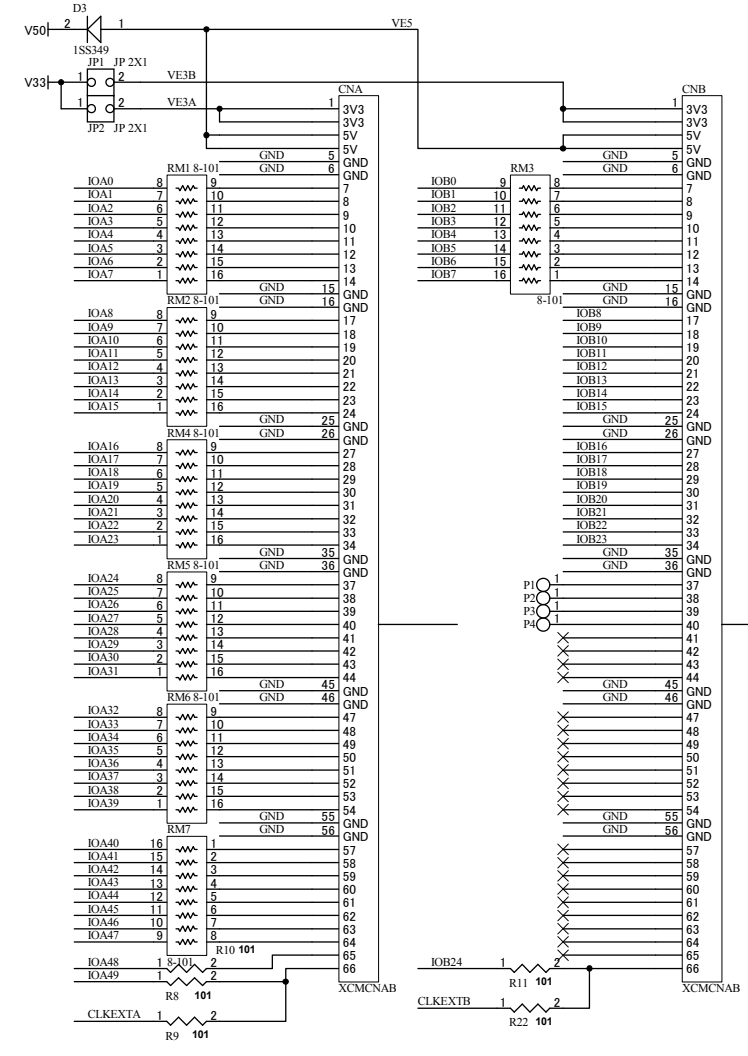
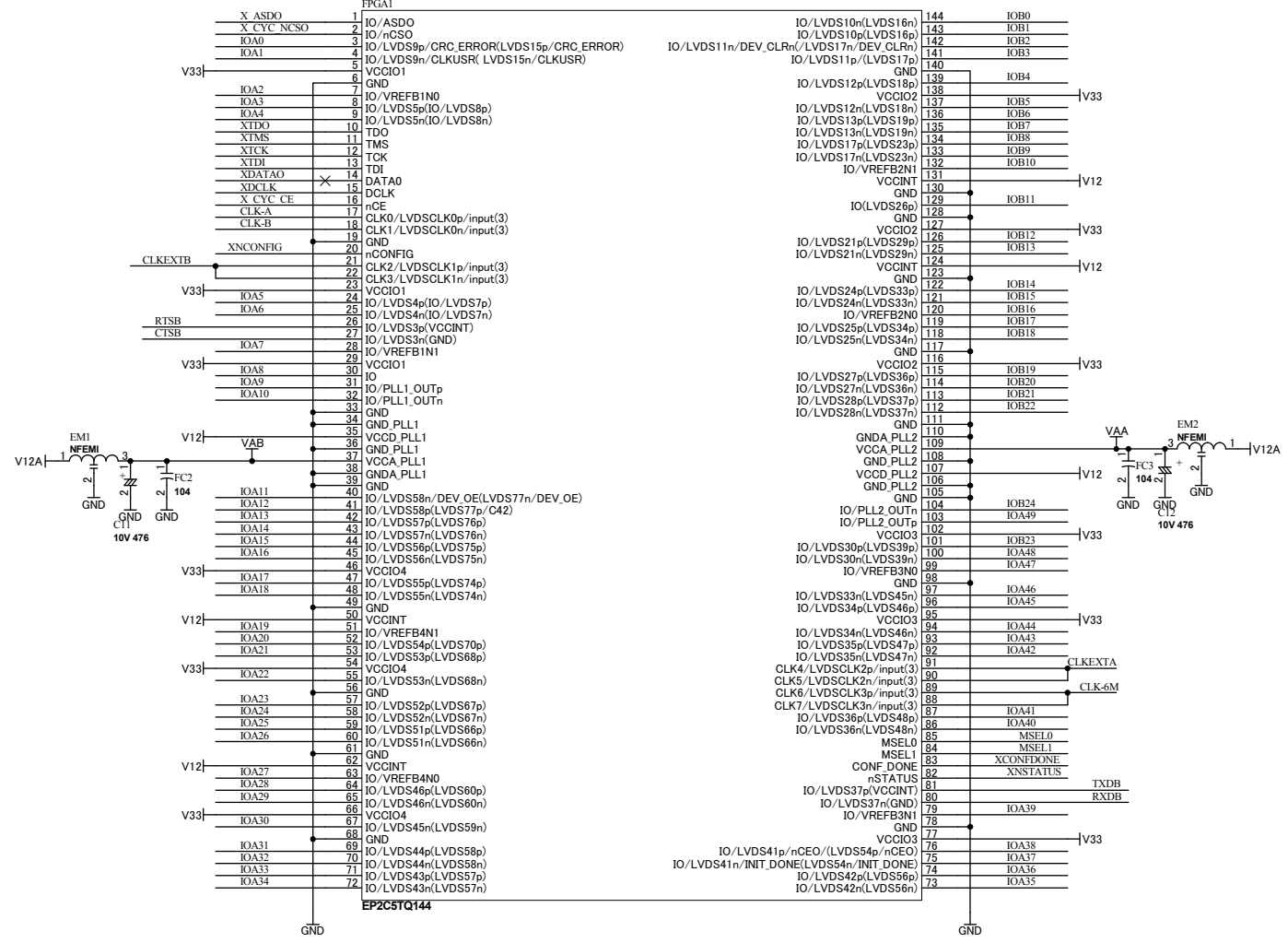
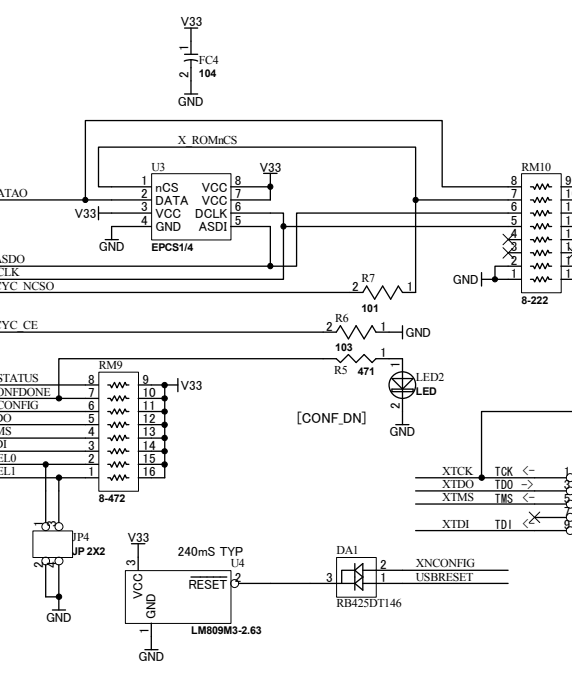
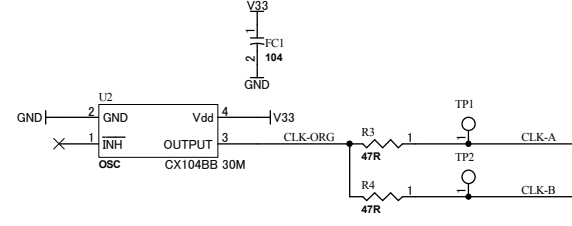
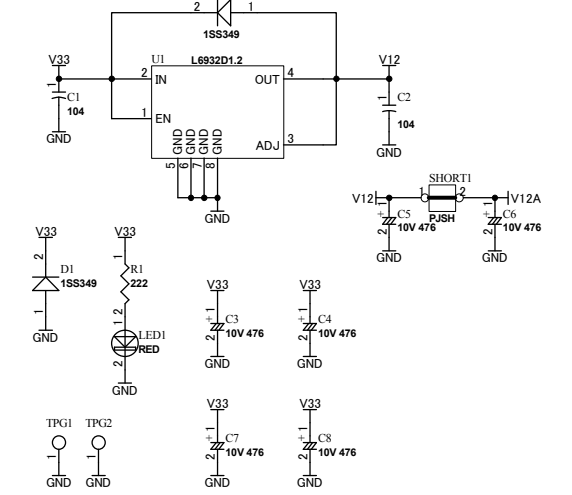


V50 | V50  
V33 | V33  
GND | GND

電源回路



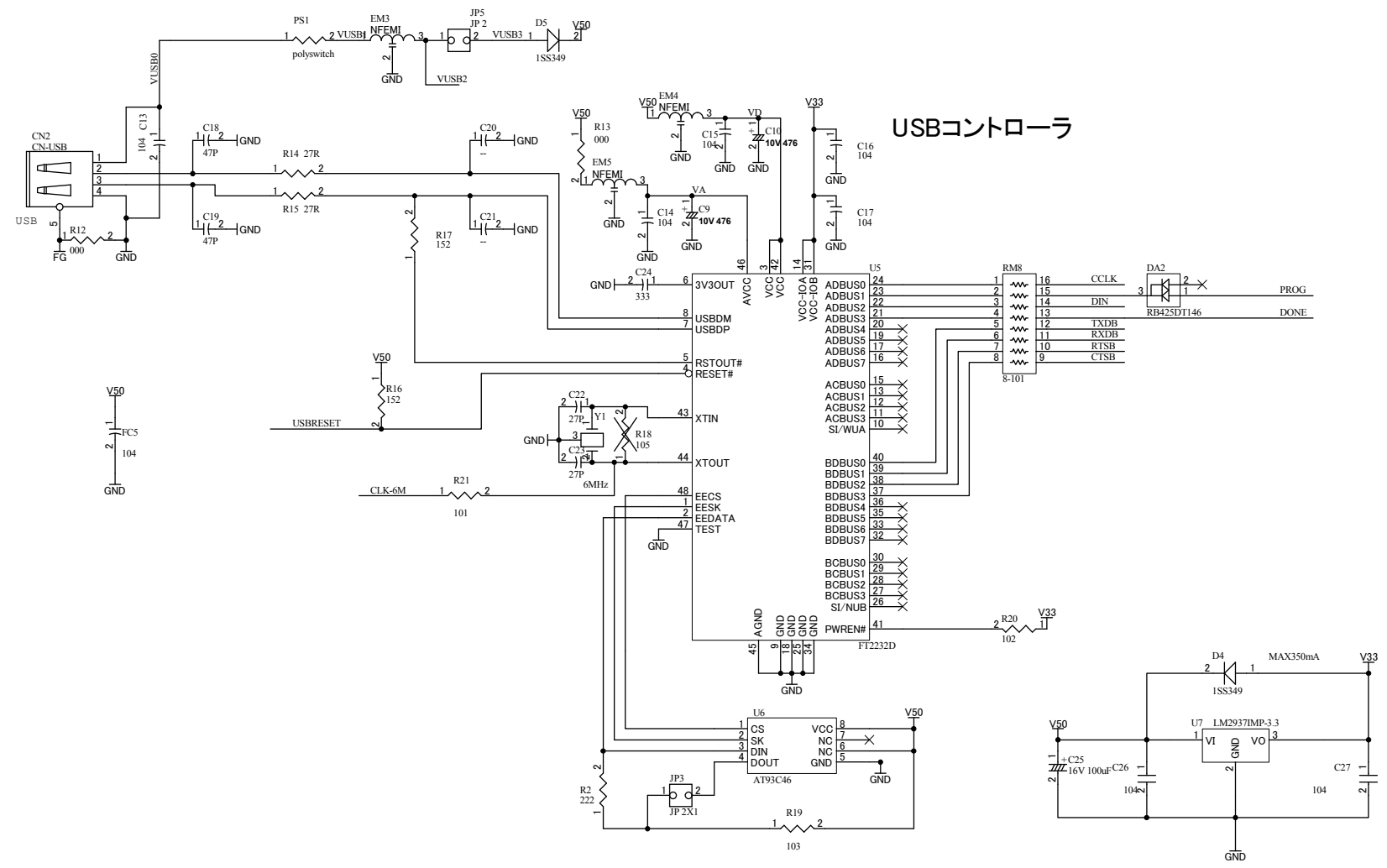
Signal	USBIF sch
XDCLK	CCLK
XNCONFIGN	PROG
XDATAO	DIN
XCONFIGNONE	DONE
TXDB	TXDB
RXDB	RXDB
RTSB	RTSB
USBRESET	USBRESET
CLK-6M	CLK-6M



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V50 | V50  
 V33 | V33  
 GND | GND



USBコントローラ

CCLK	GCLK
PROG	PROG
DIN	DIN
DONE	DONE
TXDB	TXDB
RXDB	RYDB
RTSB	RTSB
CTSB	CTSB
USBRESET	USBRESET
CLK-6M	CLK-6M

<b>HUMAN DATA</b>			
HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp		EDA-003	
Date: 6-Feb-2008	18:03:43	DOC. No. EDA-003	A
File: USBIF.sch		Sheet 2	of 2