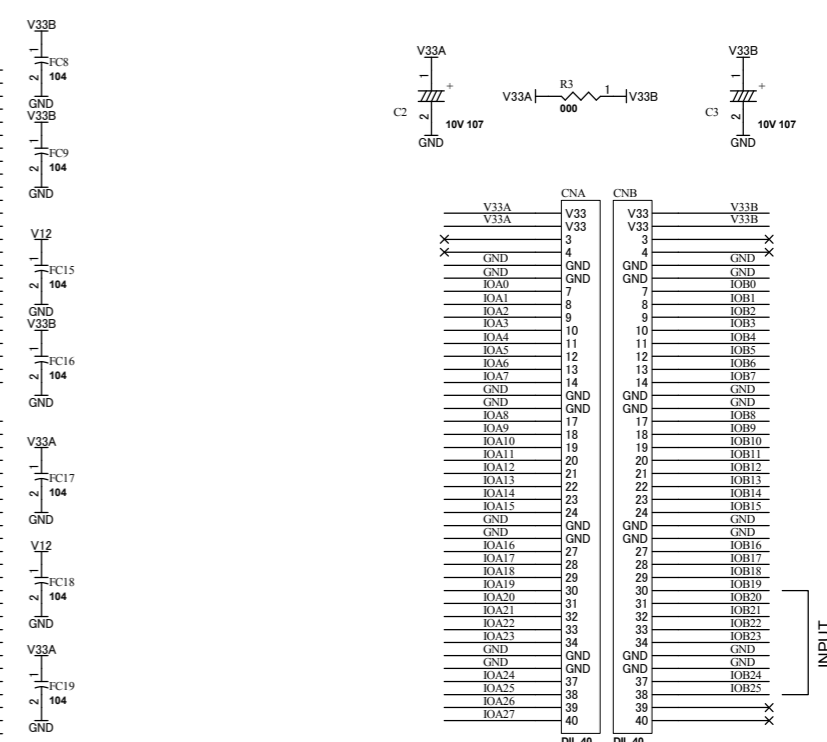


FPGA		PROG.B		X PROG	
X TMS	2	TMS	100	X PROG	104
X TDI	3	IO_L06N_0/PUDC_B	99	PLDC_B	104
IOA1	4	IO_L01P_3	98	IOB0	104
IOA2	5	IO_L01N_3	97	IOB21	104
IOA3	6	IO_L02P_3	96	V33B	104
GND	7	VCCO_0	95	GND	104
GND	8	GND	94	IOB1	104
IOA4	9	IO_L02N_3	93	IOB2	104
IOA5	10	IP_3/VREF_3	92	V33B	104
V33A	11	IO_L08N_0	91	GND	104
IOA6	12	VCCAUX	90	IOB3	104
IOA7	13	IO_L03P_3/LHCLK0	89	IOB4	104
IOA8	14	IO_L04P_3/LHCLK2	88	IOB5	104
GND	15	IO_L04N_0/GCLK8	87	GND	104
IOA9	16	IO_L04P_0/GCLK8	86	IOB6	104
IOA10	17	IO_L05P_3/TRDY2/LHCLK6	85	IOB7	104
V12	18	IO_L03N_0/GCLK7	84	IOB8	104
GND	19	IO_L03P_0/GCLK6	83	IOB9	104
IOA11	20	IO_L02N_0/GCLK5	82	IOB22	104
GND	21	IP_0/VREF_0	81	V12	104
V33A	22	IO_L06P_3	80	GND	104
X M1	23	IO_L06N_3	79	V33B	104
X M2	24	VCCAUX	78	IOB10	104
X M0	25	IO_L01N_0	77	IOB11	104
GND	26	IO_L01P_2/M2	76	X TCK	104
GND	27	IO_L01N_2/M2	75	X TDO	104
V33A	28	VCCO_2	74	GND	104
X CSOB	29	GND	73	IOB12	104
IOA12	30	IO_L02N_2/CSO_B	72	IOB13	104
VS2	31	IO_L03P_2/RDWR_B	71	IOB14	104
VS1	32	IO_L04P_2/VS2	70	IOB15	104
VS0	33	IO_L03N_2/VS1	69	GND	104
IOA13	34	IO_L04N_2/VS0	68	IOB25	104
IOA14	35	IO_L05P_2	67	V33A	104
IOA15	36	IO_L06P_2	66	V12	104
IOA16	37	IO_L05N_2/D7	65	IOB16	104
IOA17	38	IO_L06N_2/D8	64	IOB17	104
IOA18	39	IO_L07P_2/D5	63	GND	104
V12	40	IO_L04N_1/RHCLK6	62	IOB18	104
GND	41	IO_L03N_1/TRDY1/RHCLK3	61	IOB19	104
IOA19	42	IP_2/VREF_2	60	IOA27	104
IOA20	43	IO_L08P_2/GCLK14	59	IOA26	104
GND	44	IO_L08N_2/GCLK15	58	GND	104
CLK0	45	GND	57	IOA25	104
CLK1	46	IO_L09P_2/GCLK0	56	IOA24	104
V33A	47	IO_L09N_2/GCLK1	55	V33A	104
X MOSI	48	IO_L01N_1	54	VCCAUX	104
GND	49	IO_L01P_1	53	X DONE	104
X INH	50	GND	52	X CCLK	104
IOA21	51	IO_L10P_2/INIT_B	51	X DIN	104
IOA22	52	IO_L10N_2/D3	50	GND	104
GND	53	IO_L11P_2/D2	49	GND	104
GND	54	IO_L11N_2/D0/DIN/MISO	48	GND	104



V33A=V33B=3.3V

HUMAN DATA		Xilinx Spartan-3A VQG100 FPGA board	
HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/(Global) www.hdl.co.jp/(Japan)		DOC. No.	XCM-304
Date:	21-Jul-2010	File:	XCM304A.sch
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