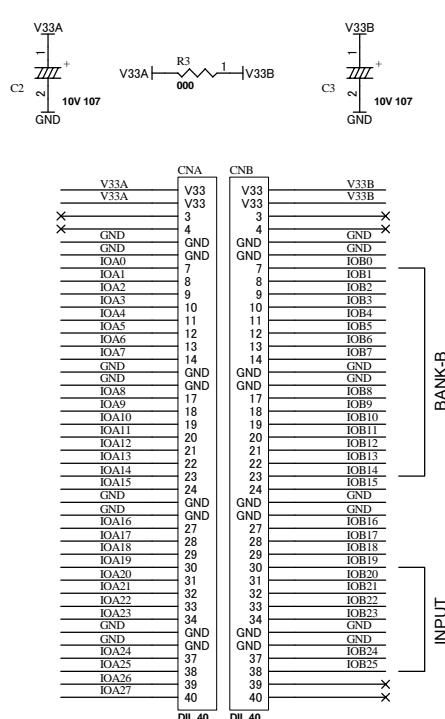


| Spartan-3A | | Spartan-3A | |
|------------|----|------------------------|-----|
| X_TMS | 1 | PROG_B | 100 |
| X_TDI | 2 | IO_L06N_0/PUDC_B | 99 |
| IOA0 | 3 | IO_L06P_0/VREF_0 | 98 |
| IOA1 | 4 | IP_0 | 97 |
| IOA2 | 5 | VCCO_0 | 96 |
| IOA3 | 6 | GND | 95 |
| GND | 7 | IO_L02N_3 | 94 |
| IOB30 | 8 | IP_3/VREF_3 | 93 |
| GND | 9 | IO_L05N_0 | 92 |
| IOA4 | 10 | IO_L05P_0 | 91 |
| IOA5 | 11 | VCCAUX | 90 |
| V33A | 12 | GND | 89 |
| IOA6 | 13 | IO_0/GCLK11 | 88 |
| IOA7 | 14 | IO_L04N_0/GCLK9 | 87 |
| GND | 15 | IO_L04P_0/GCLK8 | 86 |
| IOA8 | 16 | GND | 85 |
| IOA9 | 17 | IO_L03N_0/GCLK7 | 84 |
| V12 | 18 | IO_L03P_0/GCLK6 | 83 |
| GND | 19 | IO_L02N_0/GCLK5 | 82 |
| IOA10 | 20 | IO_L02P_0/GCLK4 | 81 |
| IOA11 | 21 | IP_0/VREF_0 | 80 |
| GND | 22 | VCCINT | 79 |
| IOB23 | 23 | IP_3 | 78 |
| V33A | 24 | GND | 77 |
| X_M1 | 25 | VCCAUX | 76 |
| X_M2 | 26 | VCCO_0 | 75 |
| X_M0 | 27 | IO_L01N_0 | 74 |
| X_M0 | 28 | IO_L01P_0/VREF_0 | 73 |
| GND | 29 | TCK | 72 |
| V33A | 30 | GND | 71 |
| X_CS0B | 31 | IO_L06N_1 | 70 |
| IOA12 | 32 | IO_L06P_1 | 69 |
| VS2 | 33 | IO_L03N_1 | 68 |
| VS1 | 34 | IO_L03P_1 | 67 |
| VS0 | 35 | GND | 66 |
| IOA13 | 36 | IP_1/VREF_1 | 65 |
| IOA14 | 37 | VCCO_1 | 64 |
| IOA15 | 38 | VCCINT | 63 |
| IOA16 | 39 | IO_L04N_1/RHCLK7 | 62 |
| IOA17 | 40 | VCCINT | 61 |
| IOA18 | 41 | IO_L04P_1/IRDY1/RHCLK6 | 60 |
| V12 | 42 | GND | 59 |
| GND | 43 | IO_L03N_1/TRDY1/RHCLK3 | 58 |
| IOB24 | 44 | IO_L03P_1/RHCLK2 | 57 |
| IOA19 | 45 | IO_L02N_1/RHCLK1 | 56 |
| IOA20 | 46 | IO_L02P_1/RHCLK0 | 55 |
| VS2 | 47 | GND | 54 |
| VS1 | 48 | IO_L01N_1 | 53 |
| VS0 | 49 | IO_L01P_1 | 52 |
| IOA21 | 50 | VCCAUX | 51 |
| IOA22 | 51 | DONE | 50 |
| | | IO_L12N_2/CCLK | 49 |
| | | IO_L12P_2/D1 | 48 |
| | | IO_L11N_2/D0/DIN/MISO | 47 |
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| HUMAN DATA | | Xilinx Spartan-3A VQG100 FPGA board | |
| HuMANDATA LTD. OSAKA JAPAN www.hdl.co.jp/en/(Global) www.hdl.co.jp/(Japan) | | DOC. No. XCM-304 | A1 |
| Date: 10-Dec-2009 11:46:06 | File: XCM304A.sch | Sheet 1 of 1 | |