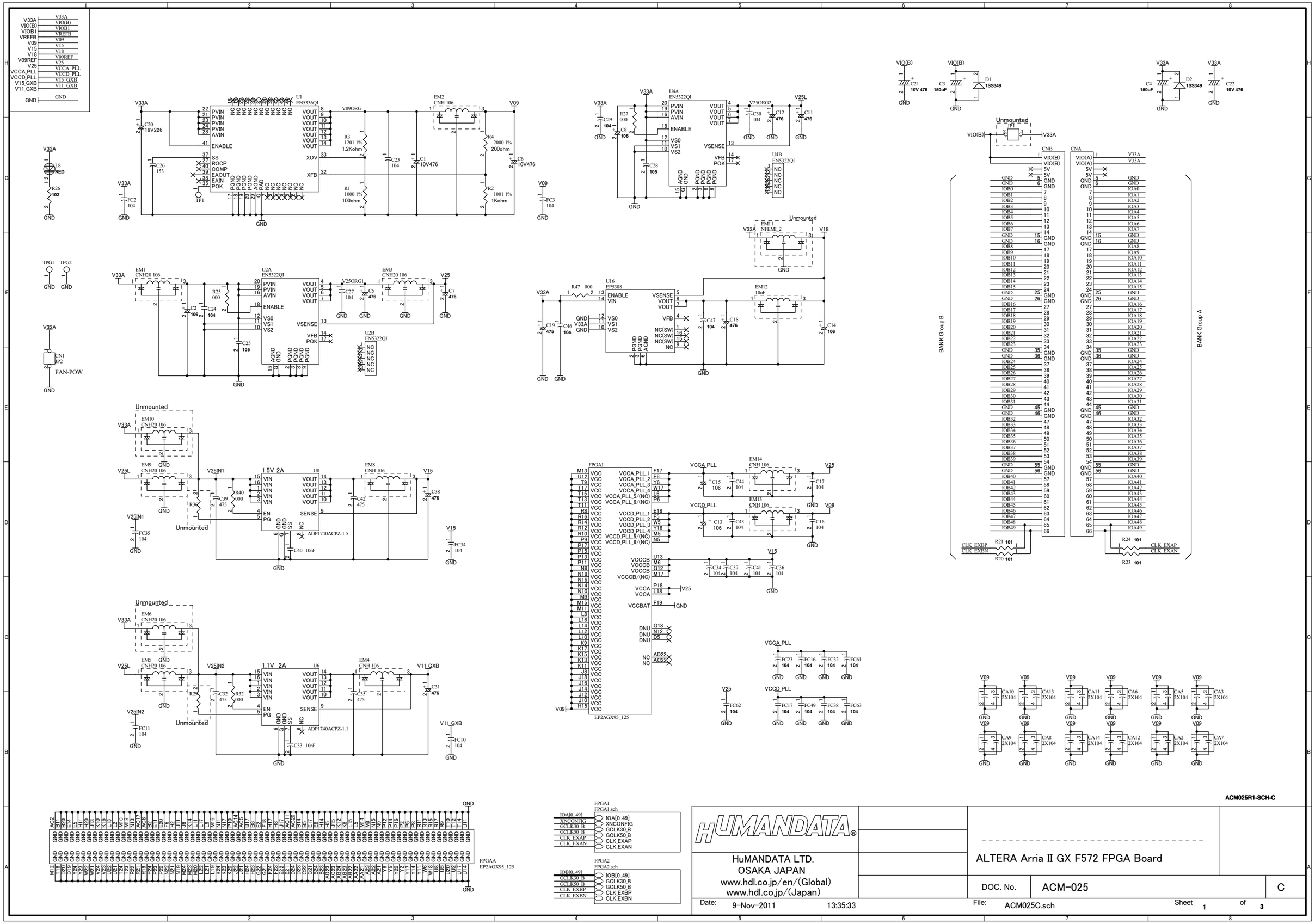
V33A
VIO(B)
VIOB1
VREFB
V09
V15
V18
V09REF
V25
VCCA_PLL
VCCD_PLL
V15_GXB
V11_GXB
GND
U1
EN5336QI
U2A
EN5322QI
U2B
EN5322QI
U4A
EN5322QI
U4B
EN5322QI
U16
EP5388
U8
ADP1740ACPZ-1.5
U6
ADP1740ACPZ-1.1
FPGAJ
EP2AGX95_125
FPGAA
EP2AGX95_125
FAN-POW
Unmounted
BANK Group B
BANK Group A
CNB
CNA
CLK_EXBP
CLK_EXBN
CLK_EXAP
CLK_EXAN
FPGA1
FPGA1.sch
FPGA2
FPGA2.sch
HuMANDATA LTD.
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ALTERA Arria II GX F572 FPGA Board
DOC. No.
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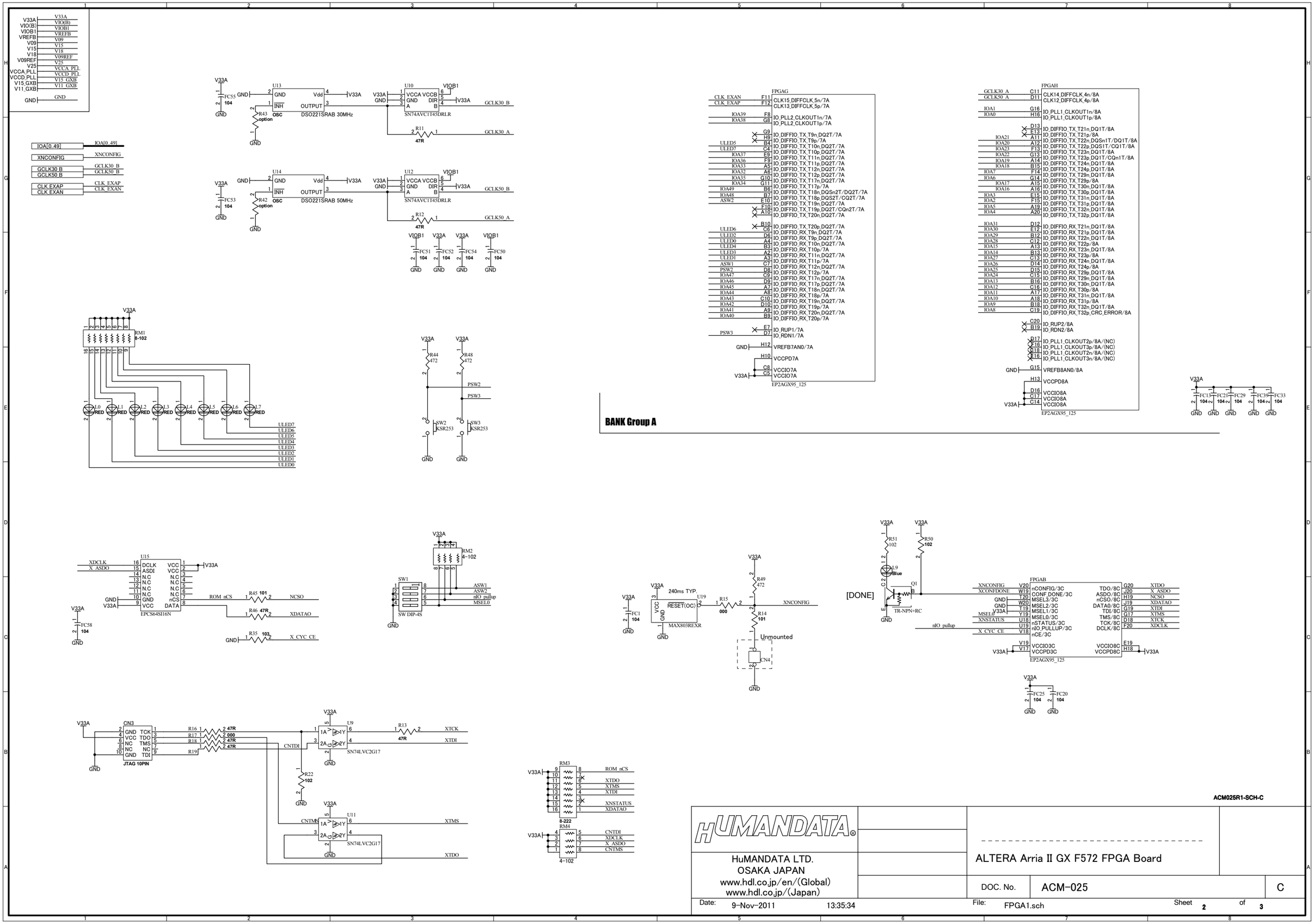
Sheet 1 of 3
ACM025R1-SCH-C


V33A
VIO(B)
VIOB1
VREFB
V09
V15
V18
V09REF
V25
VCCA_PLL
VCCD_PLL
V15_GXB
V11_GXB
GND
IOA[0..49]
XNCONFIG
GCLK30_B
GCLK50_B
CLK_EXAP
CLK_EXAN
U13
U14
U10
U12
OSC
option
DSO221SRAB 30MHz
DSO221SRAB 50MHz
SN74AVC1T45DRLR
GCLK30_B
GCLK30_A
GCLK50_B
GCLK50_A
FPGAG
EP2AGX95_125
FPGAH
BANK Group A
RM1
8-102
L0
L1
L2
L3
L4
L5
L6
L7
RED
ULED7
ULED6
ULED5
ULED4
ULED3
ULED2
ULED1
ULED0
R44
472
R48
472
PSW2
PSW3
SW2
SW3
KSR253
U15
EPCS64SI16N
XDCLK
X_ASDO
ROM_nCS
NCSO
XDATAO
X_CYC_CE
RM2
4-102
SW1
SW DIP-4S
ASW1
ASW2
nIO_pullup
MSEL0
240ms TYP.
MAX803REXR
XNCONFIG
Unmounted
CN4
[DONE]
R51
102
R50
102
L9
Blue
Q1
TR-NPN+RC
FPGAB
EP2AGX95_125
CN3
JTAG 10PIN
XTCK
XTDI
XTMS
XTDO
SN74LVC2G17
U9
U11
RM3
8-222
RM4
4-102
XNSTATUS
XDATAO
CNTDI
X_ASDO
CNTMS
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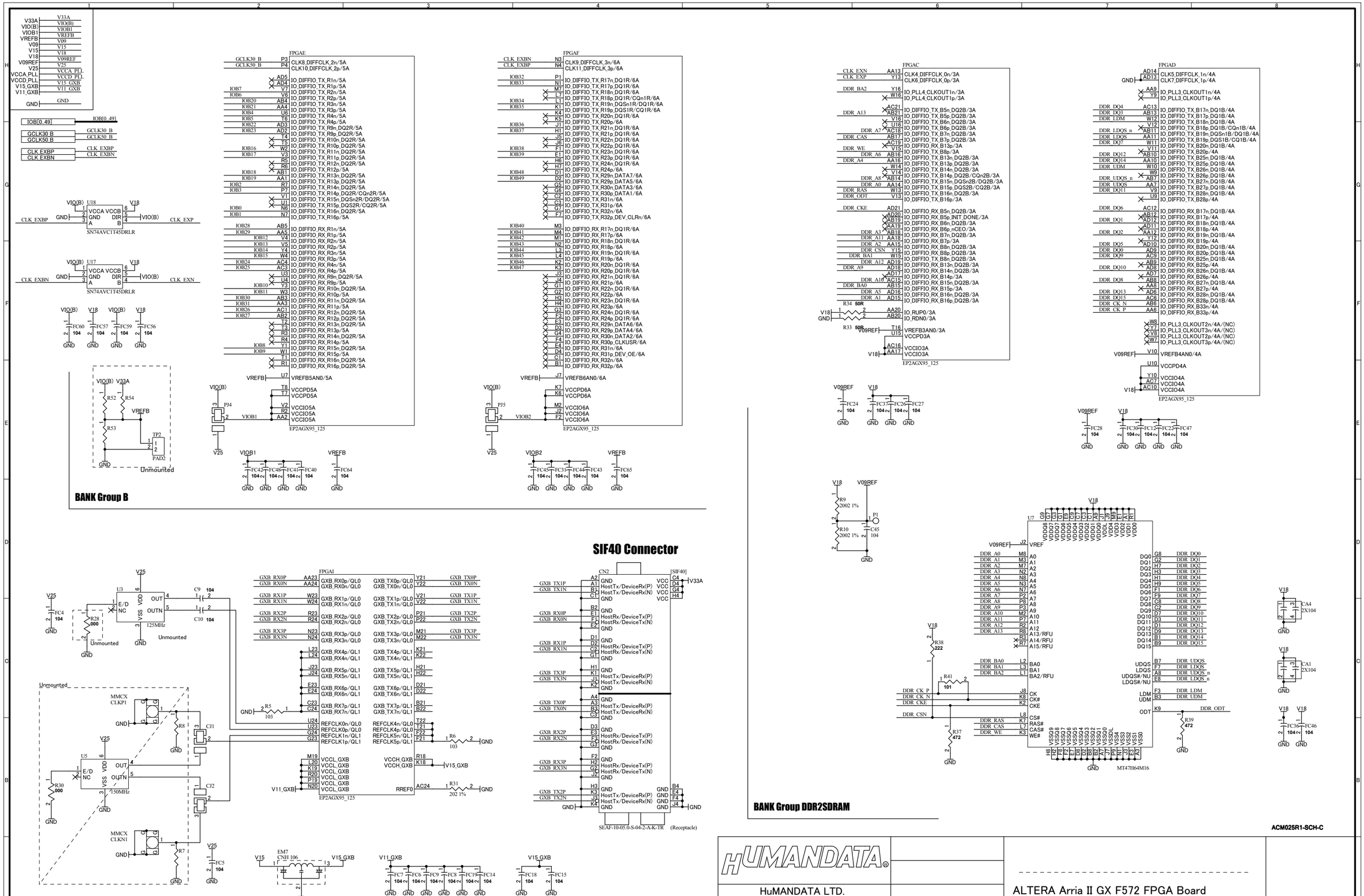

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