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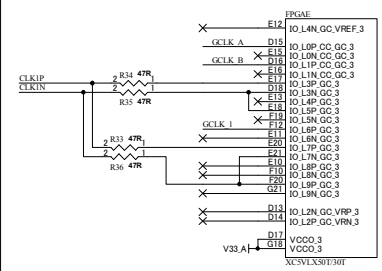
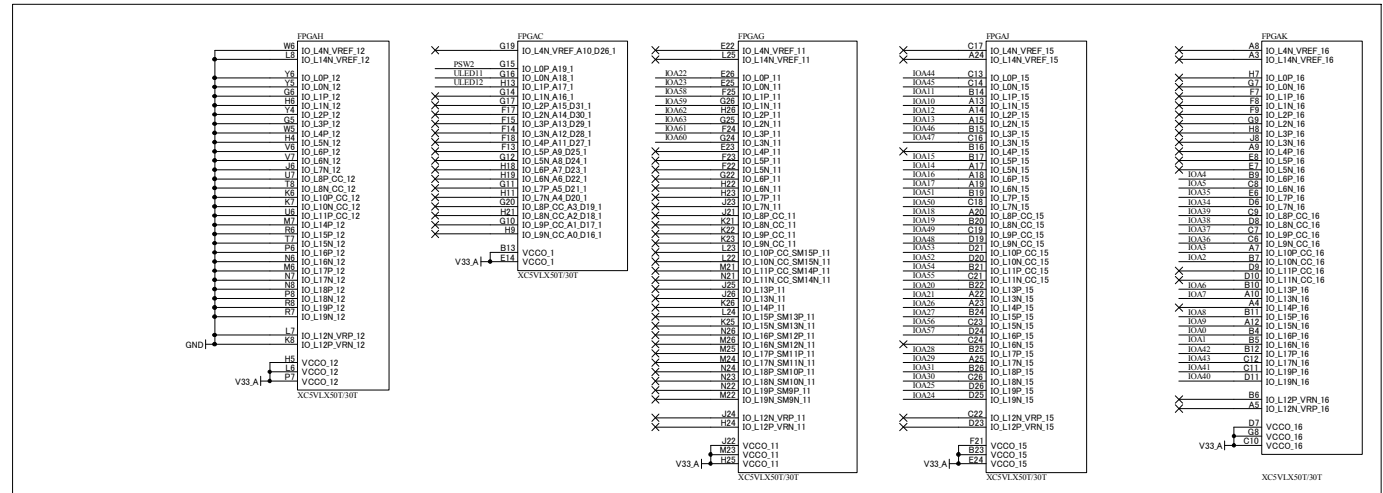
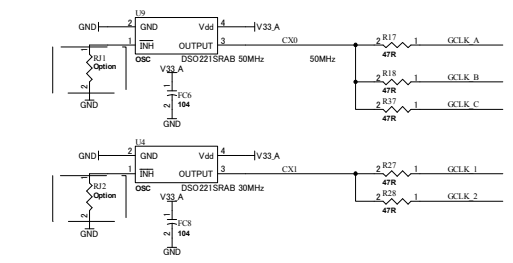
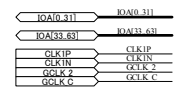
XCM107R2-SCH-E2.pdf

Virtex5 LXT FFG665 FPGA Board

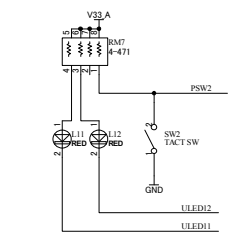
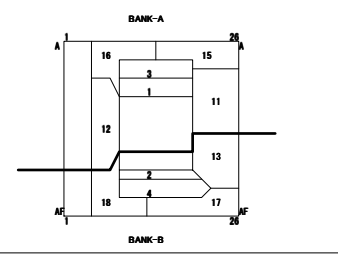
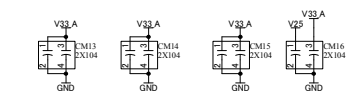
DOC. No.	XCM-107	E2
Date:	11-May-2012 09:36:36	

File: XCM107E.sch Sheet 1 of 4

V10 V10  
 V12 V12  
 V25 V25  
 V33 A V33 A  
 V33 B V33 B  
 V33 C V33 C  
 V33 D V33 D  
 GND GND



BANK-A (3.3V)

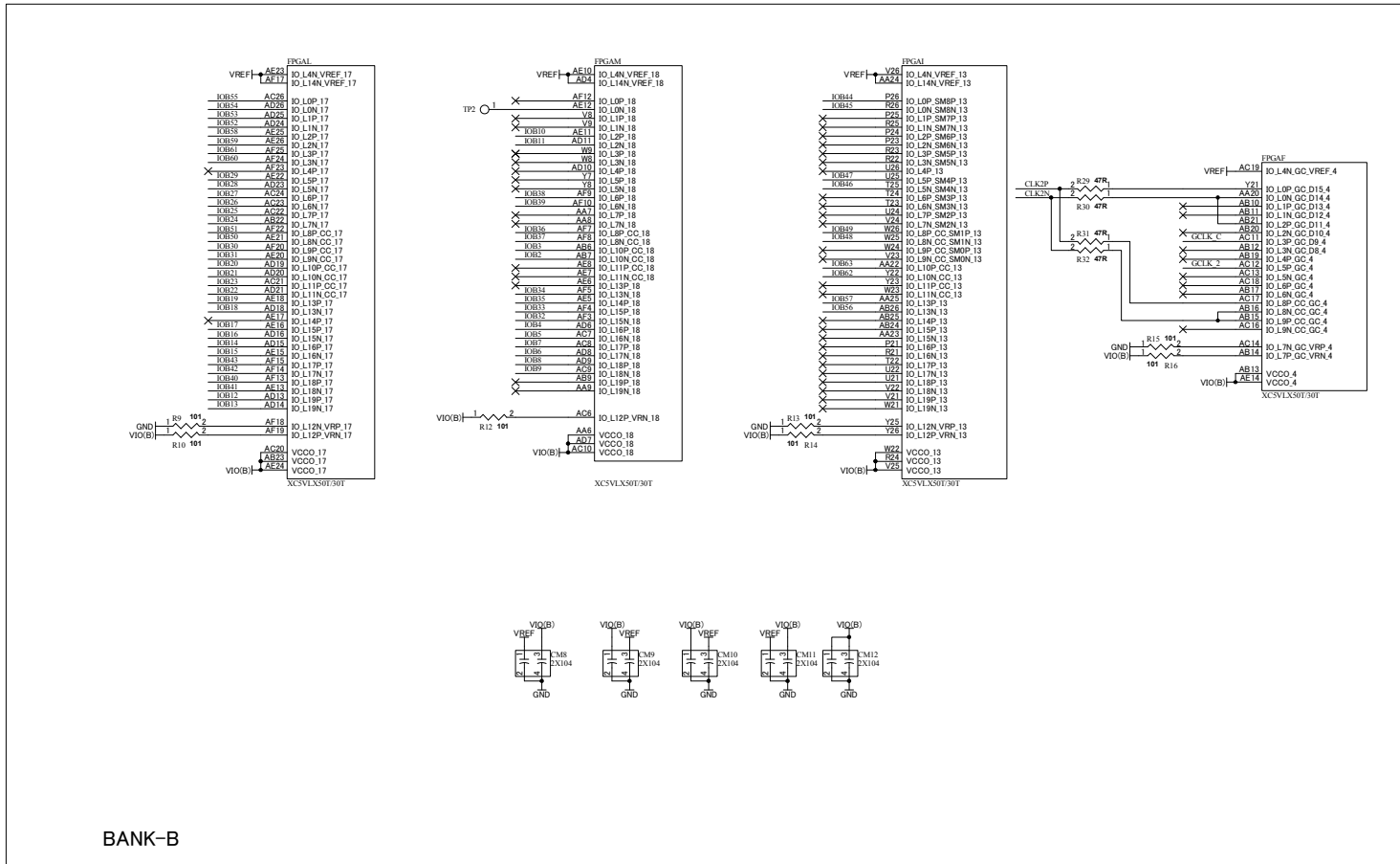


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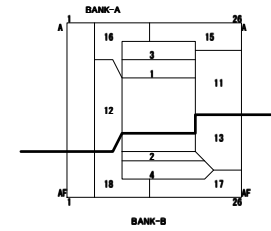
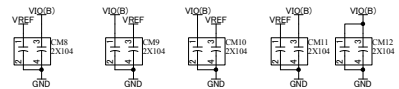
		Virtex5 LXT FFG665 FPGA Board	
		HuMANDATA LTD. OSAKA JAPAN <a href="http://www.hdl.co.jp/en/">www.hdl.co.jp/en/</a> (Global) <a href="http://www.hdl.co.jp/">www.hdl.co.jp/</a> (Japan)	DOC. No. XCM-107 Date: 11-May-2012 09:36:37

V10 V10  
 V12 V12  
 V25 V25  
 V33.A V33.A  
 VIO(B) VIO(B)  
 GND GND  
 VREF VREF

IOB17.63 IOB17.63  
 CLK3P CLK3P  
 CLK2N CLK2N  
 GCLK2.2 GCLK2.2  
 GCLK.C GCLK.C



BANK-B



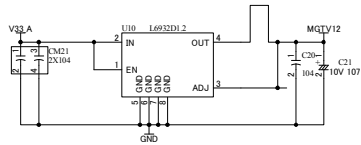
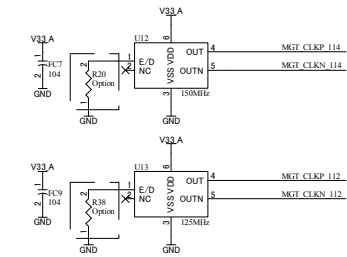
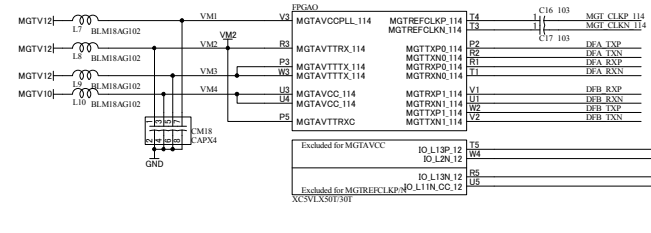
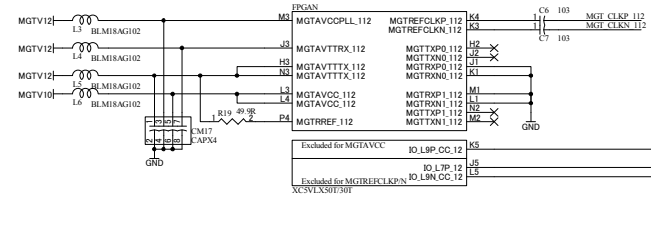
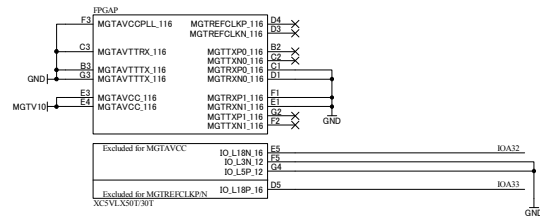
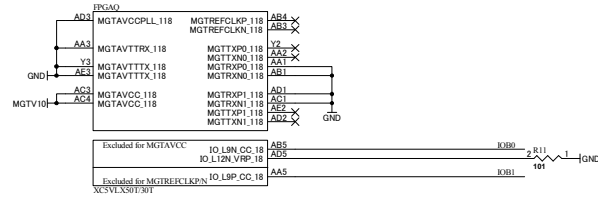
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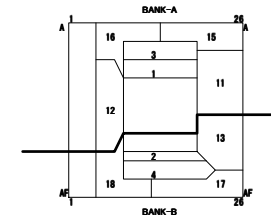
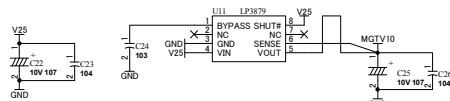
Virtex5 LXT FFG665 FPGA Board  
 DOC. No. XCM-107  
 File: X5IOB.sch  
 Sheet 3 of 4  
 E2

V10 | V10  
 V12 | V12  
 V25 | V25  
 V33A | V33A  
 V10(B) | V10(B)  
 GND | GND  
 VREF | VREF

DEARXP | DEARXP  
 DEARZN | DEARZN  
 DEARTXN | DEARTXN  
 DEARXN | DEARXN  
 DEBRXP | DEBRXP  
 DEBRZN | DEBRZN  
 DEBRTXN | DEBRTXN  
 DEBRXN | DEBRXN  
 IOA52 | IOA52  
 IOA53 | IOA53



P9 | MGTV10  
 P10 | MGTV12



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							Date:	11-May-2012