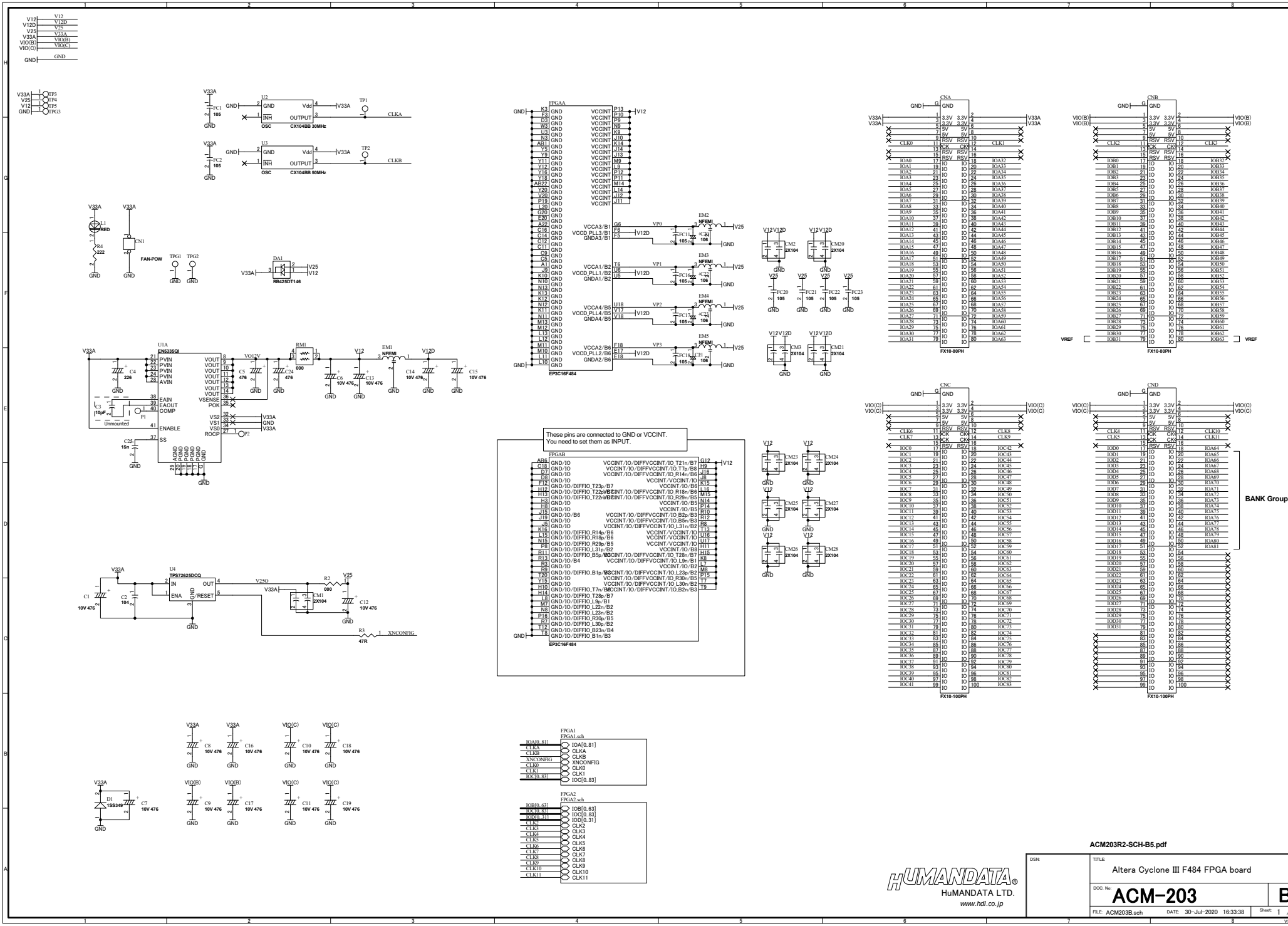

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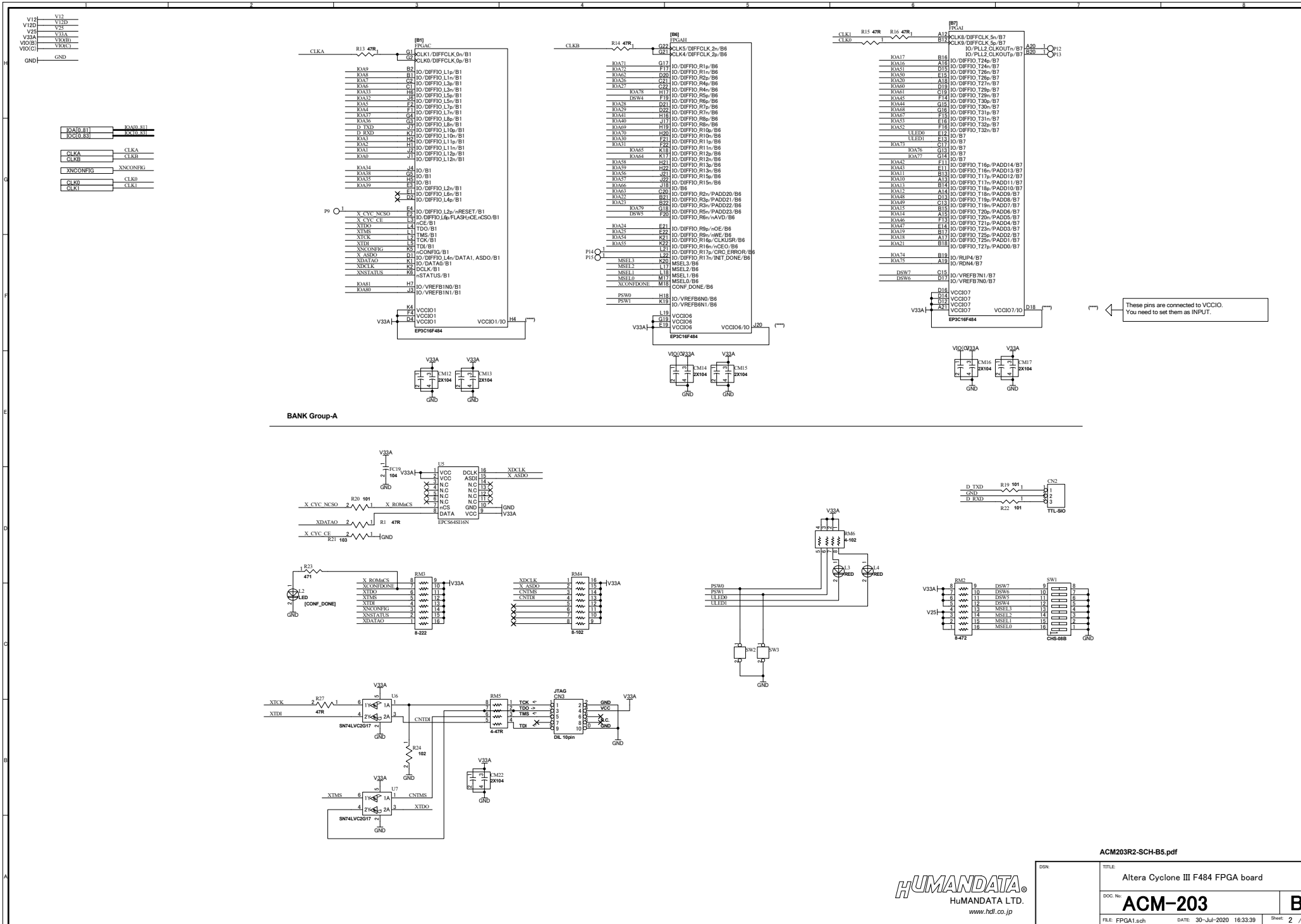
BANK Group-A
These pins are connected to VCCIO.
You need to set them as INPUT.
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Altera Cyclone III F484 FPGA board
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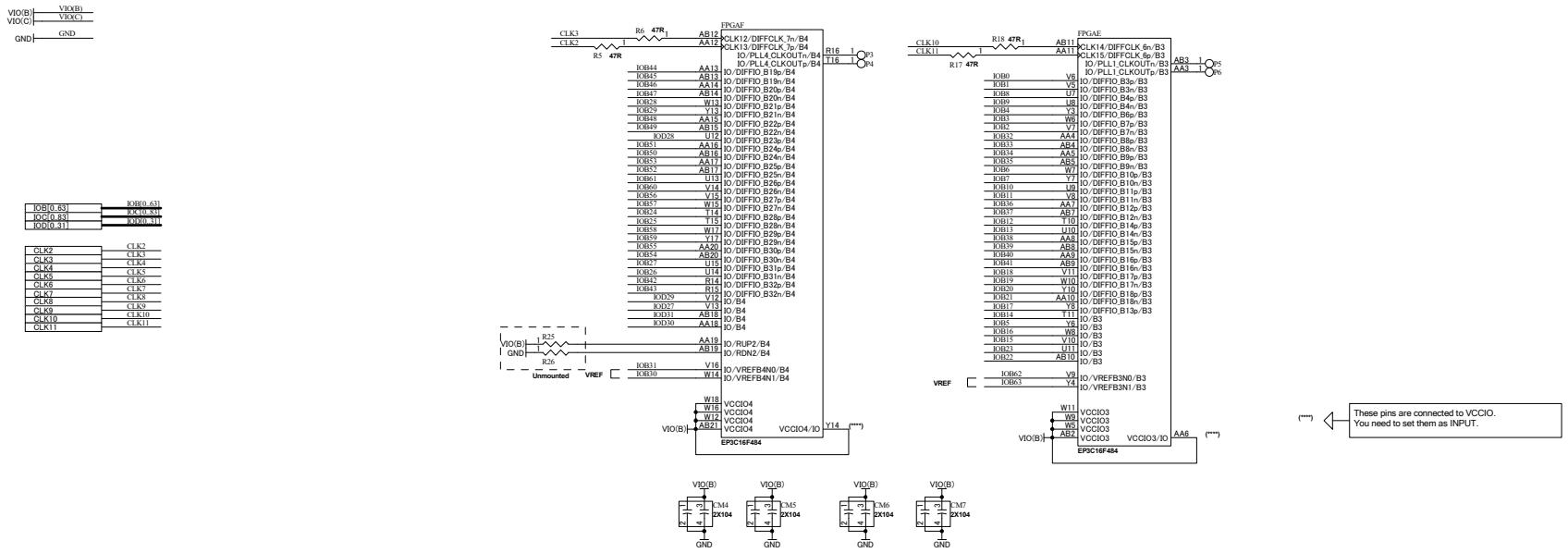
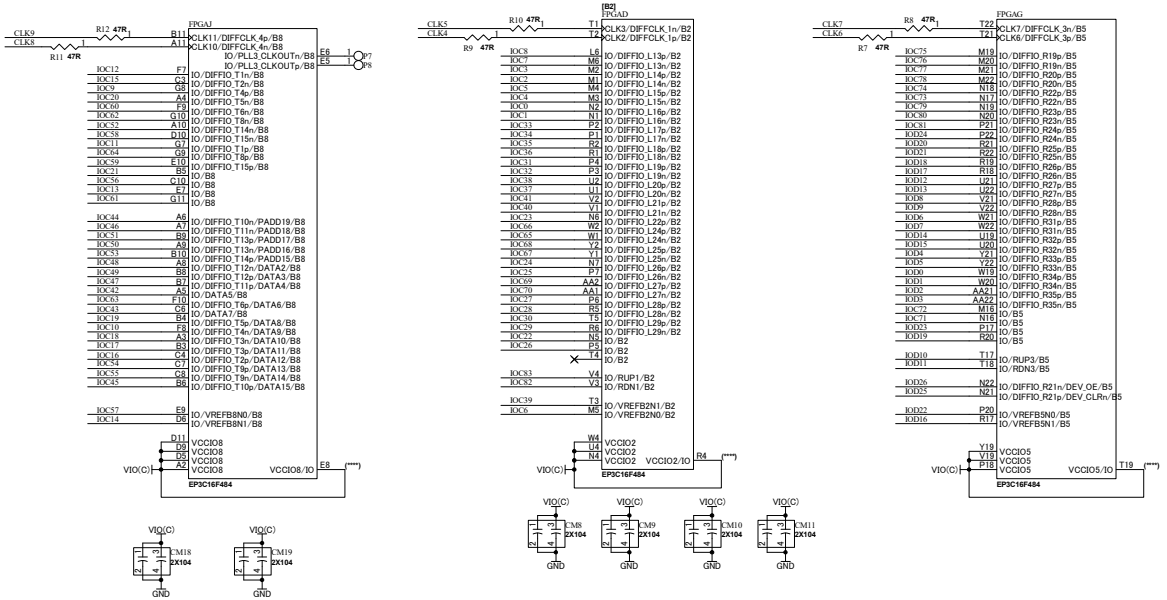
FILE: FPGA1.sch
DATE: 30-Jul-2020 16:33:39
Sheet: 2 / 3



BANK Group-B

BANK Group-C



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