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*PADS-PCB*
*NET*
*SIGNAL* CLK-A_1
 FPGA.27 R8.1 TP1.1
*SIGNAL* CLK-B_1
 FPGA.28 R9.1 TP2.1
*SIGNAL* CLK-C_1
 FPGA.23 R6.2 RM1.2
*SIGNAL* CLK-D_1
 FPGA.24 R5.2
 RM1.1
*SIGNAL* CLK-E_1
 FPGA.132 R4.2 RM2.1
*SIGNAL* CLK-F_1
 FPGA.131 R3.2 RM2.2
*SIGNAL* CLK-G_1
 FPGA.130 R1.2 RM2.3
*SIGNAL* CLK-H_1
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*SIGNAL* CLK-ORG_1
 R8.2 R9.2 U3.3
*SIGNAL* FMHOLD_1
 RM3.3 U5.7
*SIGNAL* FMWPO_1
 RM3.4 U5.3
*SIGNAL* GND
 C1.2 C2.2 C3.2 C4.2 C5.2
 C6.2 C7.2 C8.2 C9.2 CN2.2 CN2.10 CN3.2 CN3.10 CN4.2 CNA.5 CNA.6 CNA.15
 CNA.16 CNA.25 CNA.26 CNA.35 CNA.36 CNA.45 CNA.46 CNA.55 CNA.56 CNB.5 CNB.6
 CNB.15 CNB.16 CNB.25 CNB.26 CNB.35 CNB.36 CNB.45 CNB.46 CNB.55 CNB.56 D2.1
 EM1.2 EM2.2 FC1.2 FC2.2 FC3.2 FC4.2 FC5.2 FC6.2 FC7.2 FC8.2 FC9.2 FC10.2
 FC11.2 FC12.2 FC13.2 FC14.2 FC15.2 FC16.2 FC17.2 FC18.2 FC19.2 FC20.2
 FC21.2 FC22.2 FC23.2 FC24.2 FC25.2 FC26.2 FC27.2 FC28.2 FC29.2 FC30.2
 FC31.2 FPGA.9 FPGA.25 FPGA.36 FPGA.38 FPGA.49 FPGA.50 FPGA.52 FPGA.54
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 TPG2.1 U1.5 U1.6 U1.7 U1.8 U2.1 U3.2 U4.4 U5.4 U6.14
*SIGNAL* IOA0_1
 CNA.7 FPGA.3 R2.1
*SIGNAL* IOA1_1

 CNA.8 FPGA.4
*SIGNAL* IOA2_1
 CNA.9 FPGA.5
*SIGNAL* IOA3_1
 CNA.10 FPGA.6
*SIGNAL* IOA4_1
 CNA.11 FPGA.8
*SIGNAL* IOA5_1
 CNA.12 FPGA.10
*SIGNAL* IOA6_1

 CNA.13 FPGA.11
*SIGNAL* IOA7_1
 CNA.14 FPGA.12
*SIGNAL* IOA8_1
 CNA.17 FPGA.13
*SIGNAL* IOA9_1
 CNA.18 FPGA.14
*SIGNAL* IOA10_1
 CNA.19 FPGA.15
*SIGNAL* IOA11_1

 CNA.20 FPGA.30
*SIGNAL* IOA12_1
 CNA.21 FPGA.31
*SIGNAL* IOA13_1
 CNA.22 FPGA.33
*SIGNAL* IOA14_1
 CNA.23 FPGA.34
*SIGNAL* IOA15_1
 CNA.24 FPGA.35
*SIGNAL* IOA16_1

 CNA.27 FPGA.37
*SIGNAL* IOA17_1
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*SIGNAL* IOA18_1
 CNA.29 FPGA.40
*SIGNAL* IOA19_1
 CNA.30 FPGA.68
*SIGNAL* IOA20_1
 CNA.31 FPGA.69
*SIGNAL* IOA21_1

 CNA.32 FPGA.70
*SIGNAL* IOA22_1
 CNA.33 FPGA.72
*SIGNAL* IOA23_1
 CNA.34 FPGA.74
*SIGNAL* IOA24_1
 CNA.37 FPGA.75
*SIGNAL* IOA25_1
 CNA.38 FPGA.76
*SIGNAL* IOA26_1

 CNA.39 FPGA.77
*SIGNAL* IOA27_1
 CNA.40 FPGA.80
*SIGNAL* IOA28_1
 CNA.41 FPGA.81
*SIGNAL* IOA29_1
 CNA.42 FPGA.82
*SIGNAL* IOA30_1
 CNA.43 FPGA.84
*SIGNAL* IOA31_1

 CNA.44 FPGA.86
*SIGNAL* IOA32_1
 CNA.47 FPGA.87
*SIGNAL* IOA33_1
 CNA.48 FPGA.88
*SIGNAL* IOA34_1
 CNA.49 FPGA.89
*SIGNAL* IOA35_1
 CNA.50 FPGA.90
*SIGNAL* IOA36_1

 CNA.51 FPGA.92
*SIGNAL* IOA37_1
 CNA.52 FPGA.94
*SIGNAL* IOA38_1
 CNA.53 FPGA.95
*SIGNAL* IOA39_1
 CNA.54 FPGA.96
*SIGNAL* IOA40_1
 CNA.57 FPGA.97
*SIGNAL* IOA41_1

 CNA.58 FPGA.99
*SIGNAL* IOA42_1
 CNA.59 FPGA.101
*SIGNAL* IOA43_1
 CNA.60 FPGA.102
*SIGNAL* IOA44_1
 CNA.61 FPGA.103
*SIGNAL* IOA45_1
 CNA.62
 FPGA.105
*SIGNAL* IOA46_1
 CNA.63 FPGA.106
*SIGNAL* IOA47_1
 CNA.64 FPGA.107
*SIGNAL* IOA48_1
 CNA.65 FPGA.108 R6.1
*SIGNAL* IOA49_1
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*SIGNAL* IOB0_1
 CNB.7 FPGA.208 R1.1
*SIGNAL* IOB1_1
 CNB.8 FPGA.207
*SIGNAL* IOB2_1
 CNB.9 FPGA.206
*SIGNAL* IOB3_1
 CNB.10
 FPGA.205
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*SIGNAL* IOB4_1
 CNB.11 FPGA.203
*SIGNAL* IOB5_1
 CNB.12 FPGA.201
*SIGNAL* IOB6_1
 CNB.13 FPGA.200
*SIGNAL* IOB7_1
 CNB.14 FPGA.199
*SIGNAL* IOB8_1

 CNB.17 FPGA.198
*SIGNAL* IOB9_1
 CNB.18 FPGA.197
*SIGNAL* IOB10_1
 CNB.19 FPGA.195
*SIGNAL* IOB11_1
 CNB.20 FPGA.193
*SIGNAL* IOB12_1
 CNB.21
 FPGA.192
*SIGNAL* IOB13_1
 CNB.22 FPGA.191
*SIGNAL* IOB14_1
 CNB.23 FPGA.189
*SIGNAL* IOB15_1
 CNB.24 FPGA.188
*SIGNAL* IOB16_1
 CNB.27 FPGA.187
*SIGNAL* IOB17_1

 CNB.28 FPGA.185
*SIGNAL* IOB18_1
 CNB.29 FPGA.182
*SIGNAL* IOB19_1
 CNB.30 FPGA.181
*SIGNAL* IOB20_1
 CNB.31 FPGA.180
*SIGNAL* IOB21_1
 CNB.32
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*SIGNAL* IOB22_1
 CNB.33 FPGA.176
*SIGNAL* IOB23_1
 CNB.34 FPGA.175
*SIGNAL* IOB24_1
 CNB.37 FPGA.173
*SIGNAL* IOB25_1
 CNB.38 FPGA.171
*SIGNAL* IOB26_1

 CNB.39 FPGA.170
*SIGNAL* IOB27_1
 CNB.40 FPGA.169
*SIGNAL* IOB28_1
 CNB.41 FPGA.168
*SIGNAL* IOB29_1
 CNB.42 FPGA.165
*SIGNAL* IOB30_1
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*SIGNAL* IOB31_1
 CNB.44 FPGA.163
*SIGNAL* IOB32_1
 CNB.47 FPGA.162
*SIGNAL* IOB33_1
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*SIGNAL* IOB34_1
 CNB.49 FPGA.160
*SIGNAL* IOB35_1

 CNB.50 FPGA.152
*SIGNAL* IOB36_1
 CNB.51 FPGA.151
*SIGNAL* IOB37_1
 CNB.52 FPGA.150
*SIGNAL* IOB38_1
 CNB.53 FPGA.149
*SIGNAL* IOB39_1
 CNB.54
 FPGA.147
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*SIGNAL* IOB40_1
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*SIGNAL* IOB41_1
 CNB. 58 FPGA. 145
*SIGNAL* IOB42_1
 CNB. 59 FPGA. 144
*SIGNAL* IOB43_1
 CNB. 60 FPGA. 143
*SIGNAL* MSEL0_1

 FPGA. 126 JP3. 1 RM4. 1
*SIGNAL* MSEL1_1
 FPGA. 125 JP3. 3 RM4. 2
*SIGNAL* NetLED1_2
 LED1. 2 R7. 1
*SIGNAL* NetLED2_1
 LED2. 1 RM3. 7
*SIGNAL* NetR12_1
 LED4. 1
 R12. 1
*SIGNAL* NetU2_2
 D1. 2 U2. 2
*SIGNAL* RAMA0_1
 FPGA. 56 U6. 21
*SIGNAL* RAMA1_1
 FPGA. 141 U6. 23
*SIGNAL* RAMA2_1
 FPGA. 139 U6. 24
*SIGNAL* RAMA3_1
 FPGA. 138 U6. 25
*SIGNAL* RAMA4_1

 FPGA. 137 U6. 26
*SIGNAL* RAMA5_1
 FPGA. 134 U6. 1
*SIGNAL* RAMA6_1
 FPGA. 133 U6. 2
*SIGNAL* RAMA7_1
 FPGA. 128 U6. 3
*SIGNAL* RAMA8_1
 FPGA. 127 U6. 4
*SIGNAL* RAMA9_1

 CNB. 61 FPGA. 118 U6. 5
*SIGNAL* RAMA10_1
 CNB. 62 FPGA. 117 U6. 6
*SIGNAL* RAMA11_1
 CNB. 63 FPGA. 113 U6. 7
*SIGNAL* RAMA12_1
 CNB. 64
 FPGA. 112 U6. 8
*SIGNAL* RAMA13_1
 CNB. 65 FPGA. 114 R4. 1 U6. 9
*SIGNAL* RAMA14_1
 CNB. 66 FPGA. 115 R3. 1 U6. 10
*SIGNAL* RAMCEN_1
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*SIGNAL* RAMD0_1
 FPGA. 116 U6. 11
*SIGNAL* RAMD1_1
 FPGA. 67 U6. 12
*SIGNAL* RAMD2_1
 FPGA. 64 U6. 13
*SIGNAL* RAMD3_1
 FPGA. 63 U6. 15
*SIGNAL* RAMD4_1

 FPGA. 61 U6. 16
*SIGNAL* RAMD5_1
 FPGA. 60 U6. 17
*SIGNAL* RAMD6_1
 FPGA. 59 U6. 18
*SIGNAL* RAMD7_1
 FPGA. 58 U6. 19
*SIGNAL* RAM0EN_1
 FPGA. 142 U6. 22
*SIGNAL* RAMWEN_1

 FPGA. 135 U6. 27
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*SIGNAL* RXD_1
 CN4.3 FPGA.43
*SIGNAL* SPI_CK_1
 FPGA.44 RM3.2 U5.6
*SIGNAL* SPI_CS_1
 FPGA.46 RM3.5 U5.1
*SIGNAL* SPI_SI_1
 FPGA.45
 RM3.1 U5.5
*SIGNAL* SPI_SO_1
 FPGA.47 U5.2
*SIGNAL* SWO_1
 FPGA.104 RM3.8 SW1.1
*SIGNAL* TXD_1
 CN4.1 FPGA.41
*SIGNAL* ULEDO_1
 FPGA.48 LED2.2
*SIGNAL* V12

 C2.1 C3.1 FC1.1 FC2.1 FC3.1 FC4.1 FC5.1 FC6.1 FPGA.32 FPGA.66 FPGA.79
 FPGA.120 FPGA.178 FPGA.190 JP1.1 U1.3 U1.4
*SIGNAL* V12A
 EM1.1 EM2.1 JP1.2
*SIGNAL* V33A
 C1.1 C4.1
 C6.1 CN2.4 CN3.4 CNA.1 CNA.2 D2.2 FC10.1 FC13.1 FC14.1 FC15.1 FC16.1
 FC17.1 FC18.1 FC19.1 FC20.1 FC21.1 FC22.1 FC23.1 FC24.1 FC25.1 FC26.1
 FC28.1 FC30.1 FC31.1 FPGA.7 FPGA.29 FPGA.42 FPGA.62 FPGA.71 FPGA.83
 FPGA.91 FPGA.98 FPGA.109 FPGA.122 FPGA.136 FPGA.148 JP2.1 LED1.1 RM3.9
 RM3.10 RM3.11 RM3.12 RM3.13 RM3.14 RM3.15 RM3.16 RM4.9 RM4.10 RM4.11
 RM4.12 RM4.13 RM4.14 RM4.15 RM4.16 RM5.9 RM5.10 RM5.11 RM5.12 RM5.13 U1.1
 U1.2 U2.3 U3.1 U3.4 U4.3 U4.7 U4.8 U5.8 U6.28
*SIGNAL* V33B
 C5.1 C7.1 CNB.1 CNB.2 FC7.1
 FC8.1 FC9.1 FC27.1 FC29.1 FPGA.166 FPGA.172 FPGA.183 FPGA.194 FPGA.202
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*SIGNAL* VAA
 C9.1 EM2.3 FC12.1 FPGA.155 FPGA.157
*SIGNAL* VAB
 C8.1 EM1.3 FC11.1 FPGA.51
 FPGA.53
*SIGNAL* XCONFDONE_1
 CN2.3 FPGA.123 R12.2 RM4.8
*SIGNAL* XDATAO_1
 CN2.7 FPGA.20 RM5.6 U4.2
*SIGNAL* XDCLK_1
 CN2.1 FPGA.21
 RM5.5 U4.6
*SIGNAL* XNCONFIG_1
 CN2.5 D1.1 FPGA.26 RM4.7
*SIGNAL* XNSTATUS_1
 FPGA.121 RM4.3
*SIGNAL* XTCK_1
 CN3.1 FPGA.18 RM5.15
 RM5.16
*SIGNAL* XTDI_1
 CN3.9 FPGA.19 RM4.4
*SIGNAL* XTDO_1
 CN3.3 FPGA.16 RM4.6
*SIGNAL* XTMS_1
 CN3.5 FPGA.17 RM4.5
*SIGNAL* X_ASDO_1
 CN2.9
 FPGA.1 RM5.8 U4.5
*SIGNAL* X_CYC_CE_1
 CN2.6 FPGA.22 R10.2
*SIGNAL* X_CYC_NCSO_1
 FPGA.2 R11.2
*SIGNAL* X_ROMNCS_1
 CN2.8 R11.1 RM5.7 U4.1
*END*
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